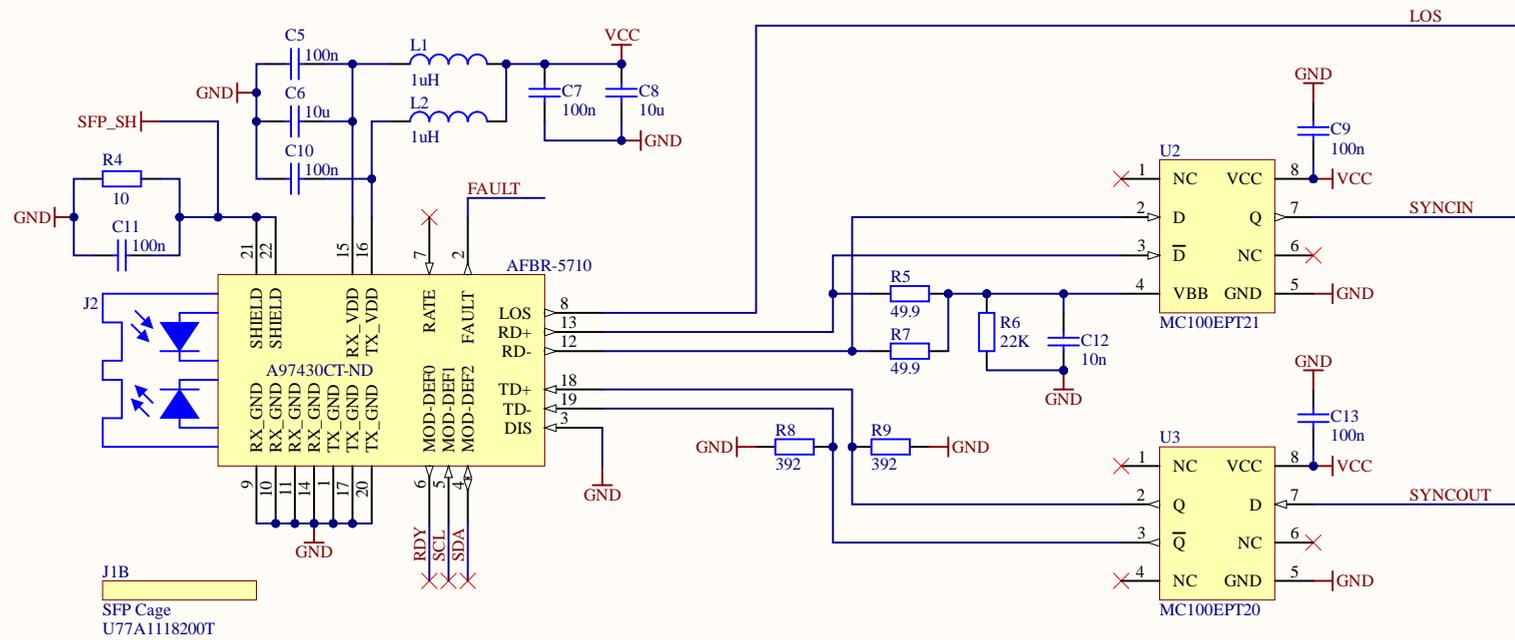
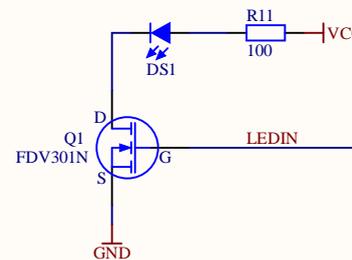
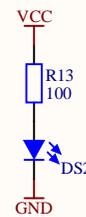
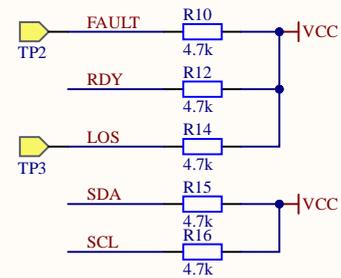


SFP Port

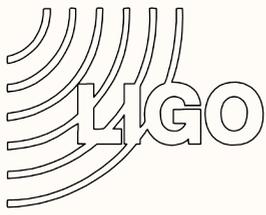


J1B
SFP Cage
U77A1118200T

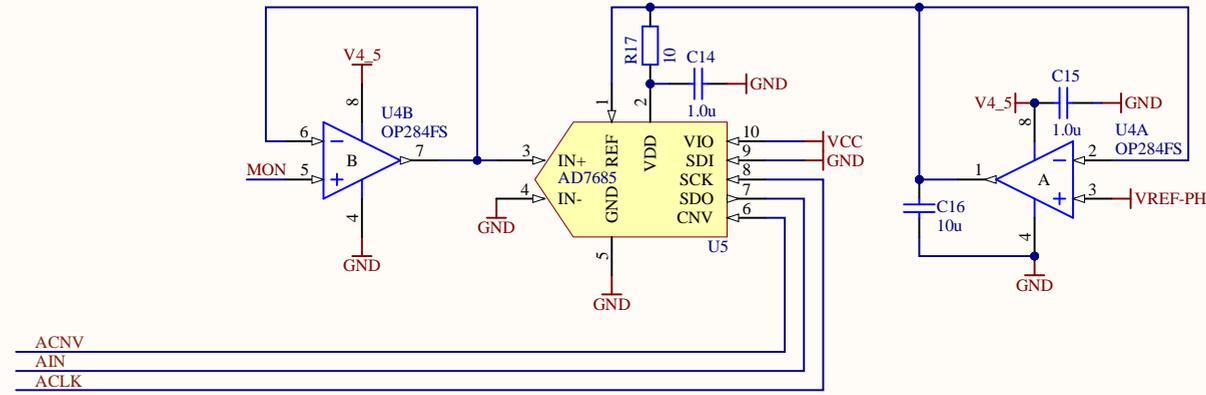


Legend
 VCC = 3.3V (digital voltage)
 V5 = 5V (digital voltage)
 V4_5 = 4.5V (analog)

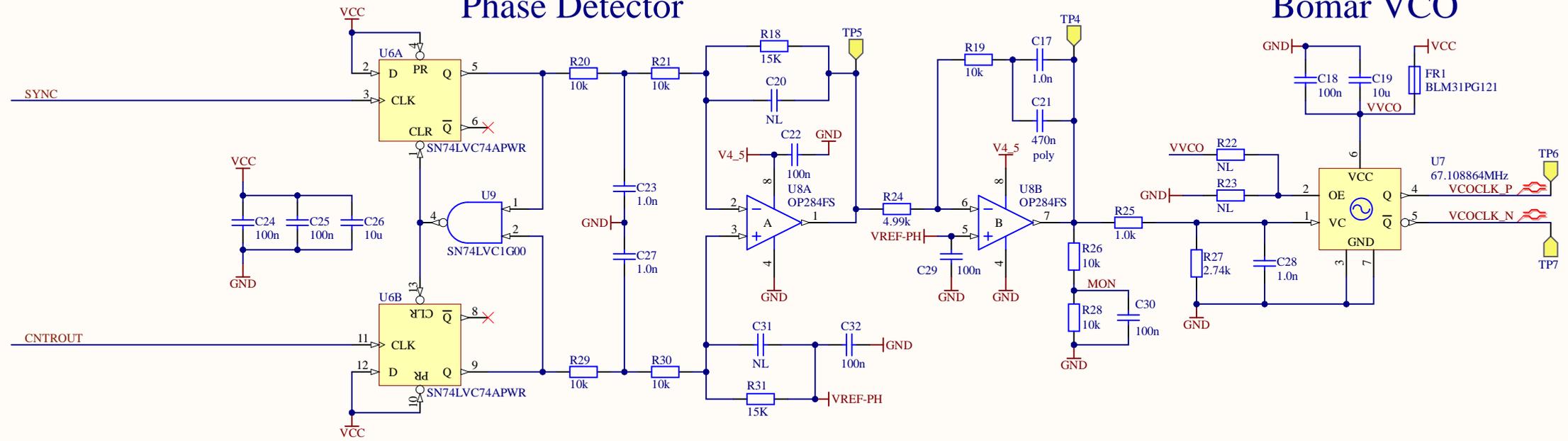
Project Timing Clock			LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology National Science Foundation	
Sheet Title SFP				
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File: ClockSingleSFP.SchDoc				



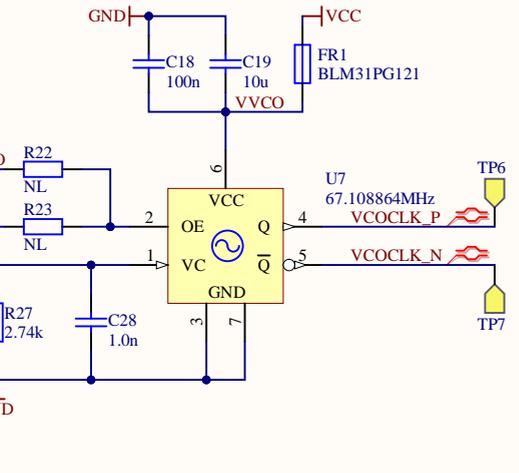
Control Voltage Monitor



Phase Detector



Bomar VCO



2.5V Reference

