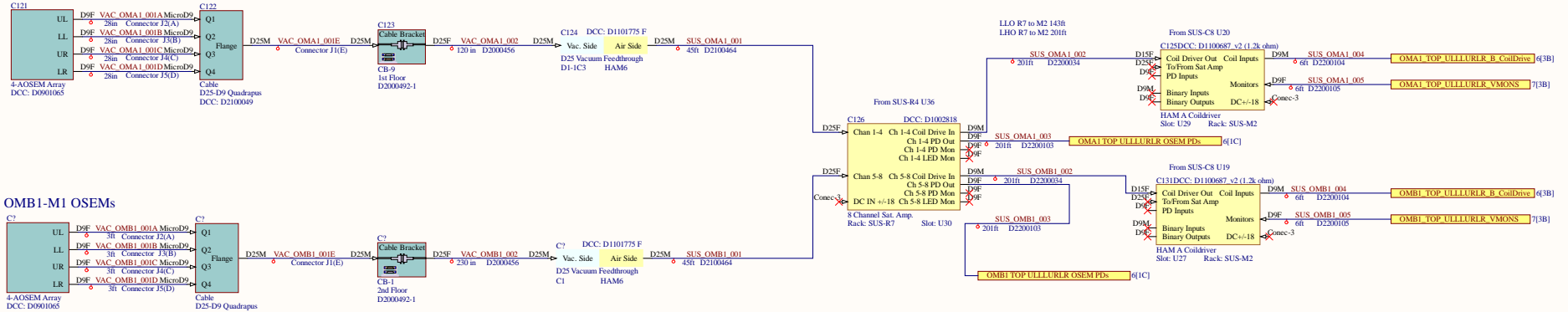
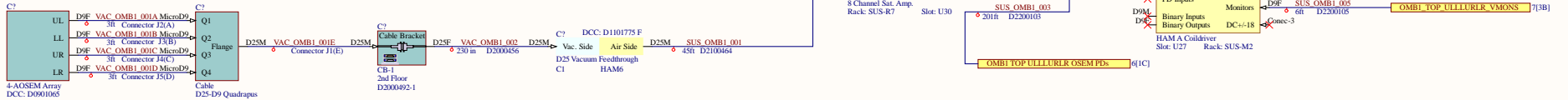


DITHERS

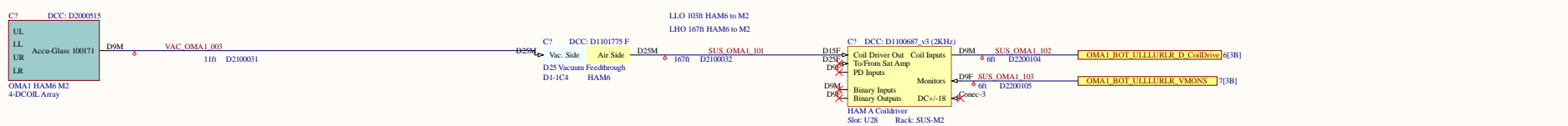
OMA1-M1 OSEMs



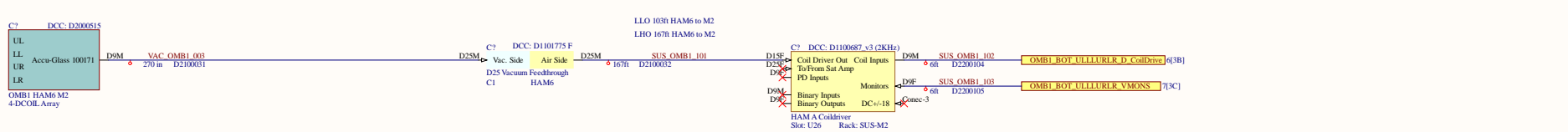
OMB1-M1 OSEMs



OMA1-M2 DITHER



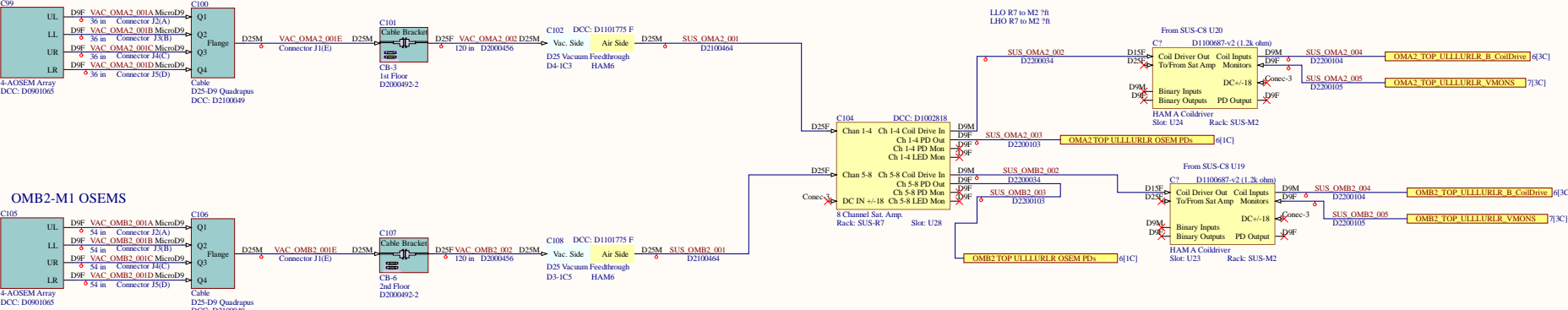
OMB1-M2 DITHER



PSAMS

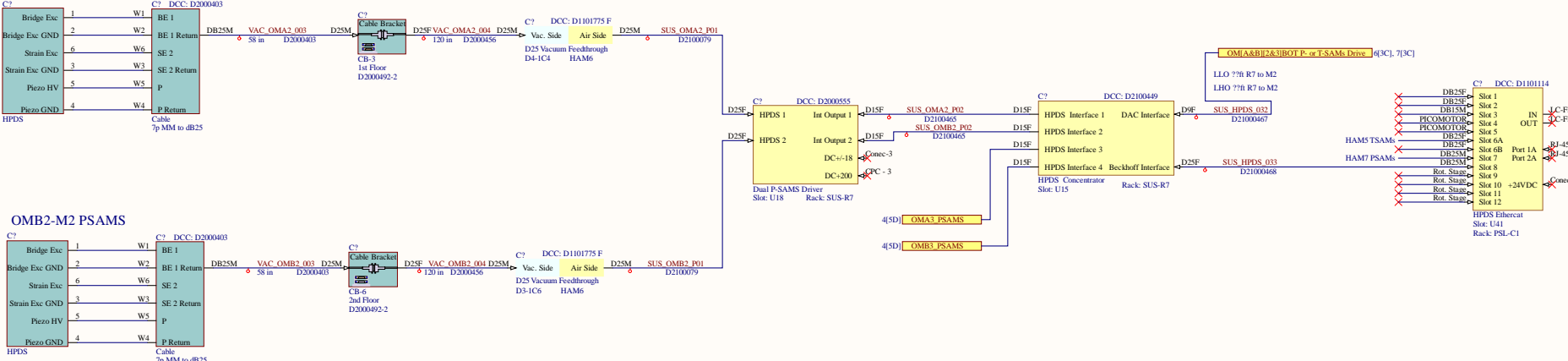
OMA2-M1 OSEMS

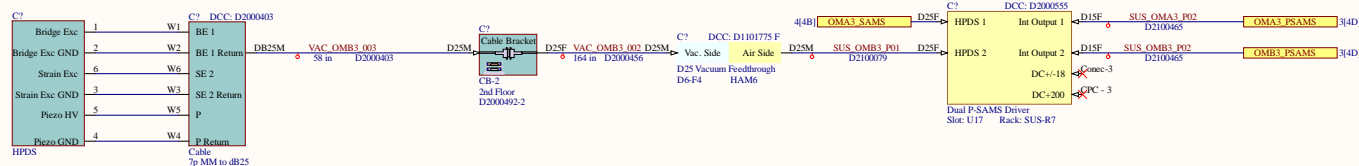
Re-use in-vacuum cables from OM2

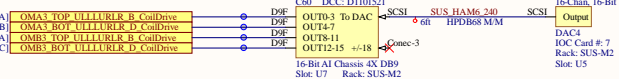
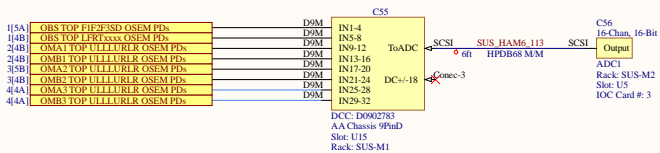


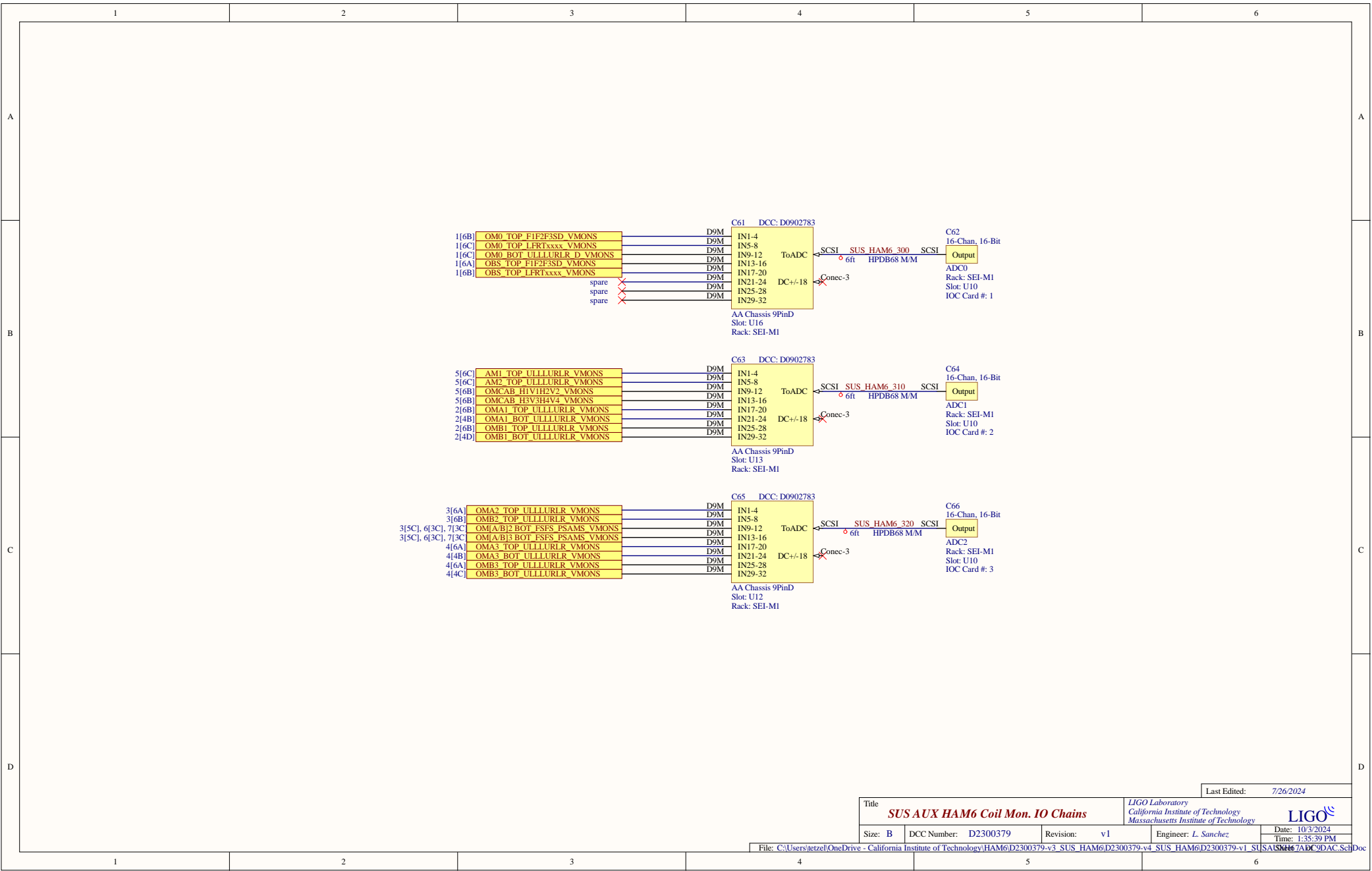
OMA2-M2 PSAMS

Re-use in-vacuum cables from OM2









SCSI

SUS HAM6 310

HPDB68 M/M

6ft

C64 16-Chan, 16-Bit

Output

ADC1

Rack: SEI-M1

Slot: U10

IOC Card #: 2

3[6A] OMA2 TOP ULLUURLR VMONS

3[6B] OMB2 TOP ULLUURLR VMONS

3[5C], 6[3C], 7[3C] OM[A/B]2 BOT FSFS PSAMS VMONS

3[5C], 6[3C], 7[3C] OM[A/B]3 BOT FSFS PSAMS VMONS

4[6A] OMA3 TOP ULLUURLR VMONS

4[4B] OMA3 BOT ULLUURLR VMONS

4[6A] OMB3 TOP ULLUURLR VMONS

4[4C] OMB3 BOT ULLUURLR VMONS

D9M

D9M

D9M

D9M

D9M

D9M

D9M

C65 DCC: D0902783

IN1-4

IN5-8

IN9-12

IN13-16

IN17-20

IN21-24

IN25-28

IN29-32

ToADC

DC+/-18

AA Chassis 9PinD

Slot: U12

Rack: SEI-M1

SCSI

SUS HAM6 320

HPDB68 M/M

6ft

C66 16-Chan, 16-Bit

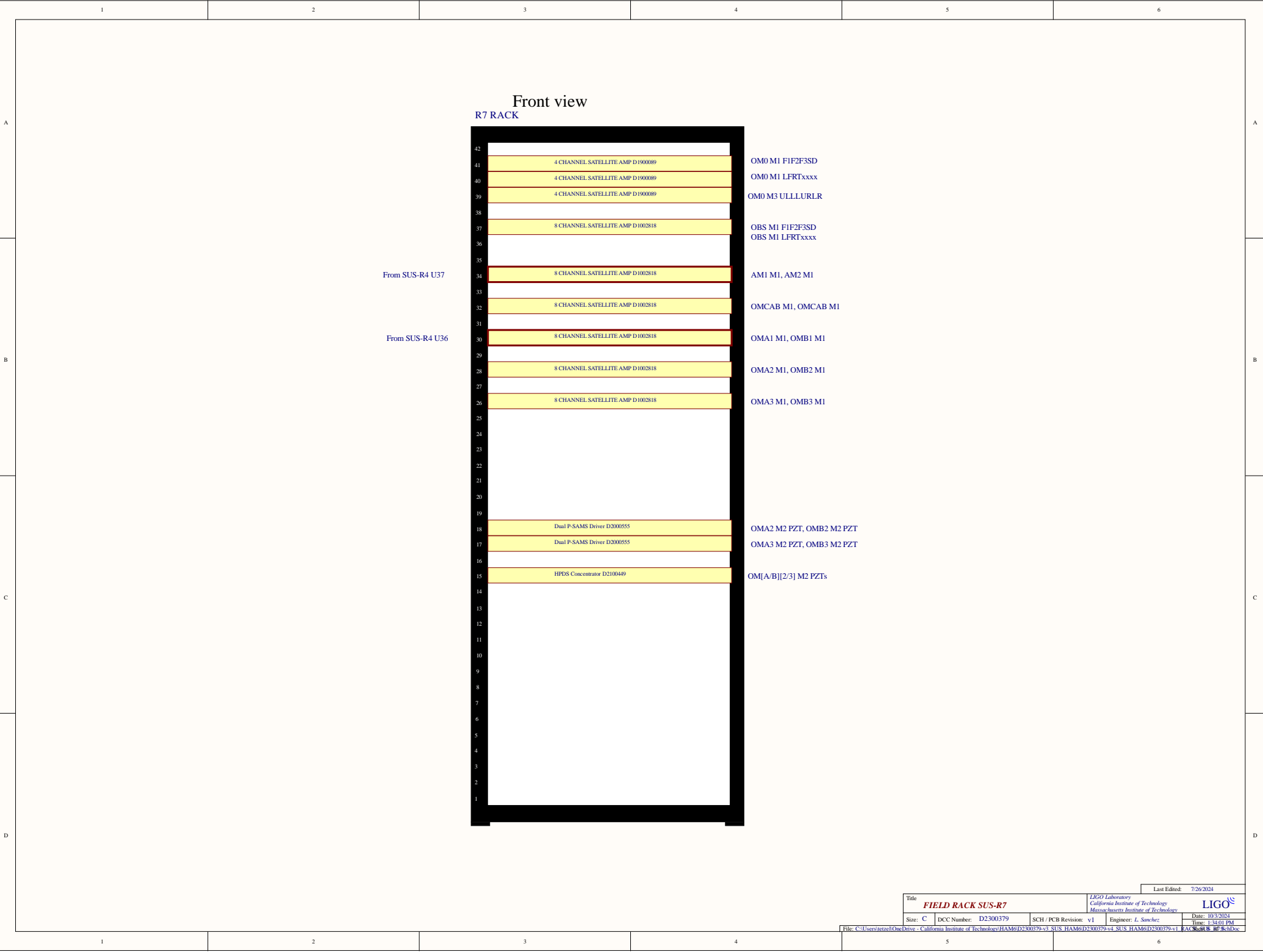
Output

ADC2

Rack: SEI-M1

Slot: U10

IOC Card #: 3



See G2301306 part III page 10 No change on SUS M1 is Full.

Front view

SEI-M1 RACK



Rack SUS-C3A

Front view

SUS-M2 RACK



Rack SUS-C4A

OM0 M1, OM0 BOT, OBS M1 CD Mons

OMCAB, OMCAB, AM1, AM2, OMA1 M1, OMA1 M2, OMB1 M1, OMB1 M2

OMA2 M1, OMB2 M1, OM[A/B][2/3], OM0 M3, OMA3 M1, OMA3 M2, OMB3 M1, OMB3 M2

From SUS-C8 23

From SUS-C8 22

From SUS-C8 20

From SUS-C8 19

From SUS-C8 20

OM0 M1 FIF2F3SD

OM0 M1 LFRTxxxx

OM0 M3 ULLLURLR

OBS M1 FIF2F3SD

OBS M1 LFRTxxxx

AM1 ULLLURLR

AM2 ULLLURLR

OMCAB H1V1H2V2

OMCAB H3V3H4V4

OMA1 M1 ULLLURLR

OMA1 M2 ULLLURLR

OMB1 M1 ULLLURLR

OMB1 M2 ULLLURLR

OMA2 M1 ULLLURLR

OMB2 M1 ULLLURLR

OMA3 M1 ULLLURLR

OMA3 M2 ULLLURLR

OMB3 M1 ULLLURLR

OMB3 M2 ULLLURLR

OMCAB, OMCAB, AM1, AM2, OM0 M1, OM0 M3, ADC/DAC Duotone
[OBS, OMA1, OMB1, OMA2, OMB2, OMA3, OMB3] [M1]

OM0 M1, OM0 M1, OBS M1, OBS M1

OMCAB, OMCAB, AM1, AM2

OMA1 M1, OMA1 M2, OMB1 M1, OMB1 M2

OMA2 M1, OMB2 M1, OM[A/B][2/3]M2, OM0 M2

OMA3 M1, OMA3 M2, OMB3 M1, OMB3 M2

Last Edited: 7/26/2024