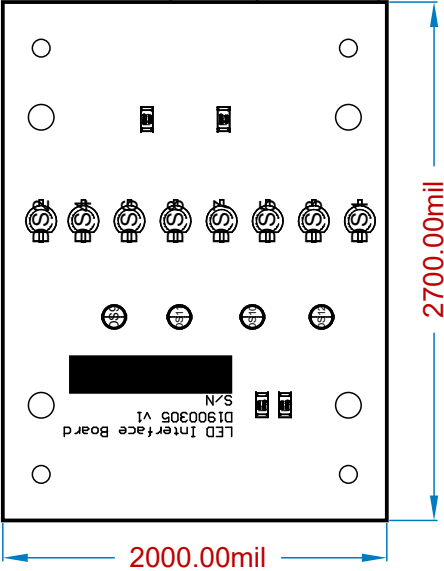
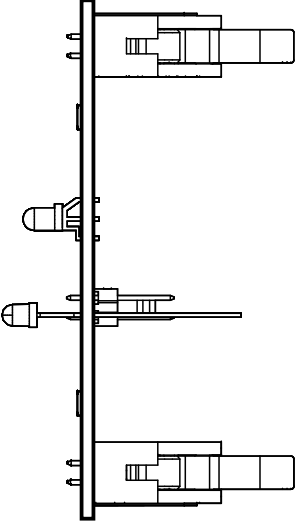


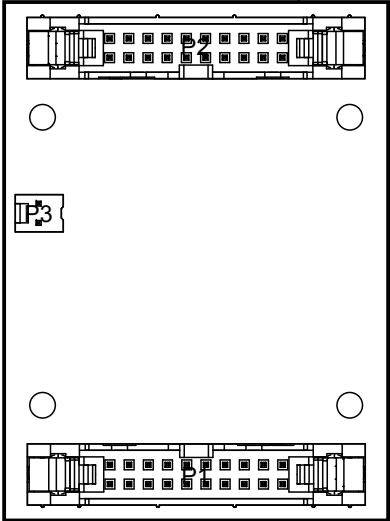
View from Top side (Scale 1:1)



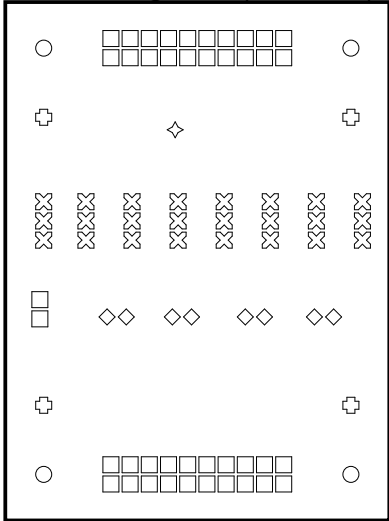
View from Front side (Scale 1:1)



View from Bottom side (Scale 1:1)



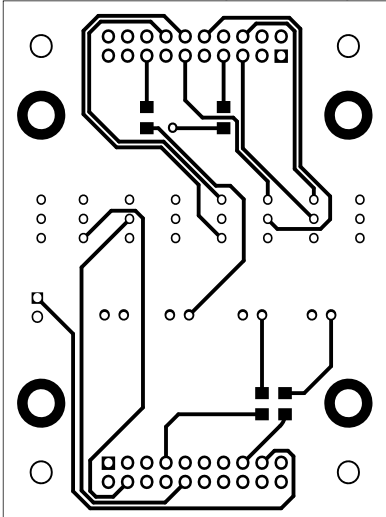
Drill Drawing View (Scale 1:1)



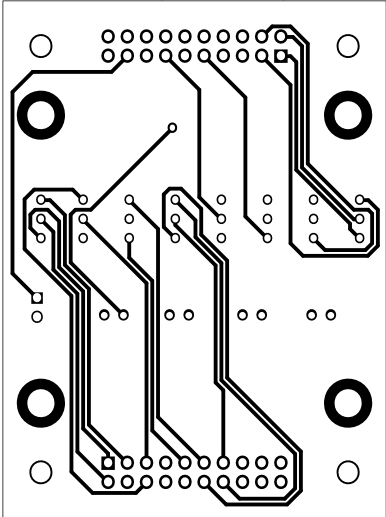
Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
◇	1	28.00mil	Plated	None
⊗	24	33.00mil	Plated	None
◇	8	35.43mil	Plated	None
□	42	43.00mil	Plated	None
○	4	100.00mil	Plated	None
⊕	4	141.00mil	Plated	None
83 Total				

Component Side (Scale 1:1)



Solder Side (Scale 1:1)



Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
Surface Material	Top Paste			Paste Mask	GTP
	Top Overlay			Legend	GTO
Copper	Top Solder	0.40mil	Solder Resist	Solder Mask	GTS
Prepreg	Component Side	1.40mil		Signal	GTL
Copper		14.10mil	FR-4	Dielectric	
Copper	GND	1.40mil		Internal Plane	GP1
Core		28.00mil		Dielectric	
Copper	GND	1.40mil		Internal Plane	GP2
Prepreg		14.10mil		Dielectric	
Copper	Solder Side	1.40mil		Signal	GBL
Surface Material	Bottom Solder	0.40mil	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
	Bottom Paste			Paste Mask	GBP
Total thickness: 62.60mil					

Bill Of Materials

Line #	Designator	Comment	Quantity
1	DS1, DS2, DS3, DS4, DS5, DS6, DS7, DS8	DUAL_LED+	8
2	DS9, DS10, DS11, DS12	LED0	4
3	P1, P2	Header 20	2
4	P3	Molex 2 PIN	1
5	R1, R2, R3, R4	1K	4

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PROPRIETARY AND CONFIDENTIAL

		UNLESS OTHERWISE SPECIFIED:		NAME	DATE	LIGO			
		DIMENSIONS ARE IN INCHES	DRAWN	Dean Schaezel	8/14/2019	TITLE A+ ISI Coil Driver LED Interface Board			
		TOLERANCES:	CHECKED						
		FRACTIONAL±	ENG APPR.						
		ANGULAR: MACH± BEND ±	MFG APPR.						
		TWO PLACE DECIMAL ±	Q.A.						
		THREE PLACE DECIMAL ±	COMMENTS:						
		INTERPRET GEOMETRIC TOLERANCING PER:							
		MATERIAL							
NEXT ASSY	USED ON	FINISH				SIZE	DWG. NO. D1900305v1		
APPLICATION		DO NOT SCALE DRAWING				SCALE:	1:1	WEIGHT:	SHEET 1 OF 1