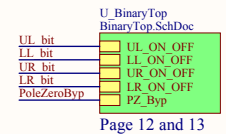
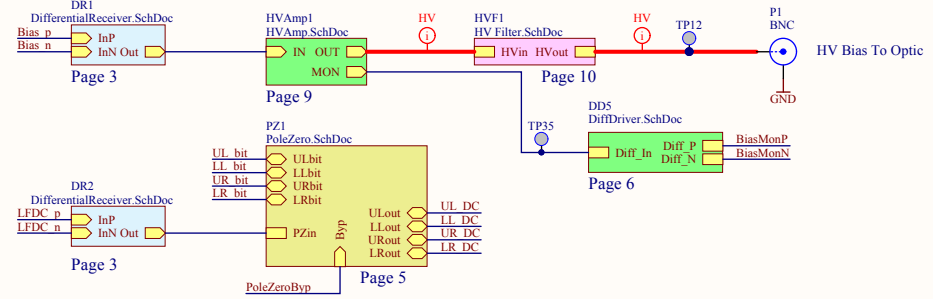
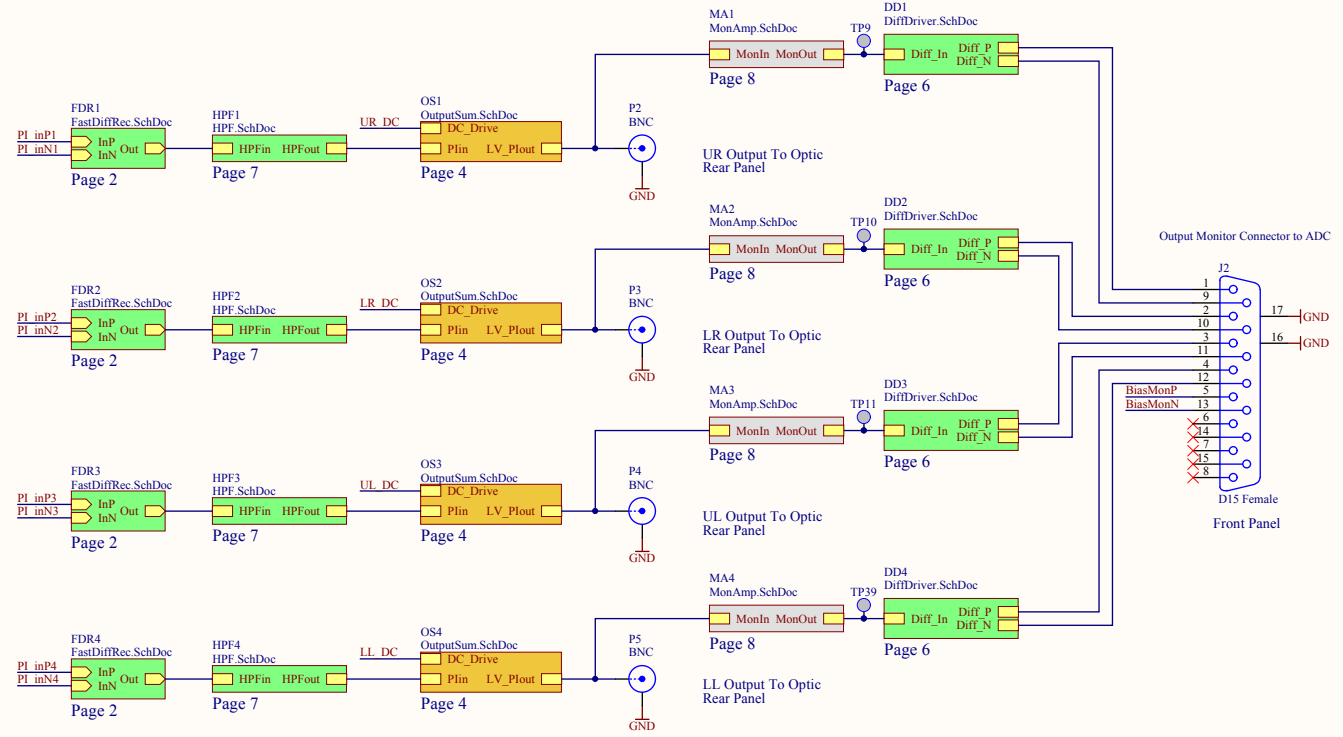
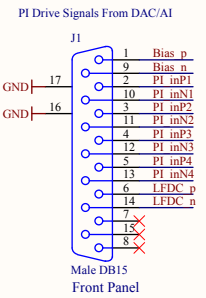


+230mA, -210mA Measured Quiescent +/- 18V Current Draw
 +20mA, -20mA Measured Quiescent +/- 24V Current Draw



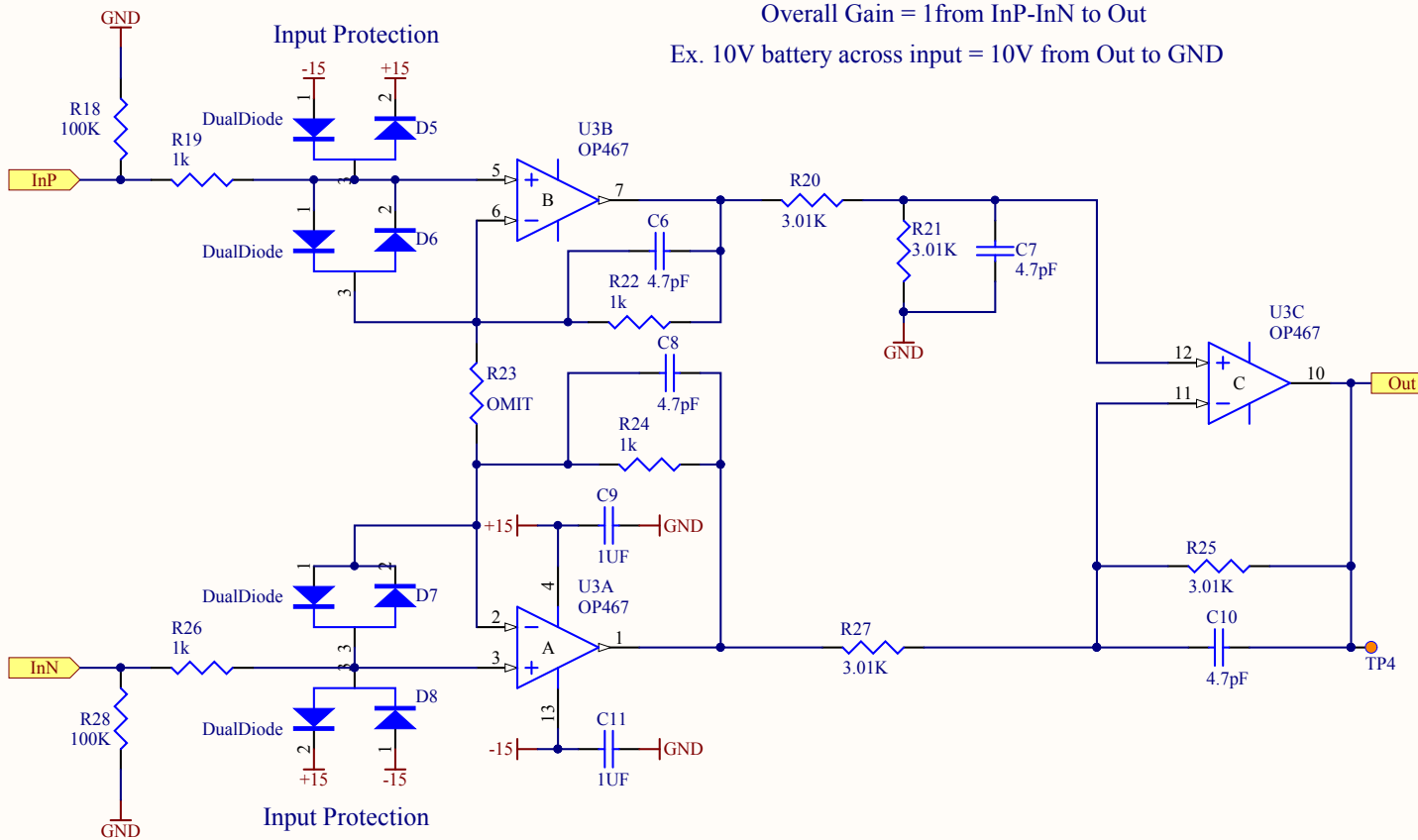
Version History:

v1 - Initial release
 v2 -
 1. Found a miswiring between the Pole-zero DC outputs and the individual quadrants. Corrected by jumpers on board. Schematic is now correct, but future revisions of the PCB must be changed.
 2. Found the relays on the Pole-zero stage are miswired. Corrected with jumpers on the board. Schematic now correct, but PCB must be updated for future revisions.
 3. Corrected typos on page numbers and sheet titles
 4. The miswiring on the LF DC relays produces an unfortunate feedback logic situation. The normal state of the relay is reported as 0V which might lead to confusion if the cable were not connected.
 5. Saw a slight microphonic tendency in the HV circuitry while at 400V out. Cause as yet unknown.
 v3 -
 1. Changed C27 from 4.7pF to 33pF in response to a 230kHz oscillation found on the HV bias amplifier
 2. It is possible that some of the microphonic tendency was due to the oscillation seen in the HV bias amplifier

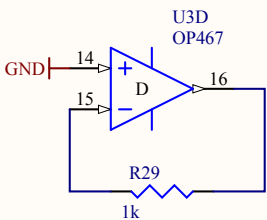


- Part1 PA95 Thermal Washer 598-1382-ND (10 per pack)
- Part2 Panel Mount SHV Cable Assembly (SHV Jack to BNC) Manufacturers Part Number: SHVJBH-RG58-BNCM-11i Quantity: 5 Manufacturer: Field Components
- Part7 0.5 inch 4-40 Standoff
- Part8 0.5 inch 4-40 Standoff
- Part9 0.5 inch 4-40 Standoff
- Part10 0.5 inch 4-40 Standoff
- Part11 0.5 inch 4-40 Standoff
- Part12 0.5 inch 4-40 Standoff
- Part13 0.5 inch 4-40 Standoff
- Part14 0.5 inch 4-40 Standoff
- Part15 0.5 inch 4-40 Standoff

Title Top Level ITM Low Voltage ESD Driver		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		Last Edited: 6/13/2016	
Size: B	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	Date: 6/13/2016	Time: 3:52:46 PM
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\ITM LVLN Driver\topSheet.SchDoc				Sheet 1 of 13	



Overall Gain = 1 from InP-InN to Out
 Ex. 10V battery across input = 10V from Out to GND



Checked All

Last Edited: 6/13/2016

Title Fast Differential Receiver		
Size: A	DCC Number: D1600122	Revision: V3

LIGO Laboratory
 California Institute of Technology
 Massachusetts Institute of Technology

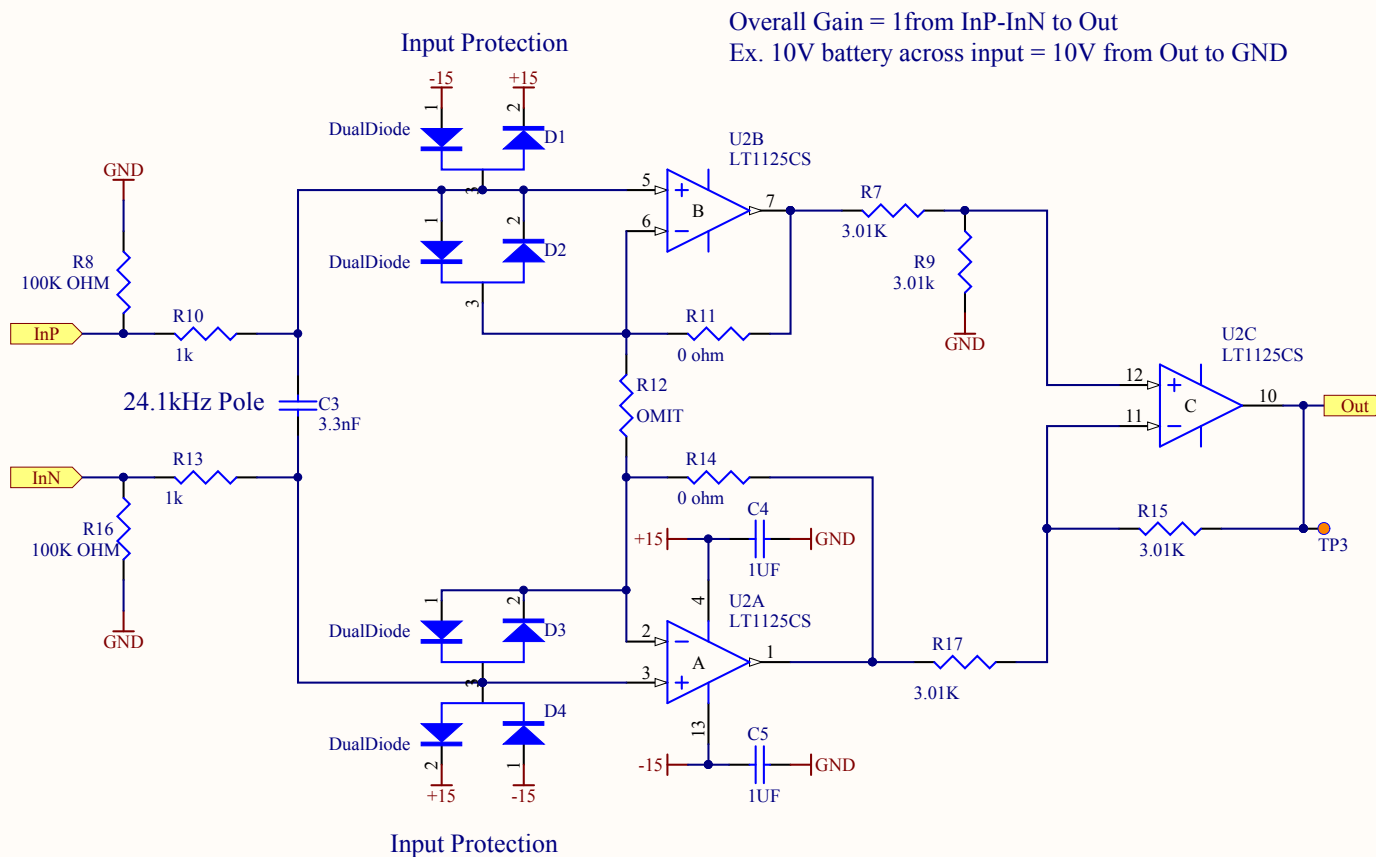


Engineer: R. Abbott

Date: 6/13/2016

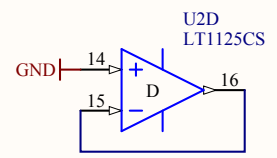
Time: 3:52:46 PM

Sheet 2 of 13



Overall Gain = 1 from InP-InN to Out
 Ex. 10V battery across input = 10V from Out to GND

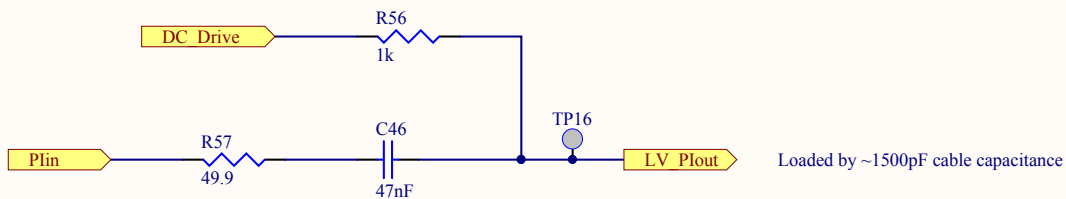
The 24.1kHz RC filter is there to cut high frequency noise to prevent slew rate limiting. Overall gain is 1 such that 10 volts peak from DAC yields 10v wrt ground at output



Checked All

Last Edited: 6/13/2016

Title Differential Receiver		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	Date: 6/13/2016	Time: 3:52:46 PM
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\DifferentialReceiver.SchDoc				Sheet 3 of 13	



This summing node combines the low frequency DC coupled signals present in the normal feedback path to each quadrant with the parametric instability correction signal. The summing was done passively to allow greater dynamic range than that afforded by an active summing stage.

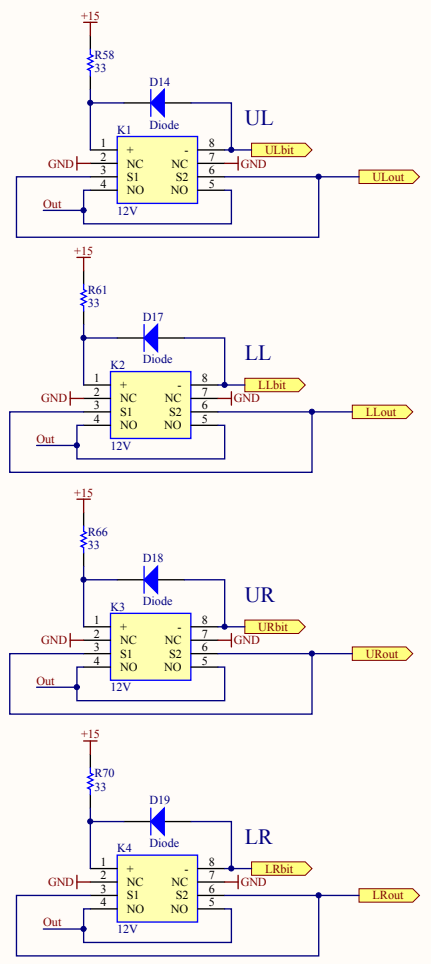
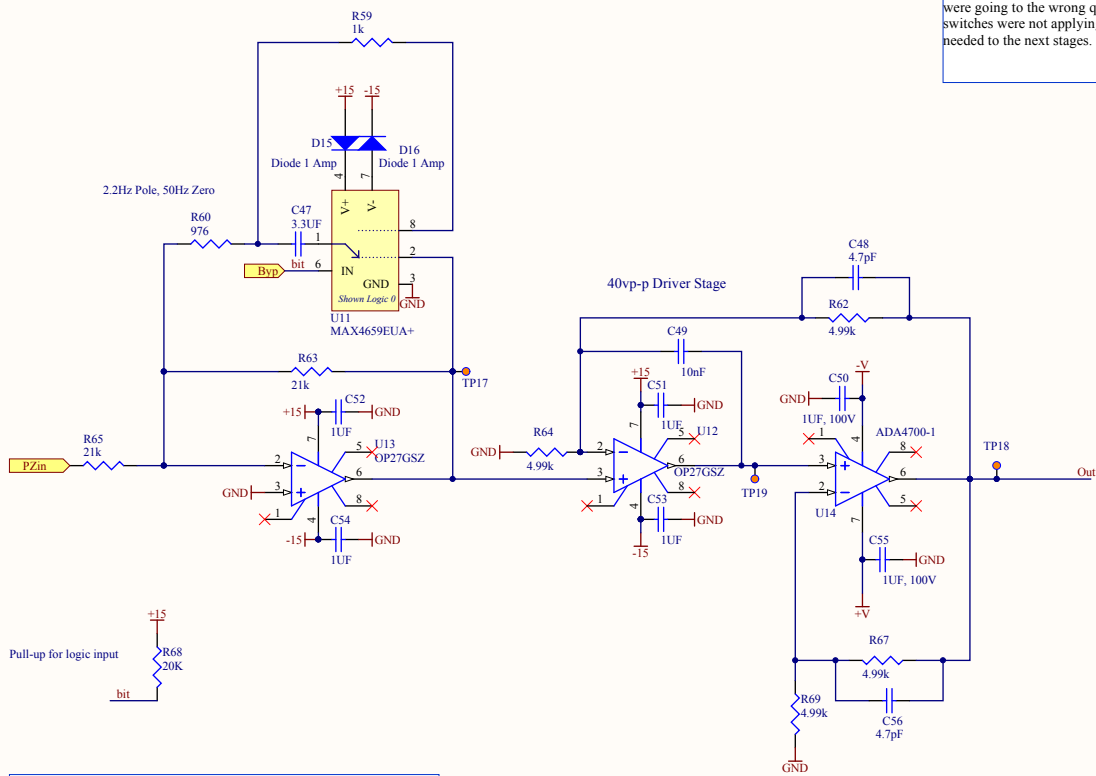
Checked All

Last Edited: 6/13/2016

Title		<i>LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology</i>		
Output Summing Node				
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	Date: 6/13/2016
				Time: 3:52:46 PM
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\OutputSum.SchDoc				Sheet 4 of 13

The +/-V input here is to drive the ADA4700 output driver chip. This voltage form can go up to +/-48V. At time of writing, we intend to use +/-24VDC supplies and see how we do.

All the nasty jumper wires are due to reconfiguring K1 through K4 to be according to this schematic. Prior to the jumpers, the outputs were going to the wrong quadrants, and the switches were not applying ground when needed to the next stages.



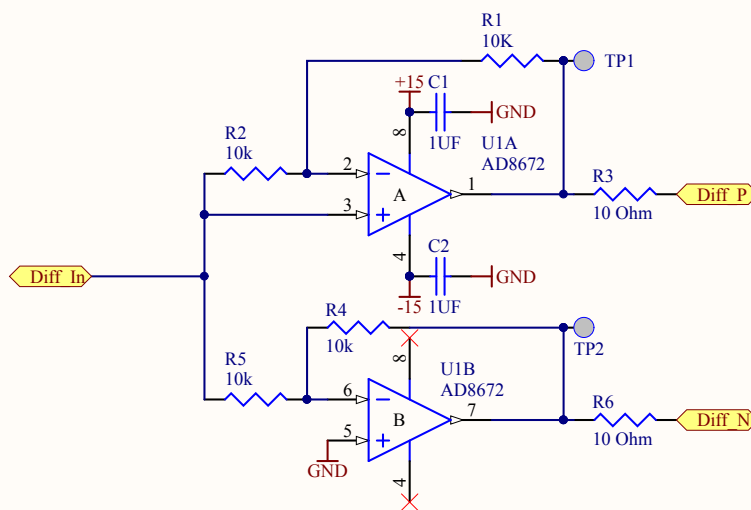
The design of this pole zero stage is motivated by the need to reduce the DAC noise at frequencies greater than 20Hz to a value less than 800nV/rHz for the ITMs. The DAC noise is estimated (per G1401399-v2) to be 800nV/rHz at 20 Hz with no filtration, so a single stage of dewhitening was used. The choice of pole and zero frequency above results in a predicted circuit output noise of 200nV/rHz at 20Hz in the presence of the anticipated DAC noise. The zero preserves some drive dynamic range at intermediate frequencies.

The ADA4700-1 stage provides the capability to increase the dynamic range to 40vp-p

Checked All

Last Edited: 6/13/2016


Title Pole-Zero and Driver		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: B	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	Date: 6/13/2016	Time: 3:52:47 PM
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\PoleZero.SchDoc					Sheet 5 of 13

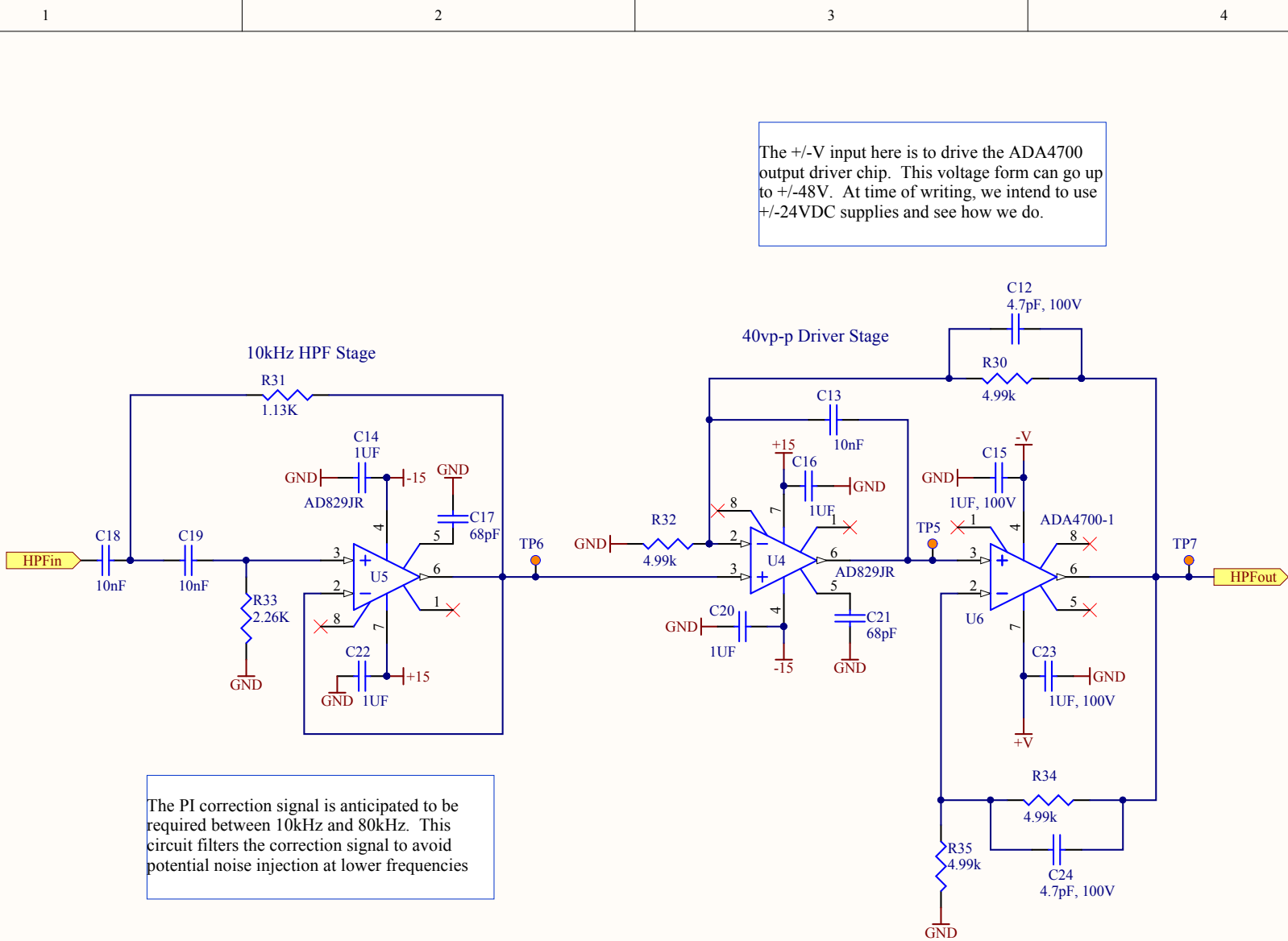


Typical LIGO differential driver circuit for the monitor signals.

Checked All

Last Edited: 6/13/2016

Title Differential Driver		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\DiffDriver.SchDoc			Date: 6/13/2016	Time: 3:52:47 PM
			Sheet 6 of 13	



The +/-V input here is to drive the ADA4700 output driver chip. This voltage form can go up to +/-48V. At time of writing, we intend to use +/-24VDC supplies and see how we do.

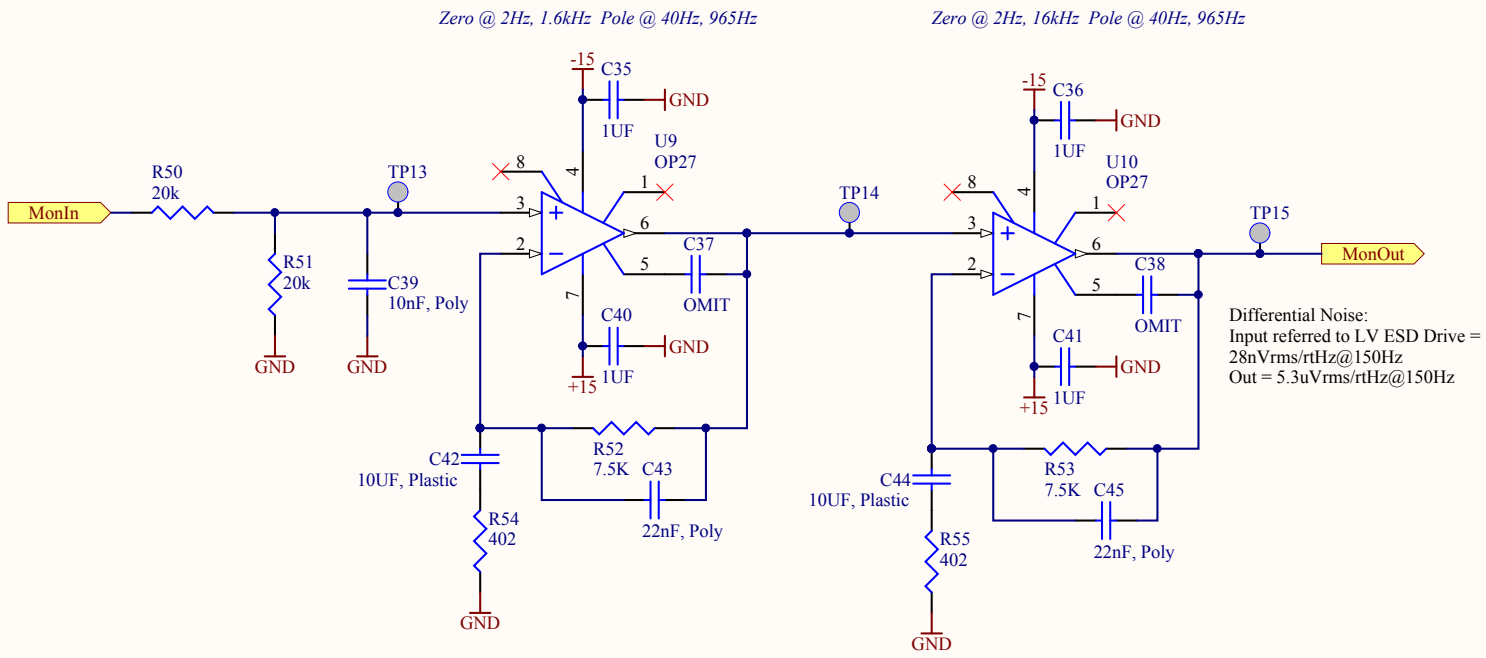
The PI correction signal is anticipated to be required between 10kHz and 80kHz. This circuit filters the correction signal to avoid potential noise injection at lower frequencies

Checked All

Last Edited: 6/13/2016

Title 10kHz Sallen Key HPF		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\HPF.SchDoc				
				Sheet 7 of 13

The large dynamic range of the output drivers (40vp-p) requires this monitor to attenuate and whiten the input signal.

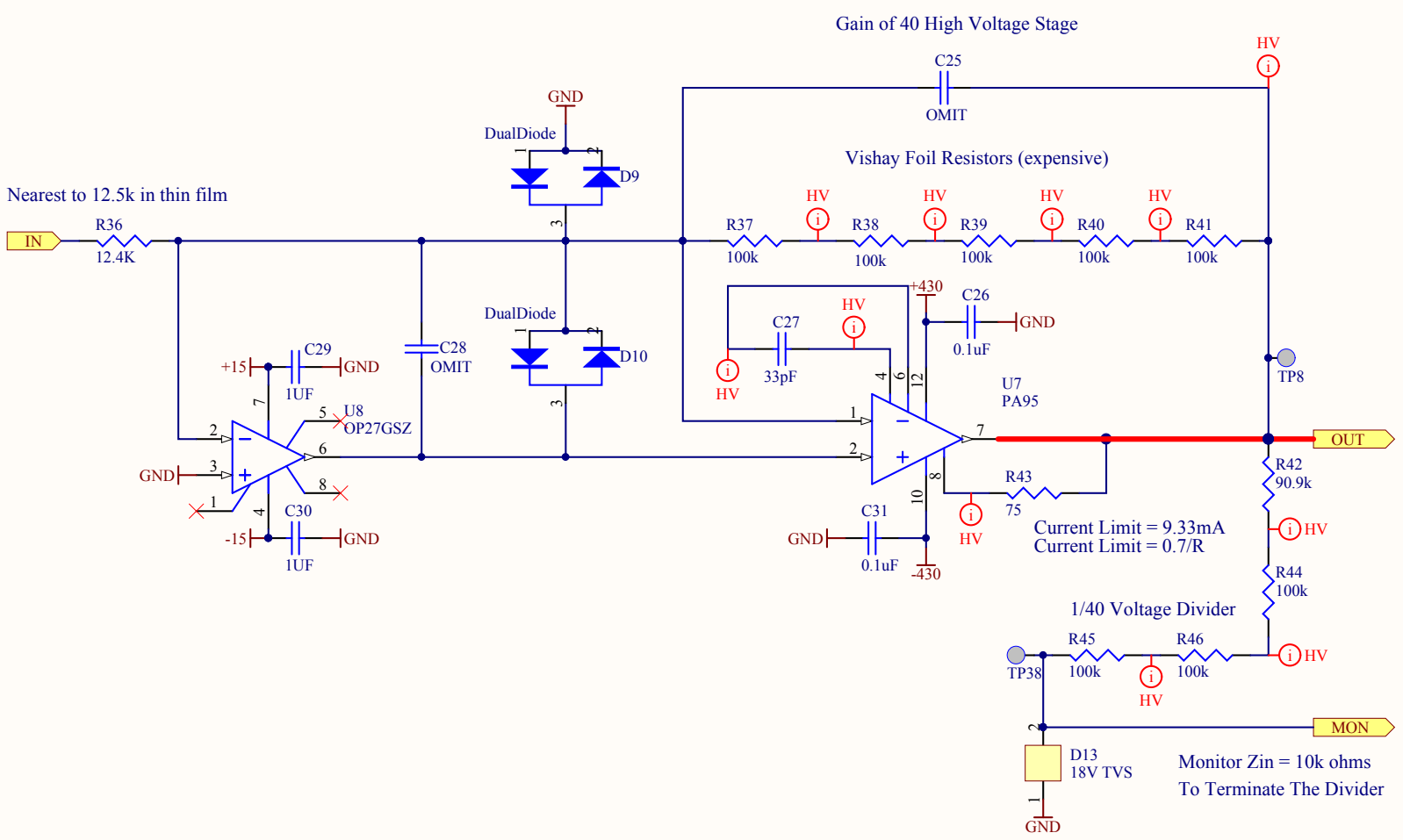


Differential Noise:
 Input referred to LV ESD Drive =
 28nVrms/rtHz@150Hz
 Out = 5.3uVrms/rtHz@150Hz

Checked All

Last Edited: 6/13/2016

Title Monitoring Amplifier		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	Date: 6/13/2016	
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\MonAmp.SchDoc				Time: 3:52:47 PM	Sheet 8 of 13

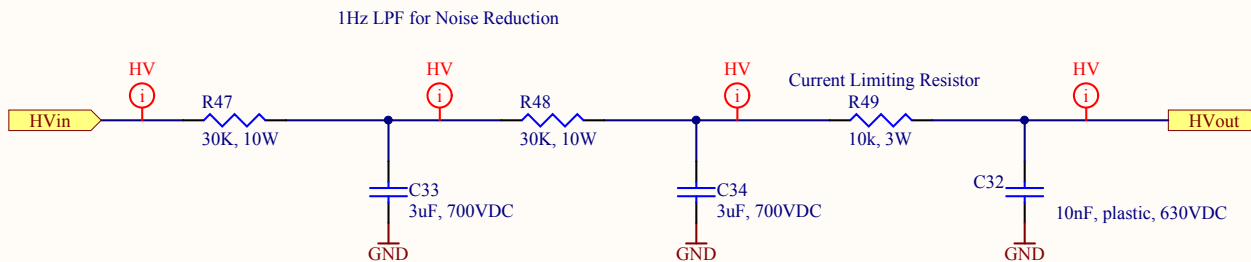


Checked All

Last Edited: 6/13/2016

Title High Voltage Amplifier		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	Date: 6/13/2016	
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\HV Amp.SchDoc				Time: 3:52:47 PM	Sheet 9 of 13

This filter can store charge. Assume the capacitors are charged until positively discharged and measured.



From T1400406 by Rai Weiss, this filter lowers the voltage noise on the bias path. This path has no requirement for fast frequency response beyond the ability to set the bias voltage on a human timescale.

An additional 10k series resistor is conservatively included as a hedge against an in-vacuum discharge event. The 10nF HV capacitor on the output can be optionally utilized to lower the source impedance to the bias electrode in the event that is useful.

Checked All

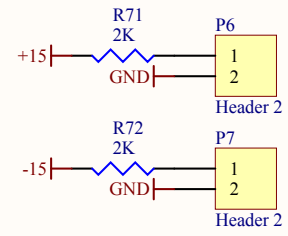
Last Edited: 6/13/2016

Title High Voltage Bias Filter		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	
Date: 6/13/2016				Sheet 10 of 13
Time: 3:52:47 PM				

Part3
 Pins for female molex connector
 WM2307-ND
 Quantity: 6

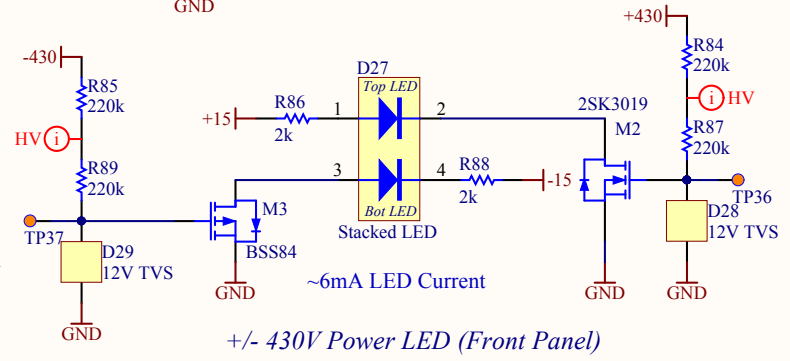
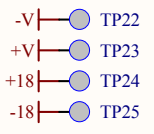
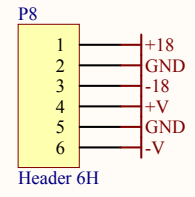
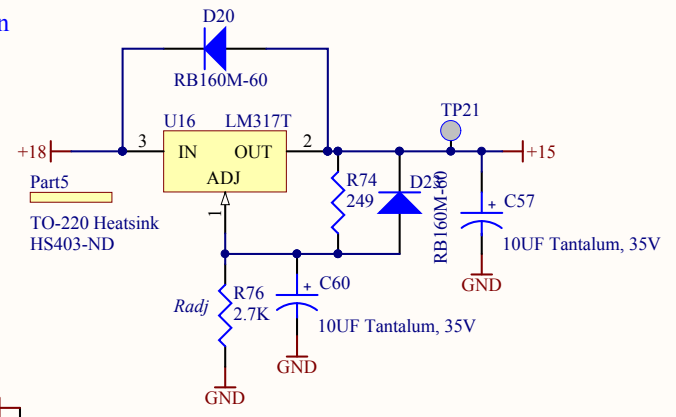
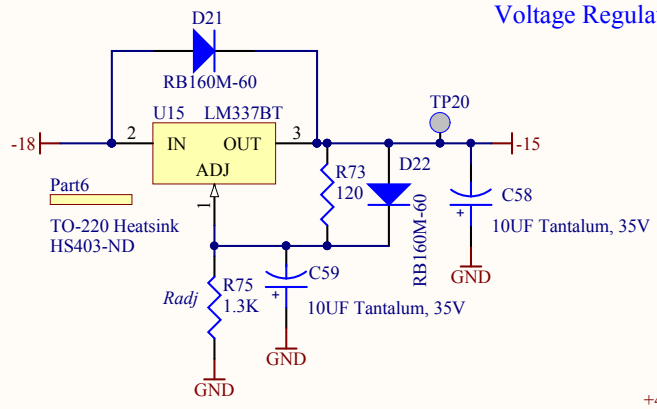
Part4
 Mating 6 pin molex connector
 WM2126-ND

The +/-V input here is to drive the ADA4700 output driver chip. This voltage form can go up to +/-48V. At time of writing, we intend to use +/-24VDC supplies and see how we do.



Rear Panel LEDs

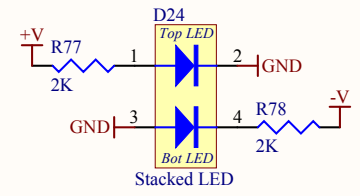
Voltage Regulation



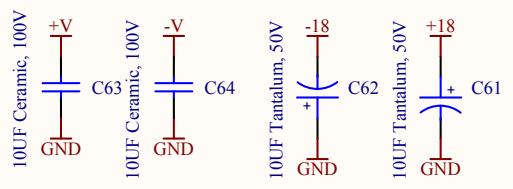
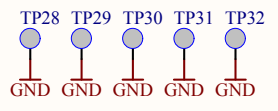
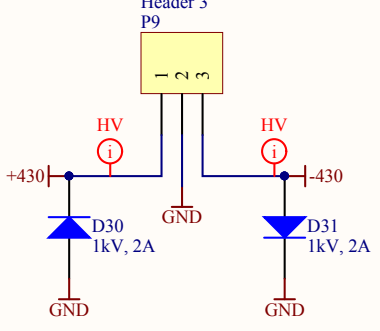
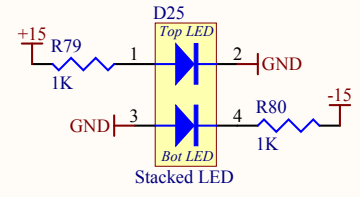
Incoming High Voltage For PA95 in HV Bias Section

+/- 430V Power LED (Front Panel)

+V/-V Power LED (Front Panel)



15VDC Power LED (Rear Panel)

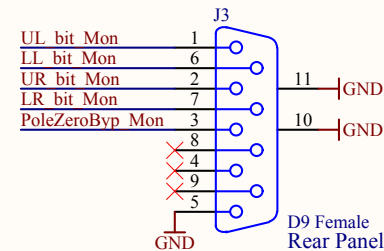
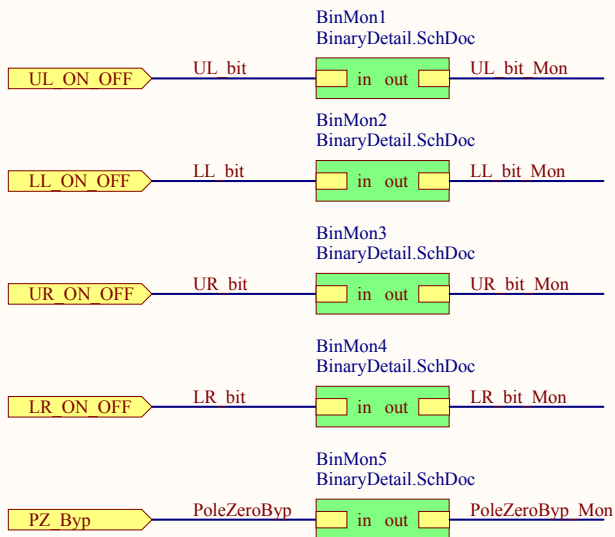


Checked All

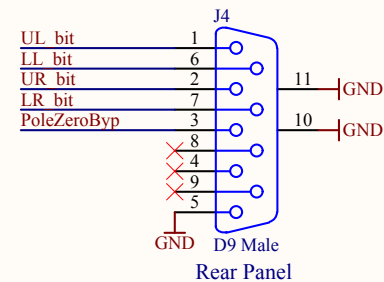
Last Edited: 6/13/2016

Title Power Supplies		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology	
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott
Date: 6/13/2016		Time: 3:52:47 PM	
Sheet 11 of 13			

Binary Monitors to Binary Input Module
 Drives Binary Input Module when monitored parameter is HIGH



Binary Inputs from Binary Output Module



Checked All

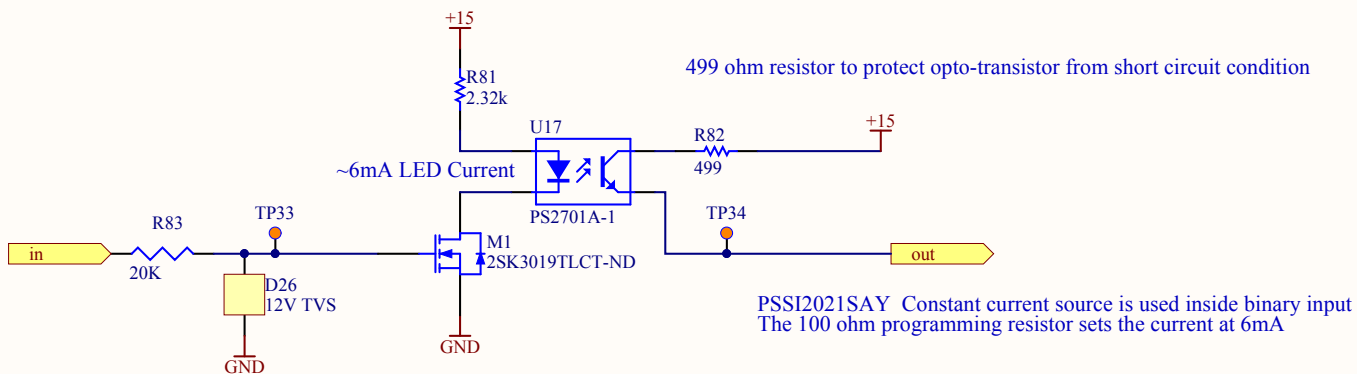
Last Edited: 6/13/2016

Title **Binary Command and Monitor**

LIGO Laboratory
 California Institute of Technology
 Massachusetts Institute of Technology



Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	Date: 6/13/2016
				Time: 3:52:47 PM
				Sheet 12 of 13

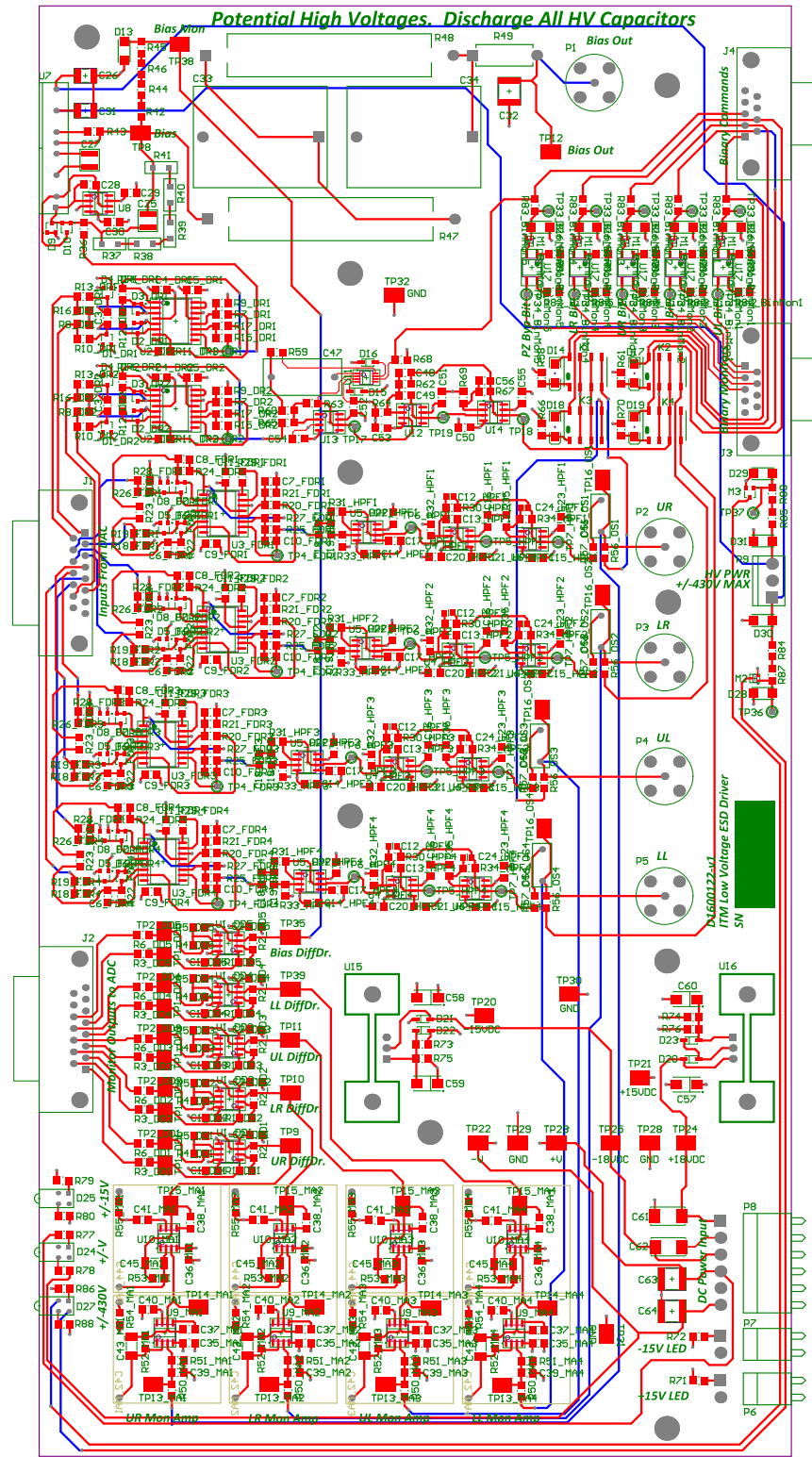


Checked All

Last Edited: 6/13/2016

Title		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO
Detail of Binary Monitor				
Size: A	DCC Number: D1600122	Revision: V3	Engineer: R. Abbott	Date: 6/13/2016
				Time: 3:52:47 PM
File: C:\Rich's Files\Mycadfiles\Suspensions\ITM LVLN Driver\BinaryDetail.SchDoc				Sheet 13 of 13

Potential High Voltages. Discharge All HV Capacitors



LIGO Bill of Materials

Source Data From: ITM_AU_Driver_V0.PDF
 Board Equipment No: A. Board
 Board Number: D886102
 Board Revision: V2
 Variant: 2000

Assembly Name: 2000-001
 Part Date: 2000-01-01

Designator	Quantity	Description	Supplier Part Number	Manufacturer Part Number	Notes
U1:001	1	U1:001	U1:001	U1:001	
U1:002	1	U1:002	U1:002	U1:002	
U1:003	1	U1:003	U1:003	U1:003	
U1:004	1	U1:004	U1:004	U1:004	
U1:005	1	U1:005	U1:005	U1:005	
U1:006	1	U1:006	U1:006	U1:006	
U1:007	1	U1:007	U1:007	U1:007	
U1:008	1	U1:008	U1:008	U1:008	
U1:009	1	U1:009	U1:009	U1:009	
U1:010	1	U1:010	U1:010	U1:010	
U1:011	1	U1:011	U1:011	U1:011	
U1:012	1	U1:012	U1:012	U1:012	
U1:013	1	U1:013	U1:013	U1:013	
U1:014	1	U1:014	U1:014	U1:014	
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U1:100	1	U1:100	U1:100	U1:100	