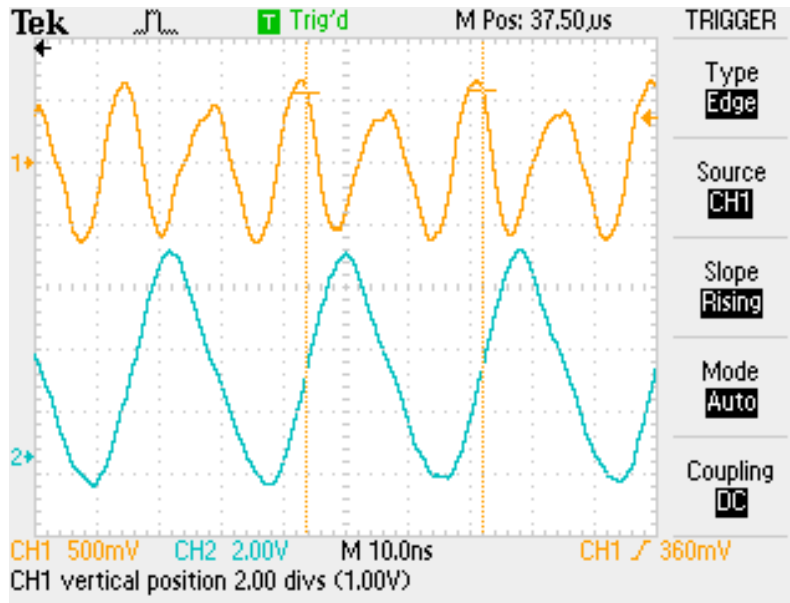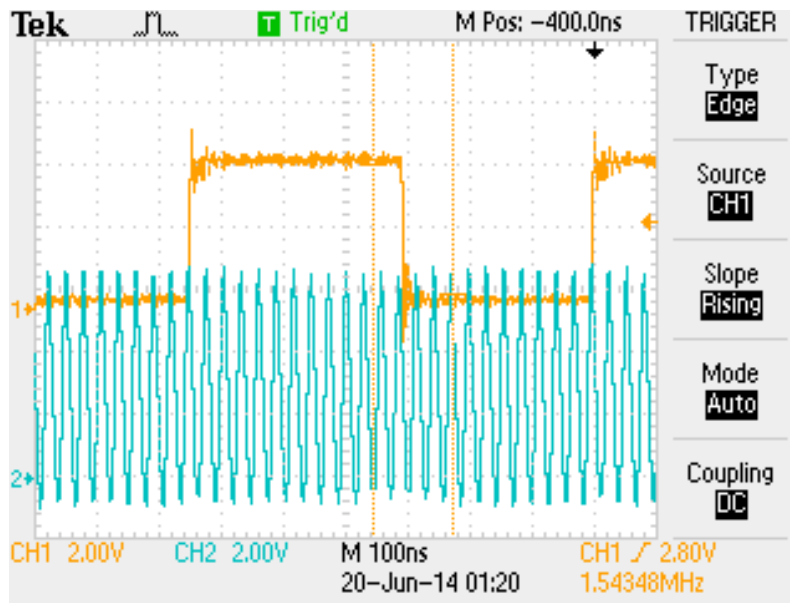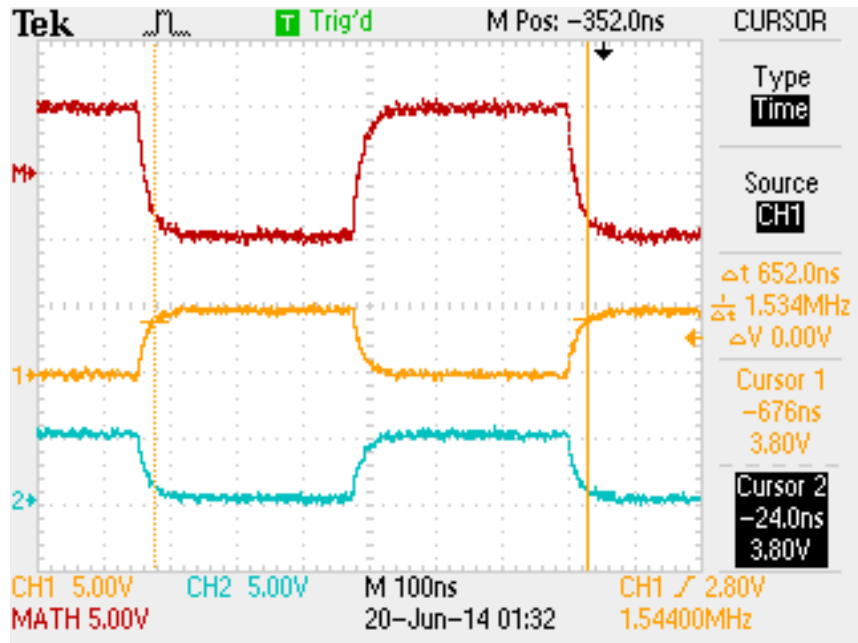Incoming 71MHz clock (orange) divided by 2, and biased (blue):
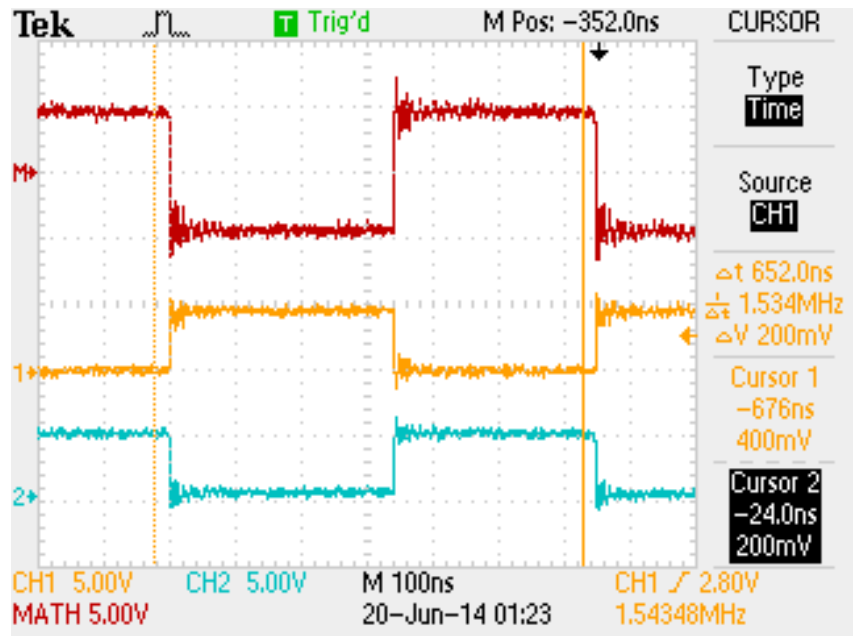

35.5MHz clock (blue) divided by 23 on the PSoC board to make 1.54 MHz (orange):
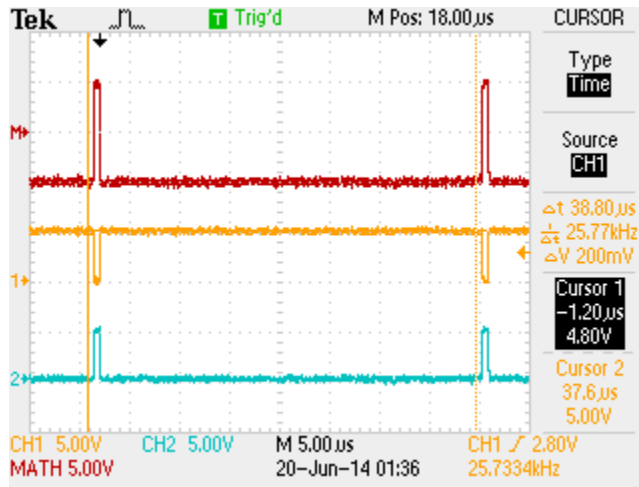
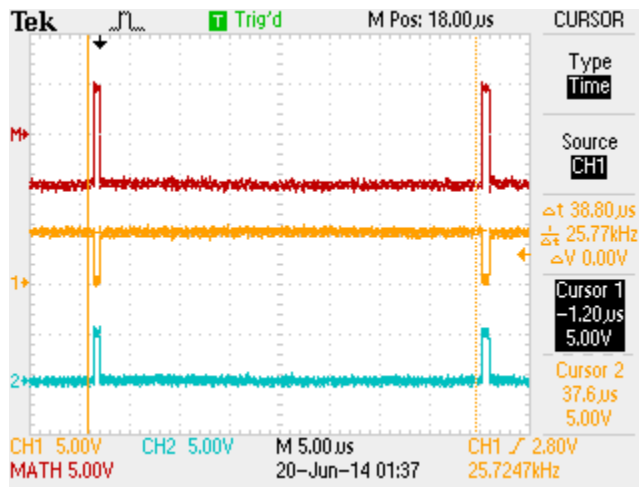CPS Chassis Differential (RS485) output clock 1.54 MHz:

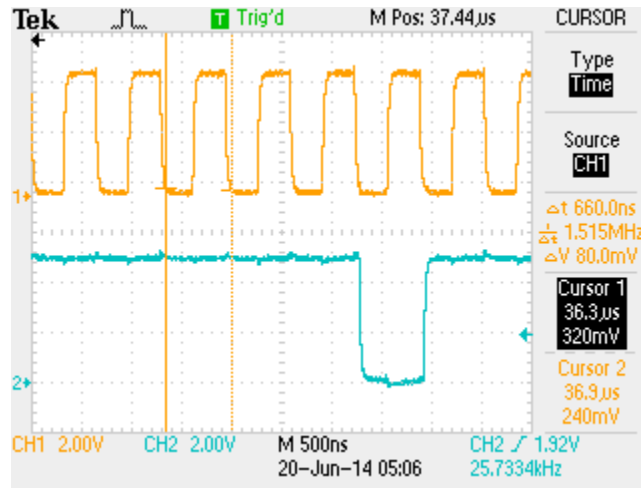Fanout Chassis differential RS485 output clock 1.54MHz:

CPS Chassis 1.54MHz / 60 = 25.73KHz Pulses:

Fanout Chassis 25.73KHz Pulses:

CPS Chassis Pulse duration vs. Clock signal:

Fanout Chassis Pulse duration vs Clock signal: