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Test Procedure for RF diplexer amplifier

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## 1 Introduction

The following Test Procedure describes the test of proper operation of the RF diplexer amplifier (E1300852). The unused outputs should always be properly terminated.

## 2 Test Equipment

- Voltmeter
- Oscilloscope
- Agilent Network/Spectrum analyzer (e.g. AG4395A)
- Agilent ESG-1000 signal generator (or similar signal source with  $f_{\max} > 150\text{MHz}$ )
- RF Power Meter (e.g. HP E4418A or N1914A)
- Board Schematics--[D1300989](#)

## 3 Tests

*The RF Preamplifier comes with the Low Noise Power Module (D0901846).*

- 1) **Verify the proper current draw.** Using a bench DC supply apply  $\pm 24\text{Volts}$  to P7 and  $\pm 17\text{ Volts}$  to P6 of the low noise power Module (D0901846). Measure the current draw of the board.

+24 Volt current \_\_\_\_\_ 0.02 A Nom.

-24 Volt current \_\_\_\_\_ 0.02 A Nom.

+17 Volt current \_\_\_\_\_ 0.2~0.3 A Nom.

-17 Volt current \_\_\_\_\_ 0.03 A Nom.

**2) On the low noise power module check the voltage on TP 1-13.**

TP1 (+17V ) \_\_\_\_\_ TP2 (-17V ) \_\_\_\_\_

TP3 , 4 ( GND ) TP5 (+ 5V) \_\_\_\_\_

TP6 (-15V ) \_\_\_\_\_ TP7 (+24V ) \_\_\_\_\_

TP8 ( GND ) TP9 ( -24V ) \_\_\_\_\_

TP10 ( GND ) TP11 (+15V ) \_\_\_\_\_

TP12 (+VREF ) \_\_\_\_\_ TP13 (-VREF ) \_\_\_\_\_

**3) If TP 1 , 2, 7 , 9 and 8 are correct then pin 5 on U1 and U7, TP14 (OK) should be Logic high ~3Volts. The front panel LED should be on.**

**Confirm.** \_\_\_\_\_

**4) The noise on TP 12, 13, 11 and 6 should be measured with a SR785 using an rms power spectrum.**

TP12 noise \_\_\_\_\_ less than 200 nVrms/sqrt Hz at 140 Hz

TP13 noise \_\_\_\_\_ less than 900 nVrms/sqrt Hz at 140 Hz

TP11 noise \_\_\_\_\_ less than 1 uVrms/sqrt Hz at 140 Hz

TP6 noise \_\_\_\_\_ less than 1 uVrms/sqrt Hz at 140 Hz.

- 5) Test the output powers of each port by applying the RF signal to the “In” port at the below specified power and frequency. With an RF power meter measure the power at the three output ports (i.e. “Direct”, “LF”, HF). Compare the output with the specification.

**Frequency: 27.3MHz, Input power -20dBm**

Output port	Measured Power	Specification (dBm)	Judgement (Pass/NG)
Direct	dBm	-32 +/- 1 dBm	
LF	dBm	> -2 dBm	
HF	dBm	< -27 dBm	

**Frequency: 136.5MHz, Input power -40dBm**

Output port	Measured Power (dBm)	Specification (dBm)	Judgement (Pass/NG)
Direct	dBm	-52 +/- 1 dBm	
LF	dBm	< -72 <sup>†</sup> dBm	
HF	dBm	> +3 dBm	

<sup>†</sup> This power level may be too small to be measured with the power meter. An RF spectrum analyzer may be needed.

- 6) Measure -0.5dB pass and -50dB rejection bandwidth by changing the input frequency from the above setting. Change the signal frequency, starting from the above specified signal frequency. Record the upper and lower frequency where the output is reduced by 0.5dB and 50dB. Confirm the pass and rejection band with the specification.

**LF port, center frequency: 27.3MHz, Input power -20dBm**

Lower boundary of the -0.5dB band	Upper boundary of the -0.5dB band	Pass band width $df=f_{0.5dBHI} - f_{0.5dBLO}$	Spec.	Judgement (Pass/NG)
$f_{0.5dBLO} =$ MHz	$f_{0.5dBHI} =$ MHz	$df =$ MHz	$df > 1\text{MHz}$	

Lower boundary of the -50dB band	Upper boundary of the -50dB band	Rejection band specification	Judgement (Pass/NG)
$f_{50dBLO} =$ MHz	$f_{50dBHI} =$ MHz	$f_{50dBLO} > 18.2\text{MHz}$ $f_{50dBLO} < 45.5\text{MHz}$	

**HF port, center frequency: 135.6MHz, Input power -40dBm**

Lower boundary of the -0.5dB band	Upper boundary of the -0.5dB band	Pass band width $df=f_{0.5dBHI} - f_{0.5dBLO}$	Spec.	Judgement (Pass/NG)
$f_{0.5dBLO} =$ MHz	$f_{0.5dBHI} =$ MHz	$df =$ MHz	$df > 2\text{MHz}$	

Lower boundary of the -50dB band	Upper boundary of the -50dB band	Rejection band specification	Judgement (Pass/NG)
$f_{50dBLO} =$ MHz	$f_{50dBHI} =$ MHz	$f_{50dBLO} > 127.4\text{MHz}$ $f_{50dBLO} < 145.6\text{MHz}$	

- 7) **Measure the input power level to give the harmonic distortion of -30dBc.** Connect the LF or HF ports to an RF spectrum analyzer. Setup the harmonic distortion measurement (the function “Multi Peak search” is useful). Increase the signal amplitude with a 0.1dB step until the largest harmonics is 30dB relative to the main signal peak (“Delta Marker” is useful for this measurement). Record the power of the harmonic (up to 5th harmonics for 27.3MHz and 3rd harmonics for 136.5MHz). Confirm the output power level is larger than the specification.

**LF port, Signal frequency: 27.3MHz**

Input power for 30dBc harmonics	Power level of the main signal	Specification	Judgement (Pass/NG)
dBm	dBm	> 16 dBm	

Harmonics			
2nd (54.6MHz)	3rd (81.9MHz)	4th (109.2MHz)	5th (136.5MHz)
dBc	dBc	dBc	dBc

**HF port, Signal frequency: 136.5MHz**

Input power for 30dBc harmonics	Power level of the main signal	Specification	Judgement (Pass/NG)
dBm	dBm	> 16 dBm	

Harmonics			
2nd (273.0MHz)	3rd (409.5MHz)		
dBc	dBc		

- 8) **Measure the output noise level of the LF and HF output ports. A**

Output Port	Measured noise level	Specification	Judgement (Pass/NG)
LF (27.3MHz)	nV/rtHz	< 20 nV/rtHz	
HF (136.5MHz)	nV/rtHz	< 300 nV/rtHz	