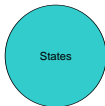




Disengaged

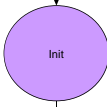
Transitional states, which will exit after a fixed time



States

Error Checking happens independent of the state  
Some errors (no green arm lock, trans PD error) will cause a transition to disengaged state

Logic.ON and no errors  
OR Logic.Force



Init

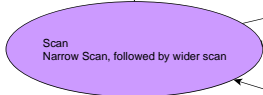
IR trans > DitherThresh

disable the feedback in the dither state by setting Controls.Enable



DitherServo

No resonance found at starting frequency or  
last found resonance frequency

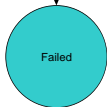


Scan  
Narrow Scan, followed by wider scan

IR trans > DitherThresh

IR trans < DitherThresh

No IR trans found



Failed



Done

Error signal below thresh,  
IR trans above thresh  
for Logic.time delay