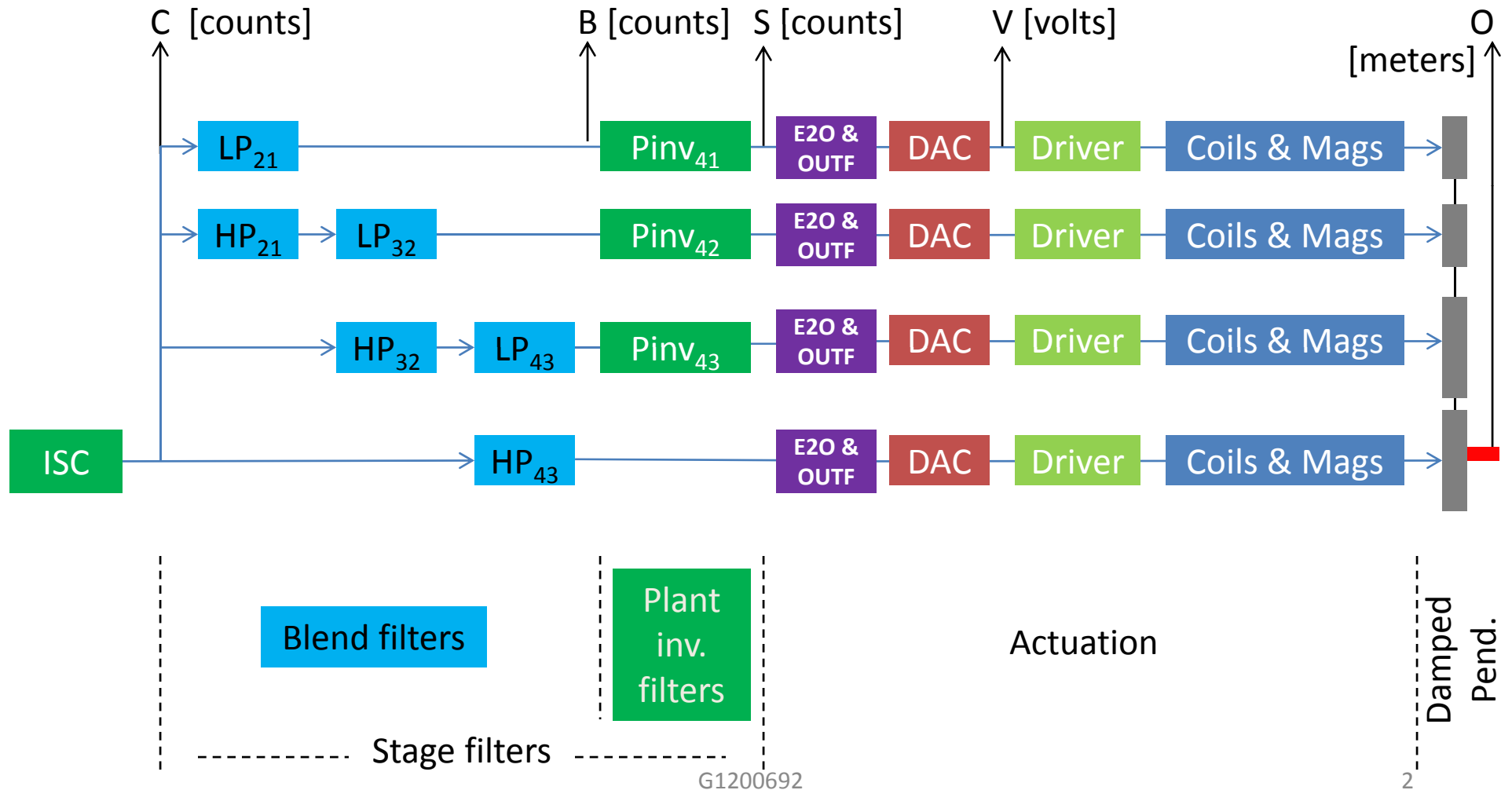


Hierarchical Control Notes – Blending Style

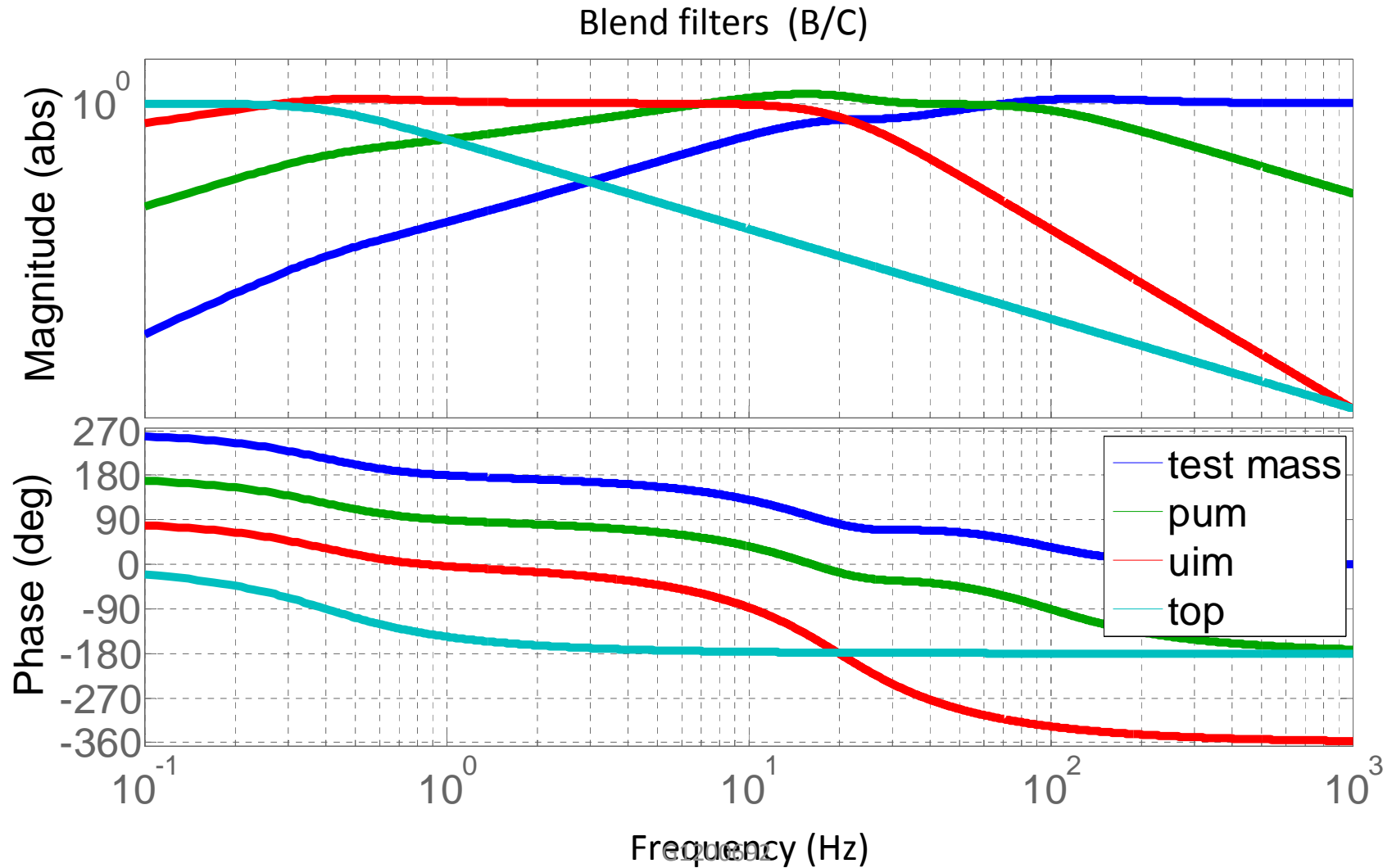
Follows quad example

Note: coil drivers and ESD are LASTI
style; seismic noise is outdated

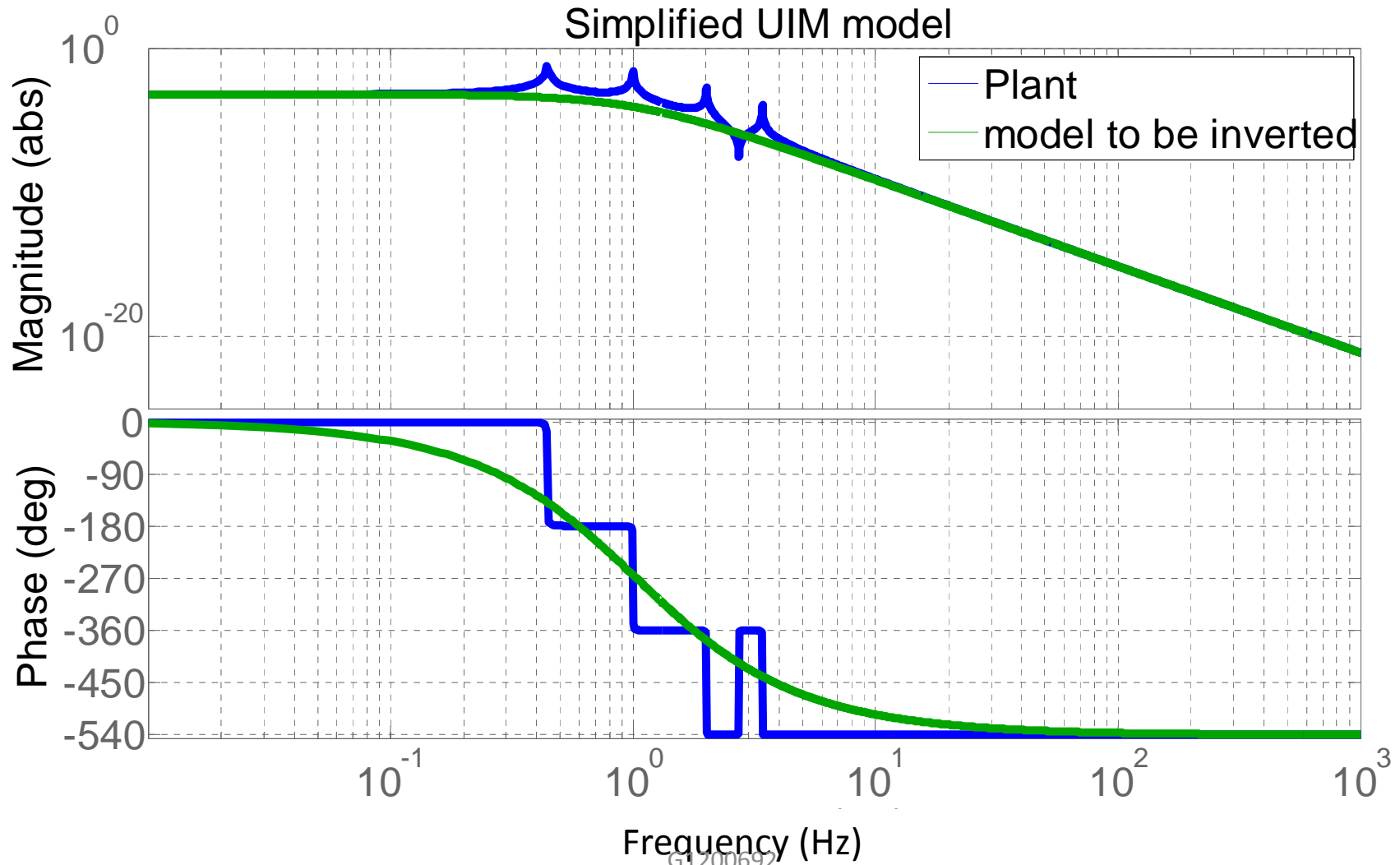
Output path of blended H.C.



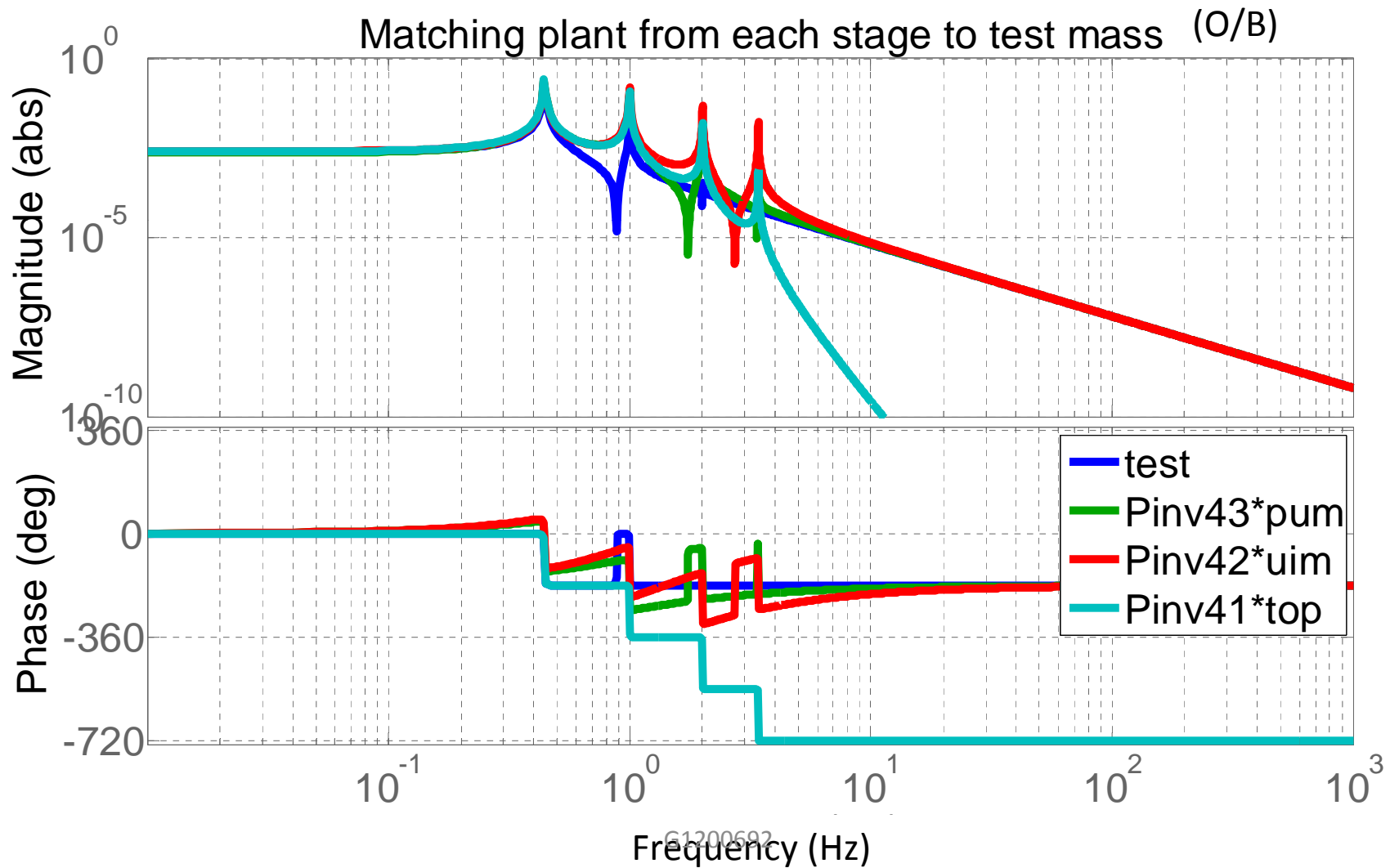
Complimentary Blend Filters



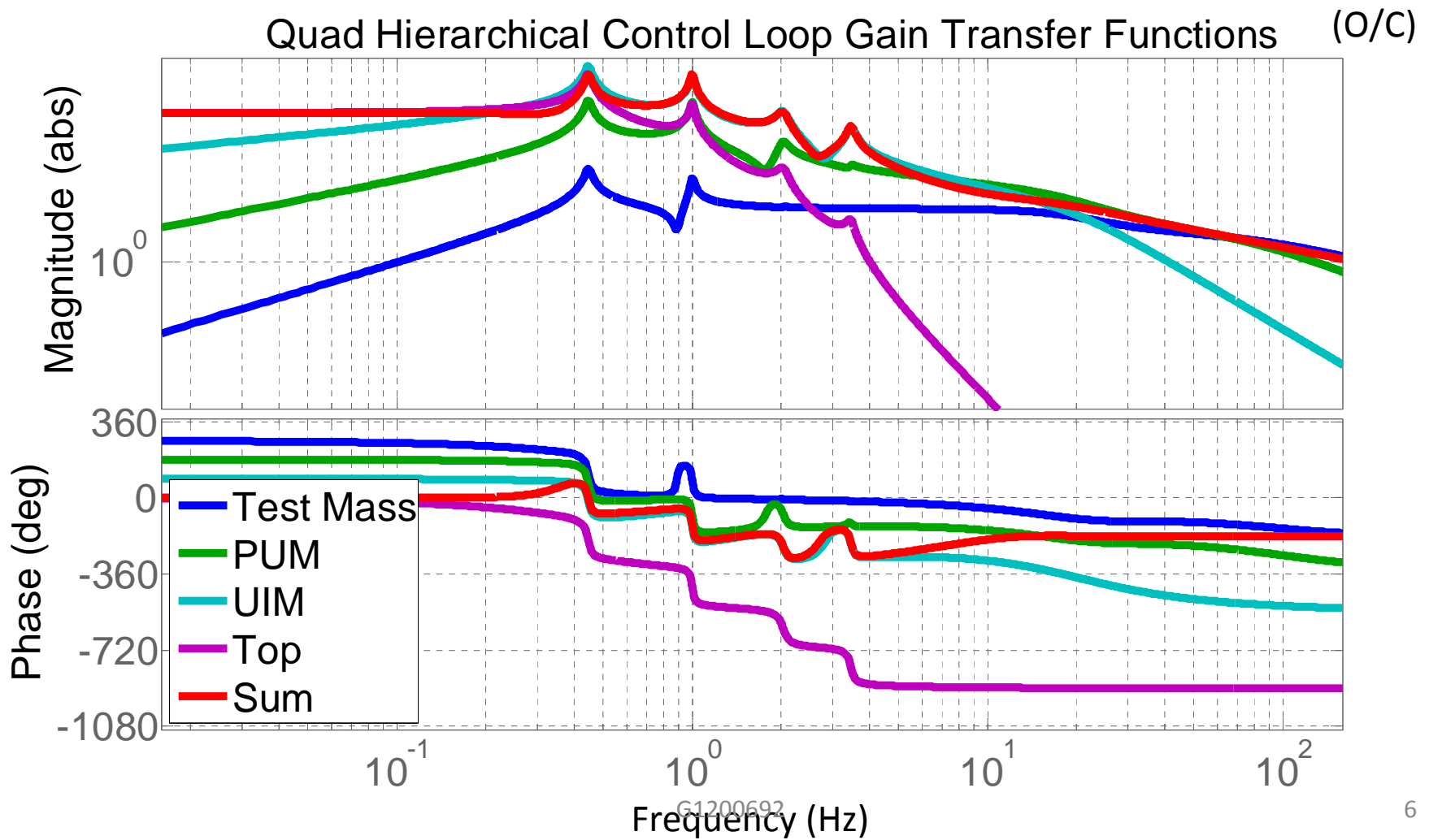
Plant inversion filters



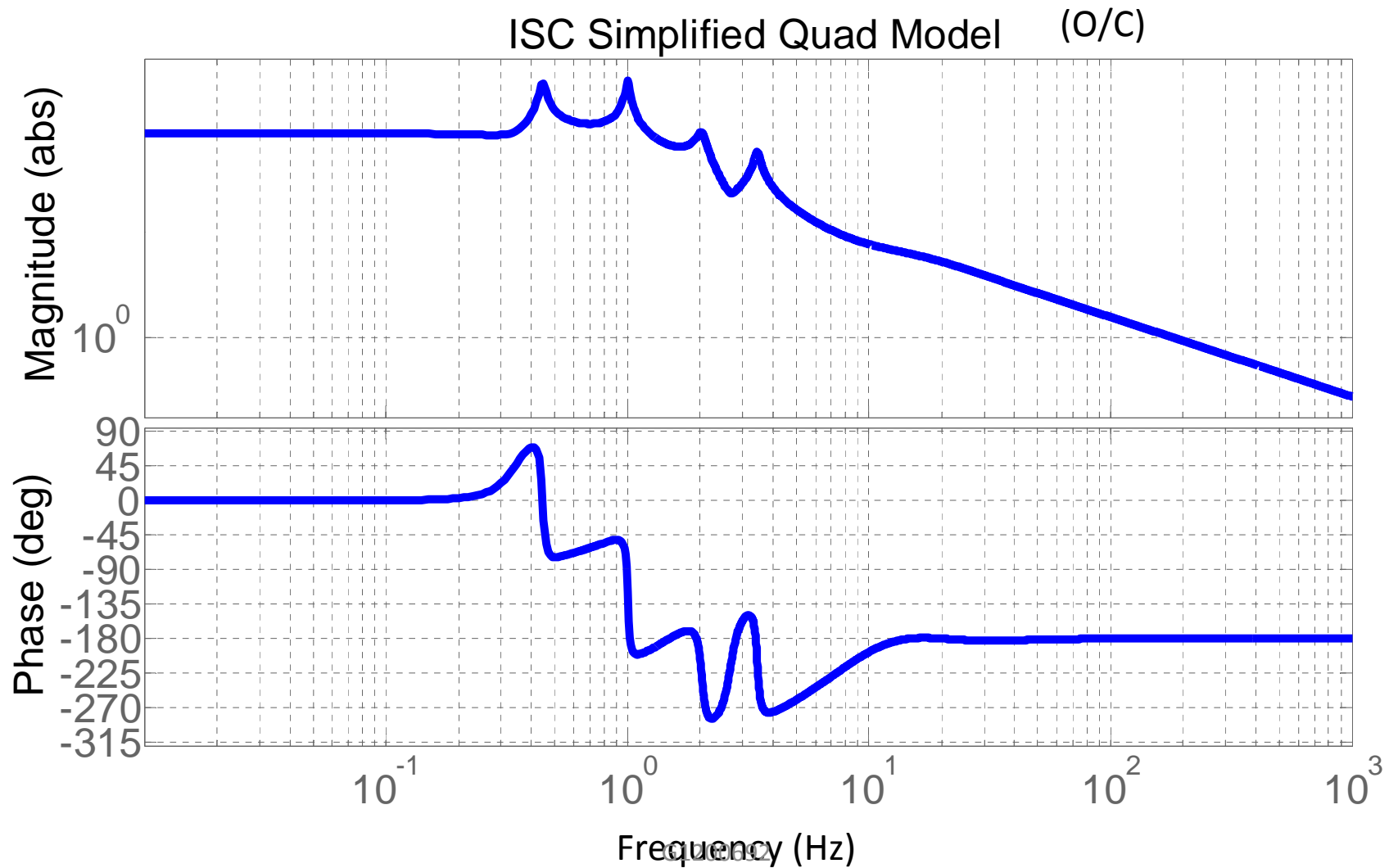
Plant times inversion filters



Loop gain plots



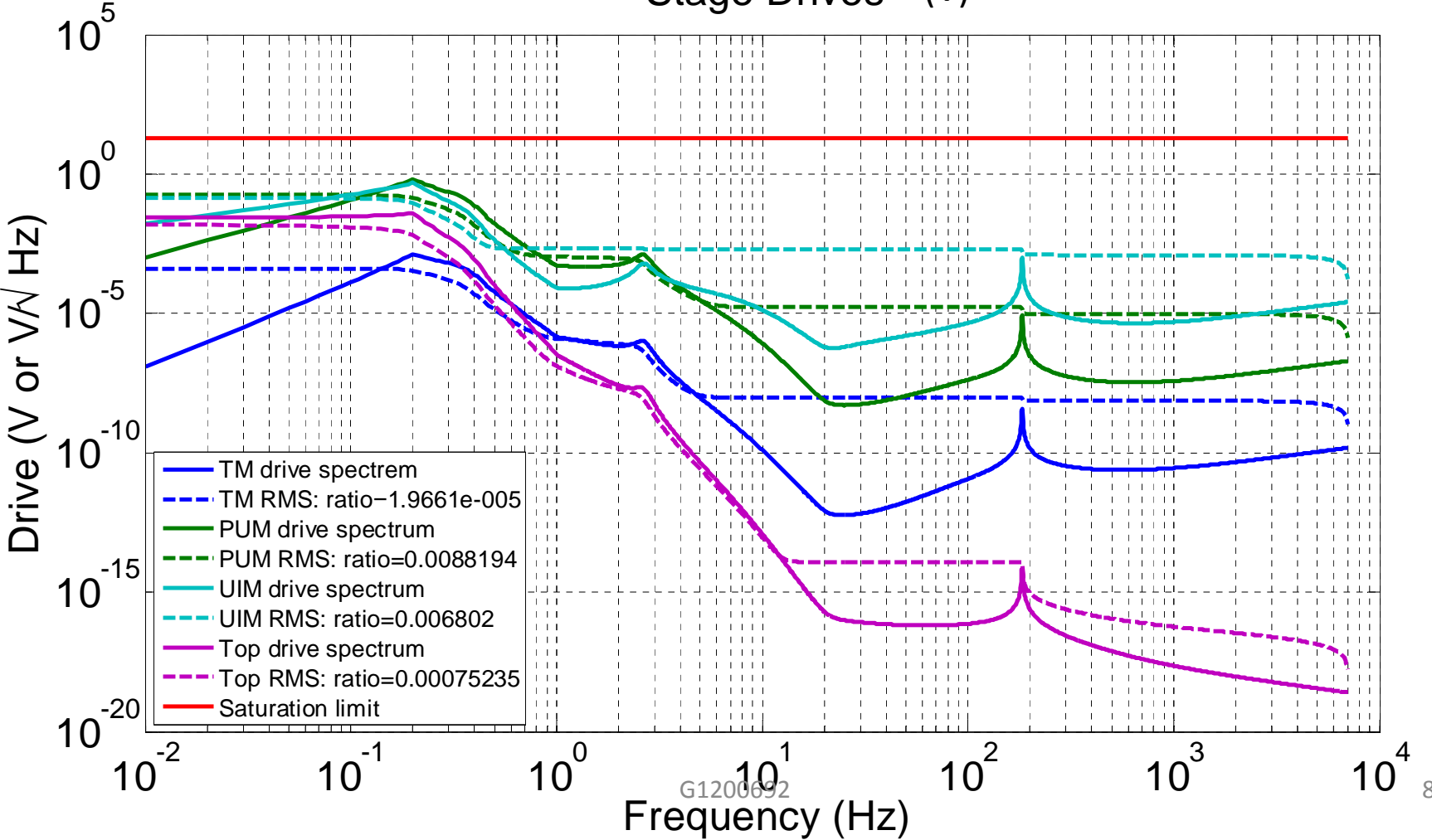
Plant seen by ISC (sum of loop gains)



Closed Loop DAC voltages (10 V limit)

Estimated by: $\frac{(\text{int. noise})(\text{stage filter})(\text{output filter})}{(\text{number of coils})(\text{ISC plant})}$

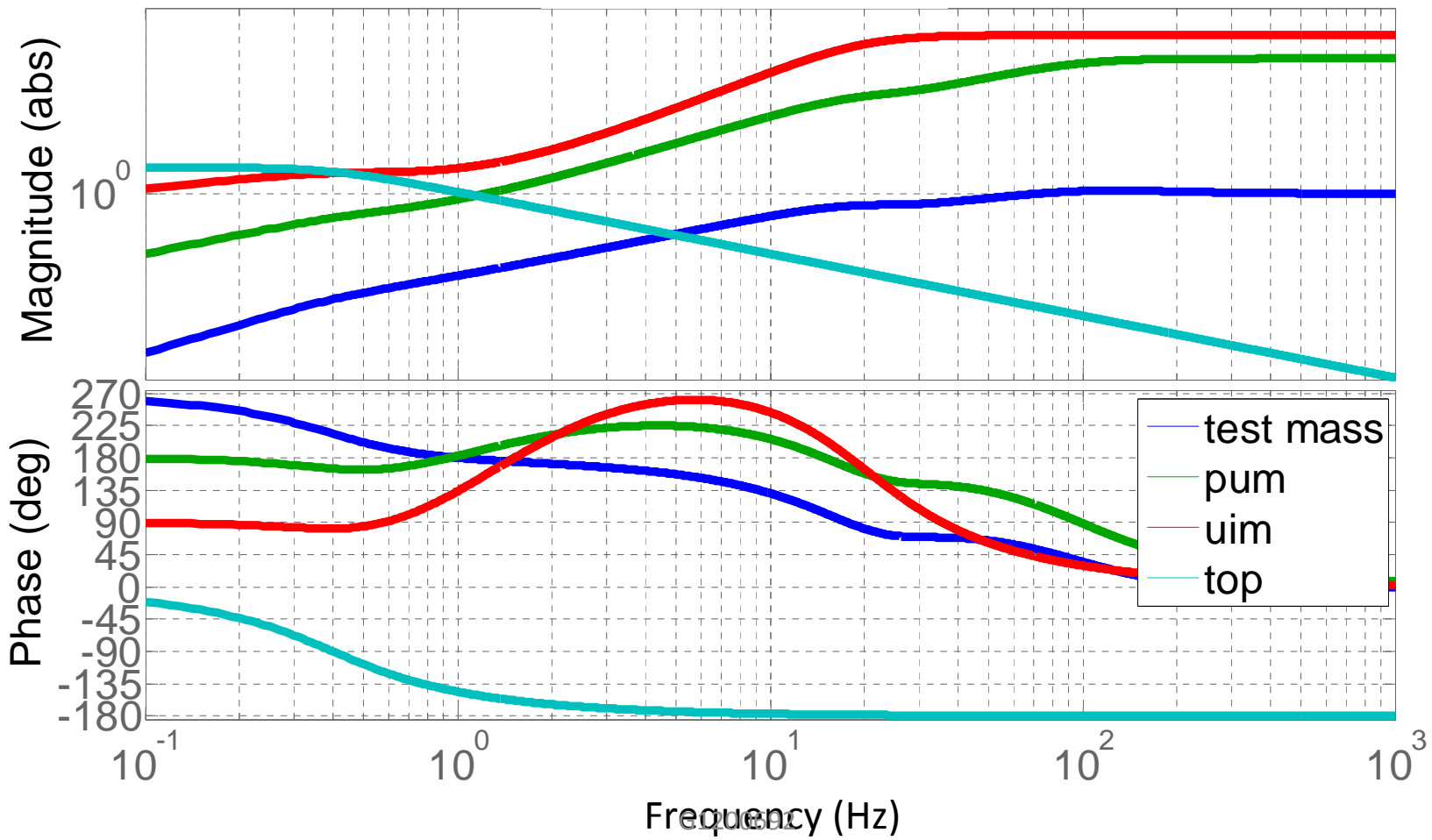
Stage Drives (V)



G1200692

Other plots

Stage filters (S/C)



DARM noise to be suppressed

