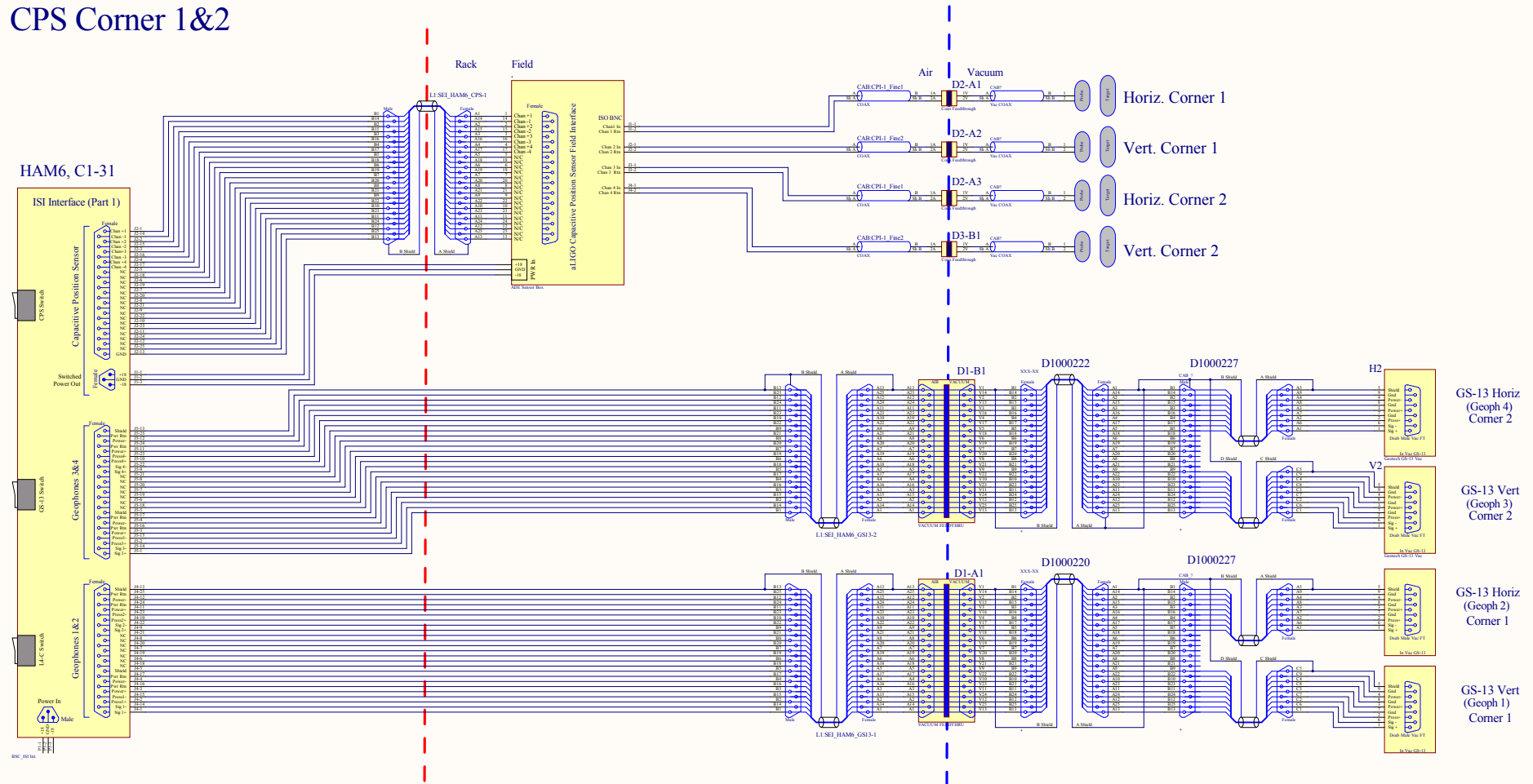


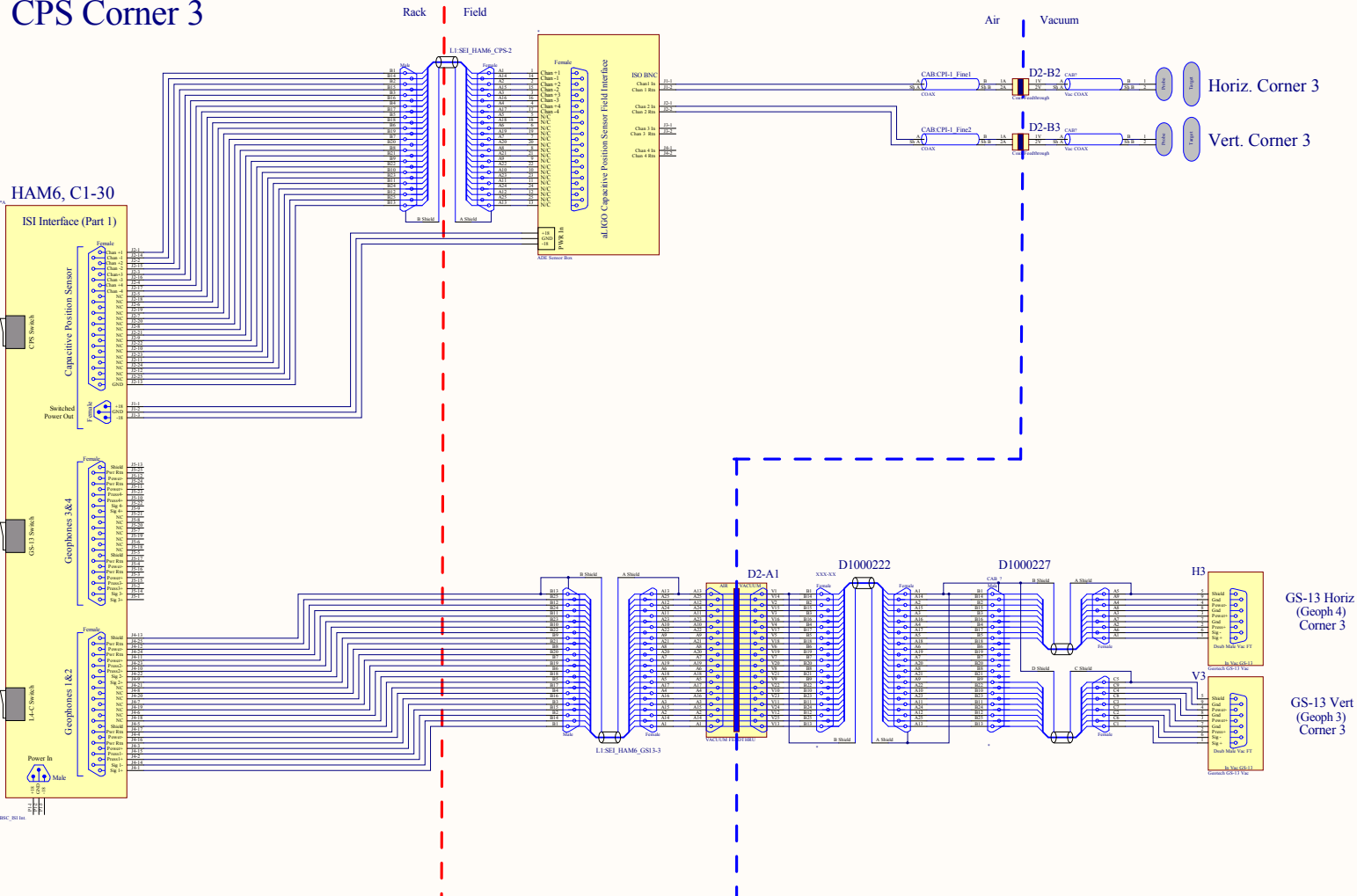
Geophones Corner 1&2 CPS Corner 1&2

HAM6, C1-31



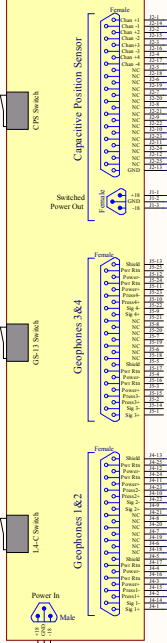
Geophones Corner 3

CPS Corner 3



HAM6, C1-30

ISI Interface (Part 1)



IRC_3014a

Horiz. Corner 3

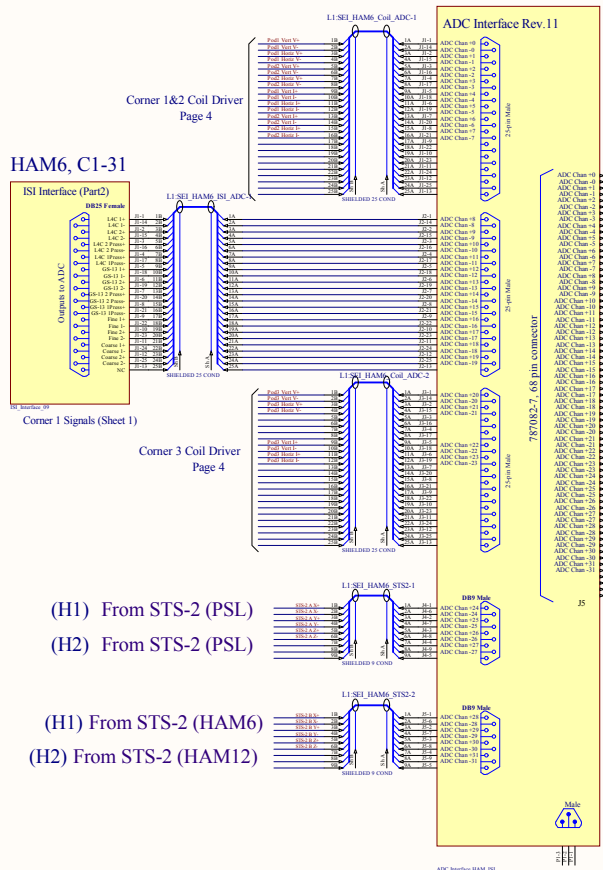
Vert. Corner 3

GS-13 Horiz
(Geoph 4)
Corner 3

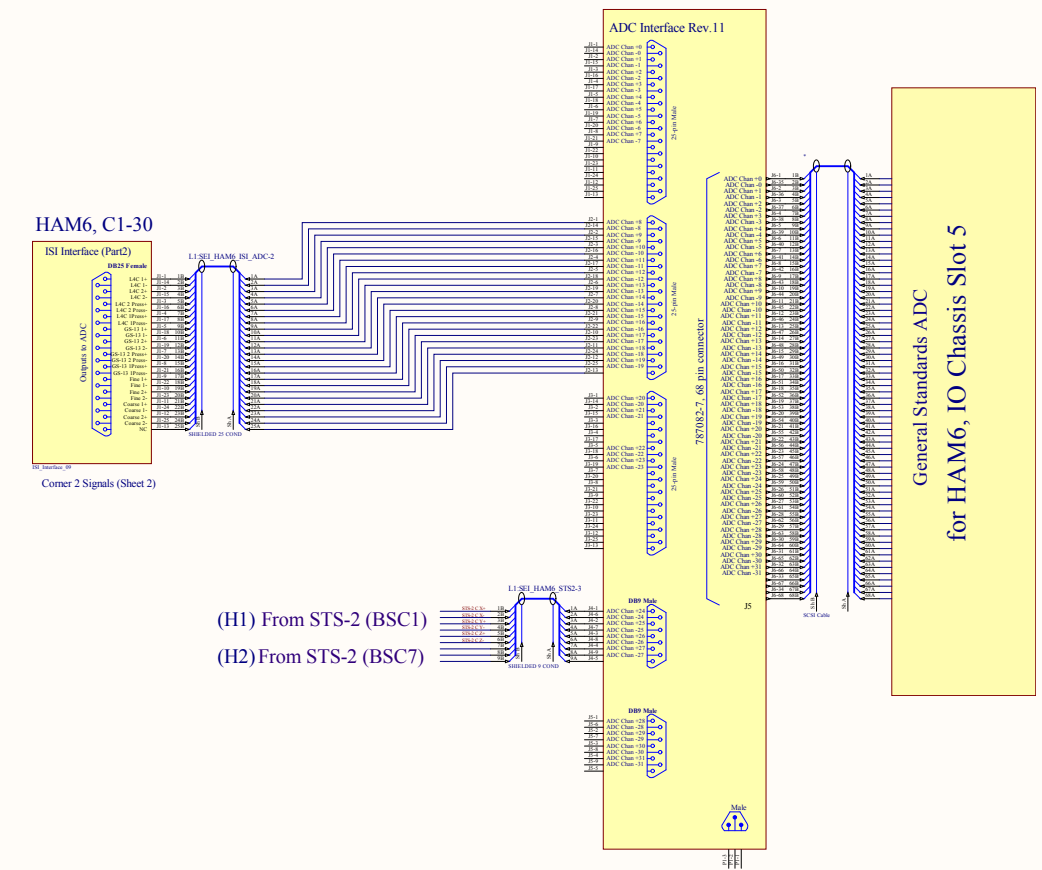
GS-13 Vert
(Geoph 3)
Corner 3

HAMs 1&6 ISI ADCs

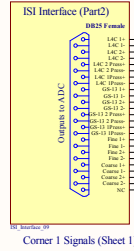
HAM6, C1-28



HAM6, C1-27



HAM6, C1-31

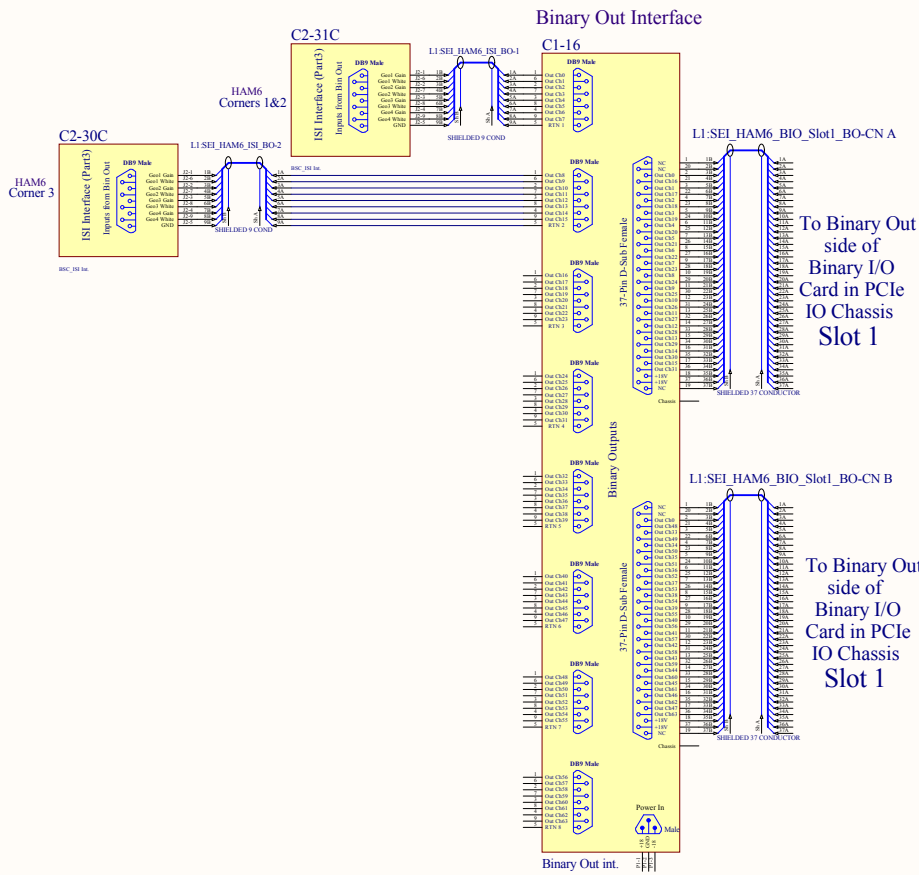


HAM6, C1-30



- (H1) From STS-2 (PSL)
- (H2) From STS-2 (PSL)
- (H1) From STS-2 (HAM6)
- (H2) From STS-2 (HAM12)

- (H1) From STS-2 (BSC1)
- (H2) From STS-2 (BSC7)



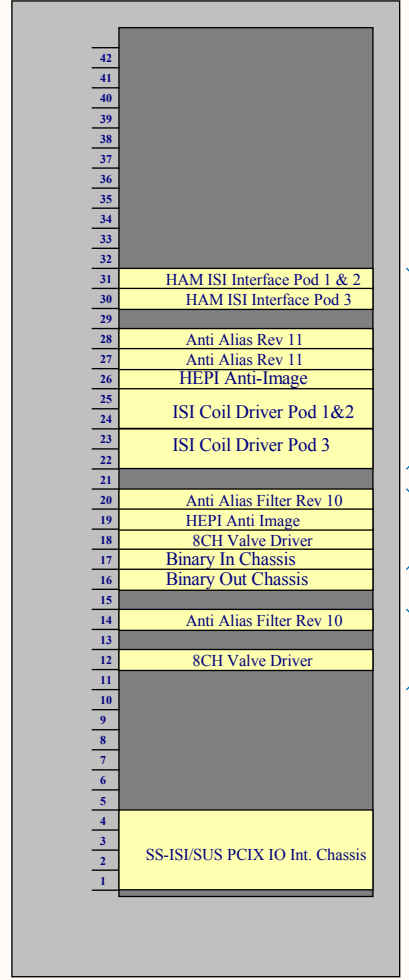
To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 1

To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 1

To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 1

To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 1

HAMs 1&6 L1-SEI-C1



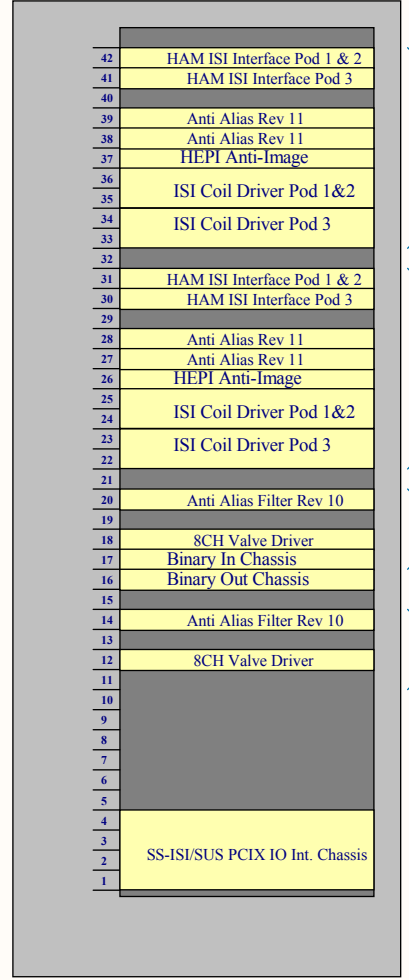
HAM AA Chassis = 10
 HEPI AA Chassis = 6
 HEPI AI Chassis = 6
 Filter Boards = 76
 Binary In Chassis = 4
 Binary Out Chassis = 3

ISI 6

HEPI 1

HEPI 6

HAMs 2&3 L1-SEI-C2



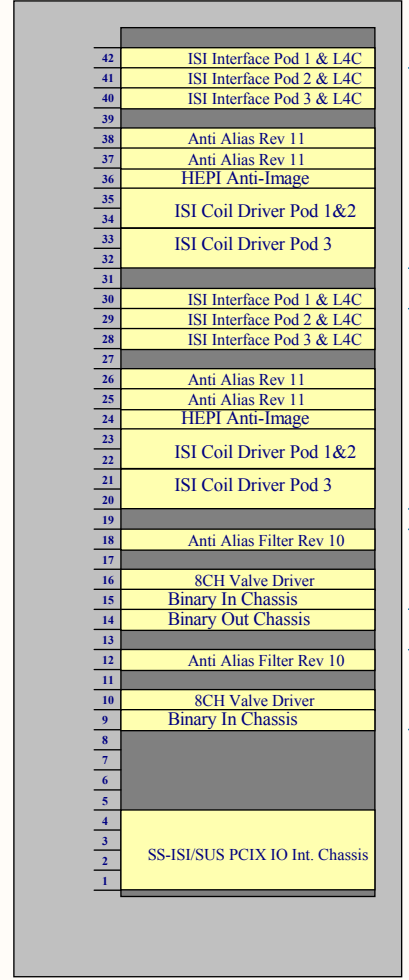
ISI 2

ISI 3

HEPI 2

HEPI 3

HAMs 4&5 L1-SEI-C3



ISI 4

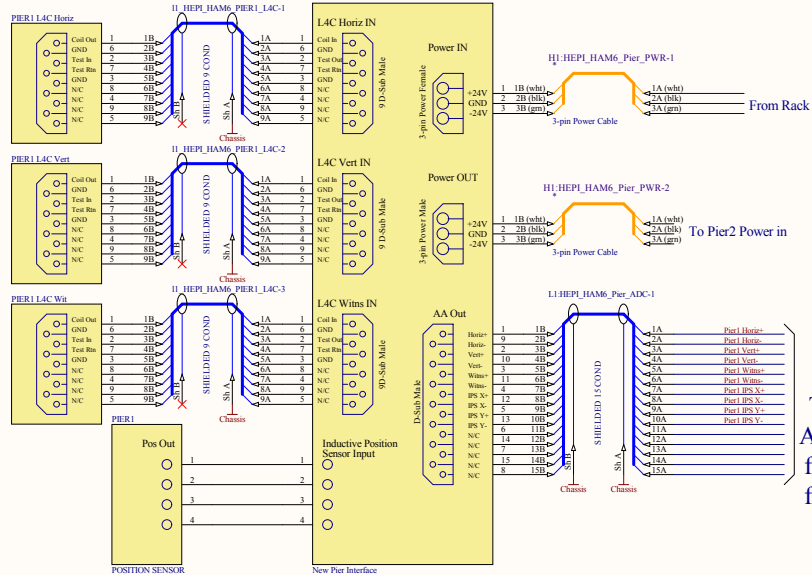
ISI 5

HEPI 4

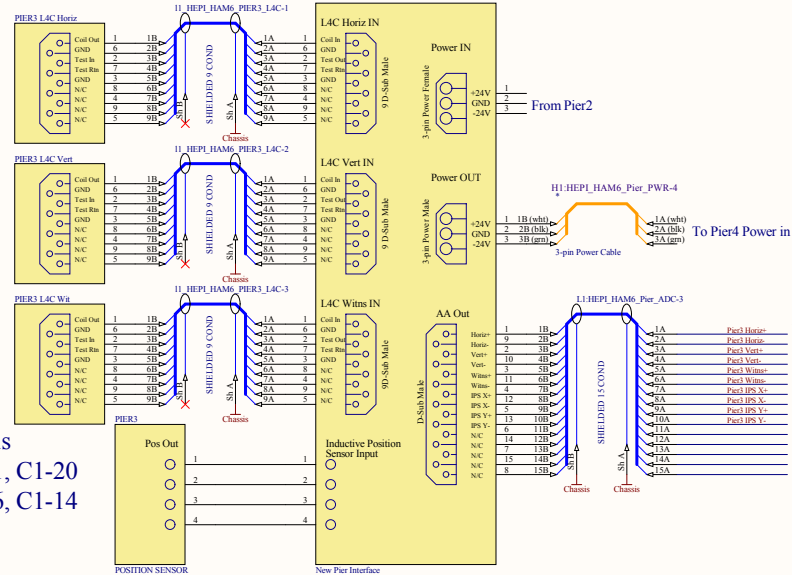
HEPI 5

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Drawn by: Ben Abbott	Date: 8/30/2011	Revision: v3	Cannot open file C:\Ben\miscellan y\ligologo_1.jpg
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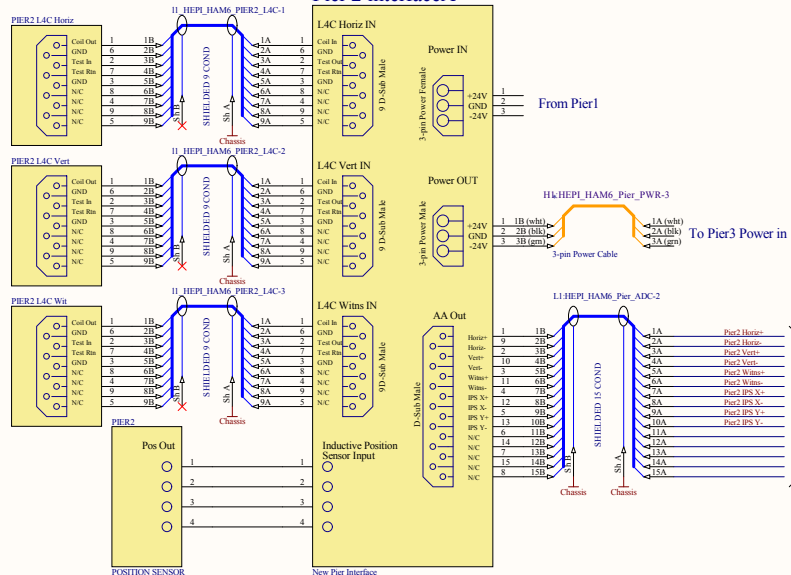
Pier 1 InterfaceA



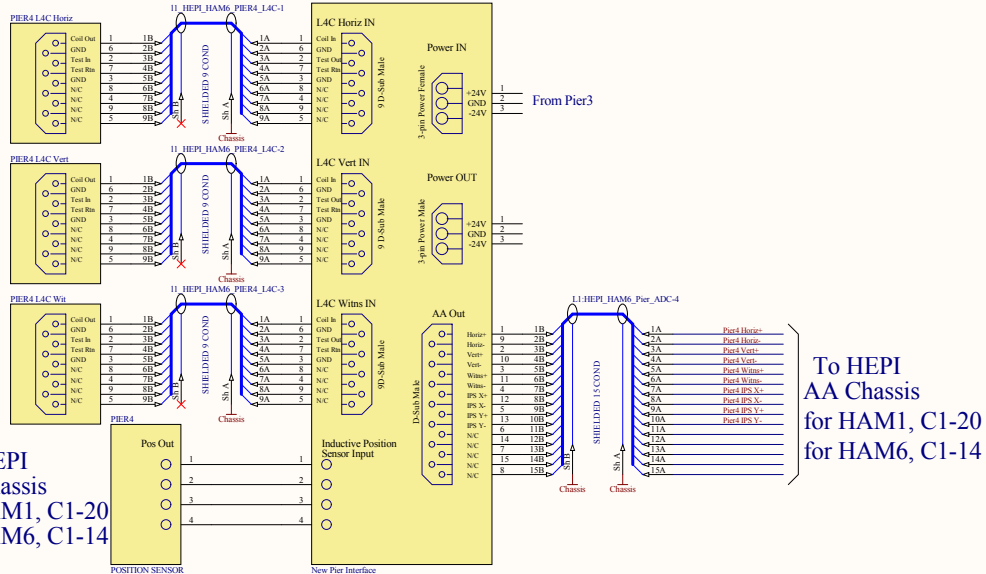
Pier 3 InterfaceA



Pier 2 interfaceA

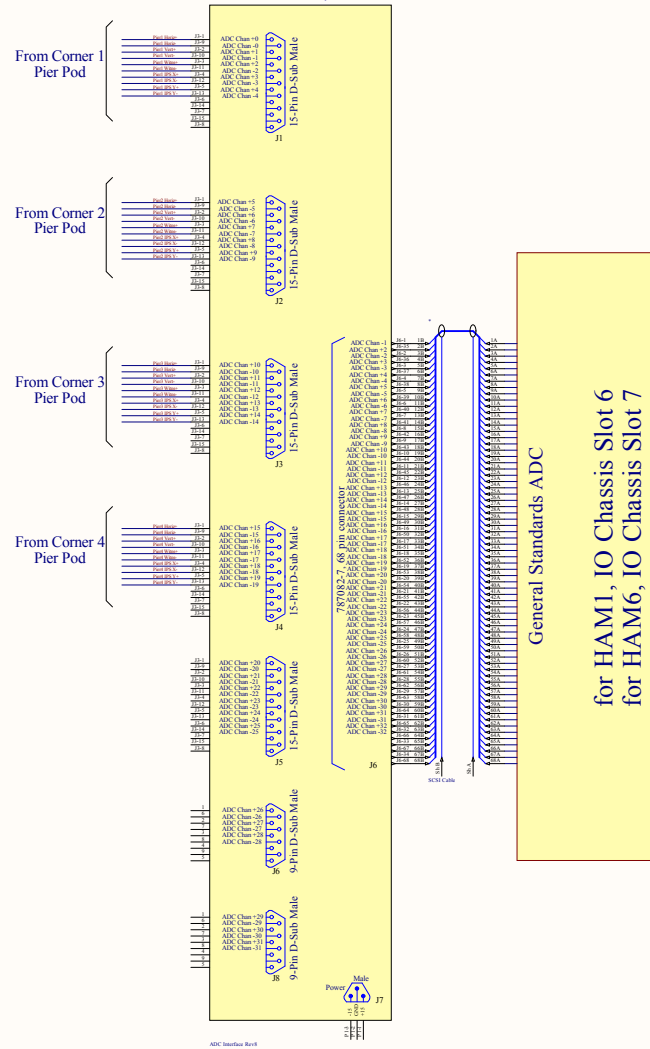


Pier 4 InterfaceA



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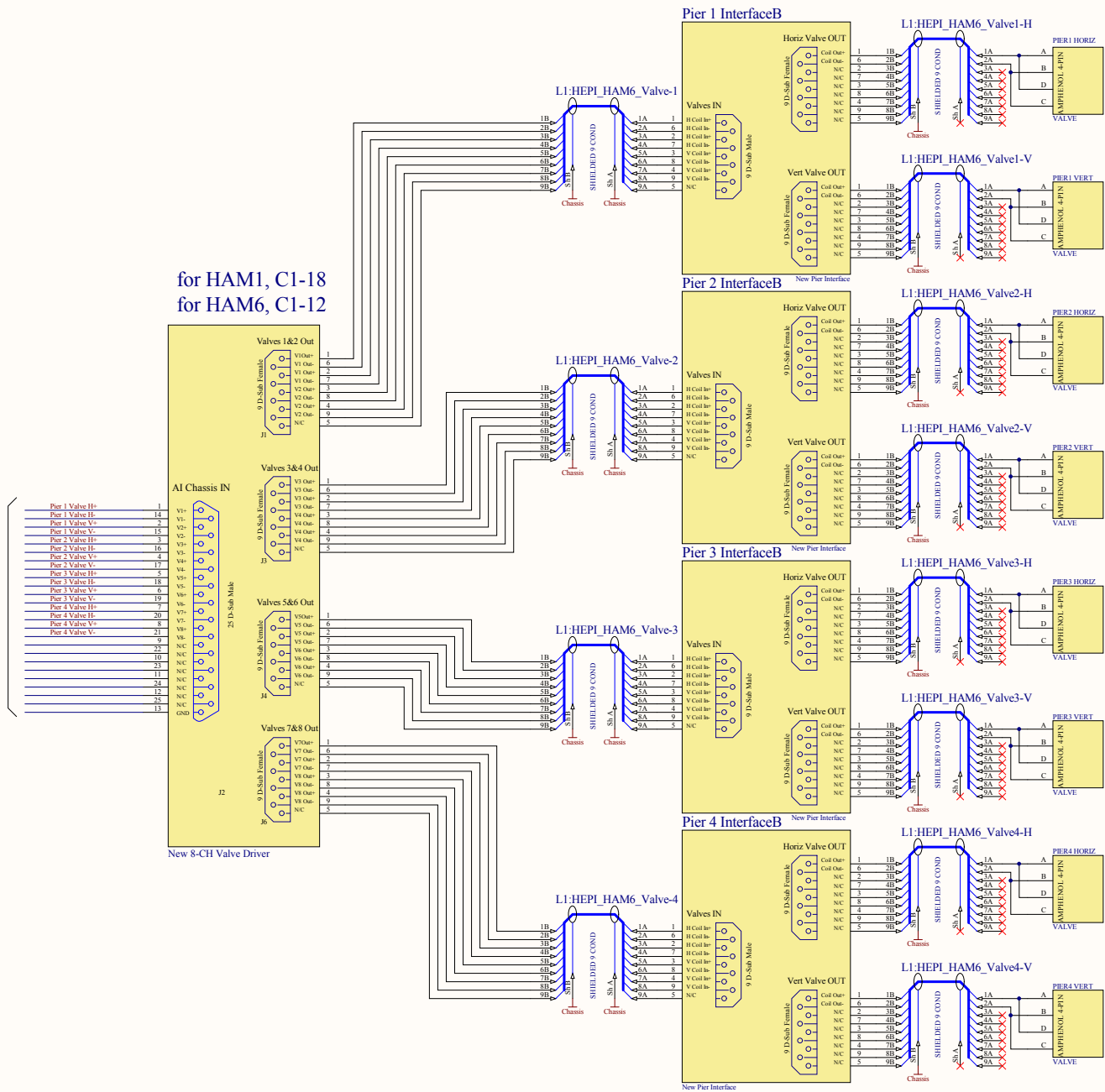
for HAM1, C1-20
for HAM6, C1-14



General Standards ADC
for HAM1, IO Chassis Slot 6
for HAM6, IO Chassis Slot 7

From AI Chassis
for HAM1, C1-19
for HAM6, C1-26
Page 4

for HAM1, C1-18
for HAM6, C1-12



New 8-CH Valve Driver

Title ALIGO HAMs 1&6 ISI System Schematics			
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Drawn by: Ben Abbott	Date: 8/30/2011	Revision: v3	
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