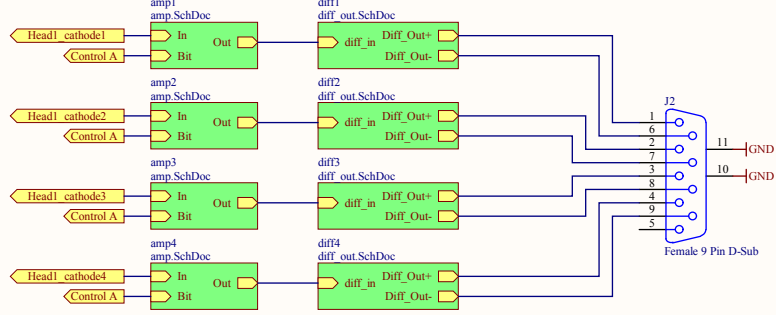
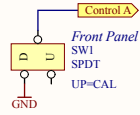
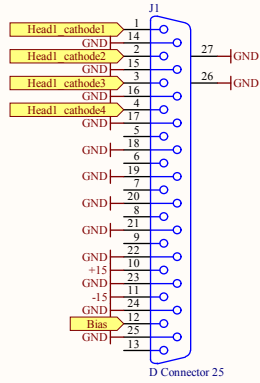
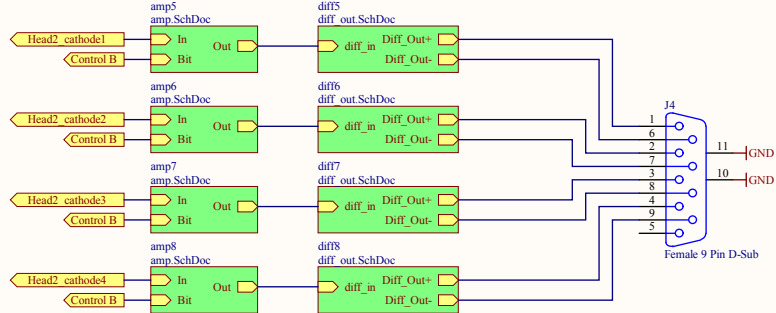
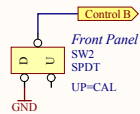
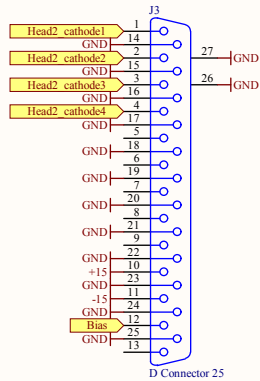


Front Panel Interface to Head 1

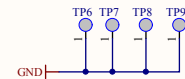
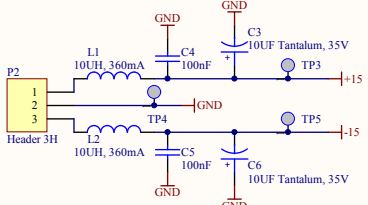
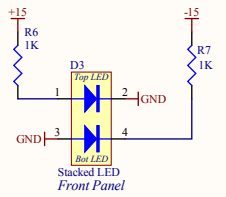
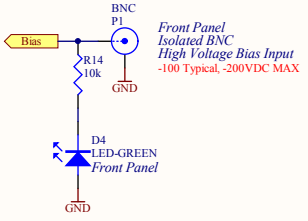


Rear Panel Analog Outputs

Front Panel Interface to Head 2

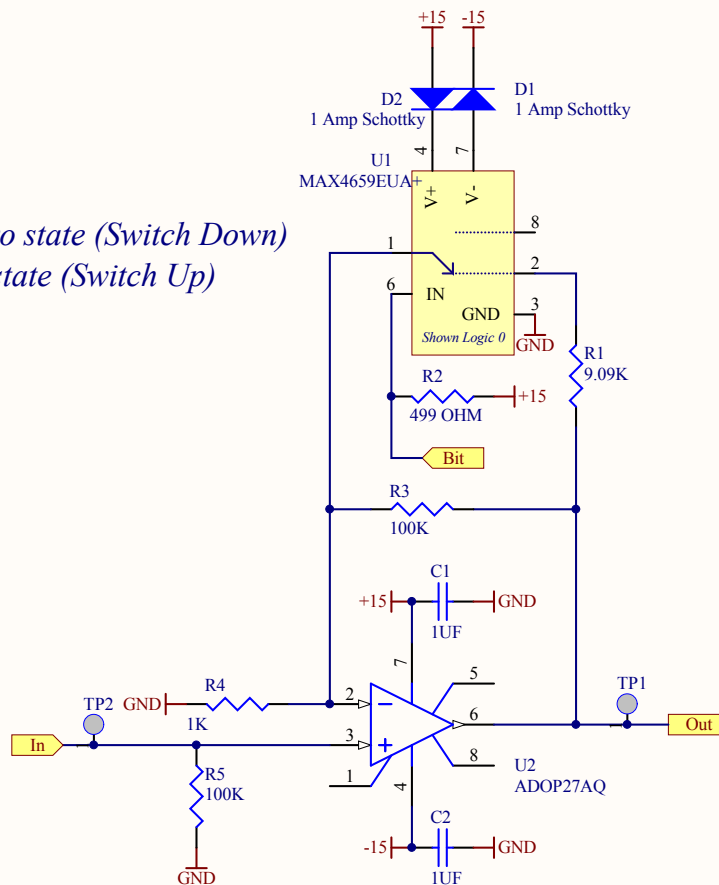


Rear Panel Analog Outputs




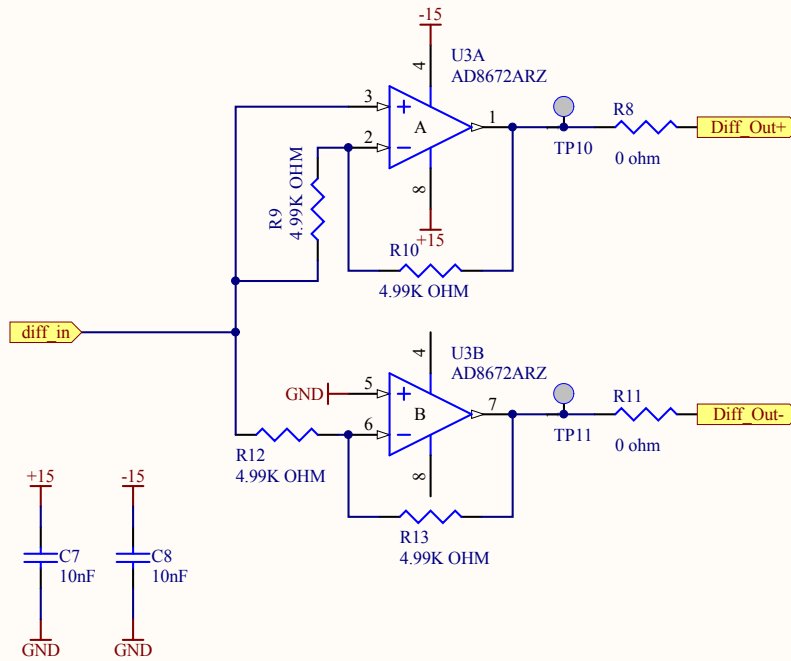
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Size: B	DCC Number: D1100546	Revision: v1	Engineer: R. Abbott	Date: 3/31/2011	Time: 6:27:07 PM
File: C:\Rich's Files\Myreadfiles\ISC\LegacyWFS\Interface\wfs_top.SchDoc					
Last Edited: 31 March 2011					

Gain = 10.09 when in logic zero state (Switch Down)
 Gain = 101 when in logic one state (Switch Up)



Last Edited: 31 March 2011

Title Amplifier Stage		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		 Date: 3/31/2011 Time: 6:27:07 PM
Size: A	DCC Number: D1100546	Revision: v1	Engineer: R. Abbott	
File: C:\Rich's Files\Mycadfiles\ISCLegacyWFSinterface\amp.SchDoc				Sheet 2 of 3



Last Edited: 31 March 2011

Title		<i>Differential Output Driver</i>		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: A	DCC Number: D1100546	Revision: v1	Engineer: R. Abbott	Date: 3/31/2011		Time: 6:27:07 PM	
File: C:\Rich's Files\Mycadfiles\ISCLegacyWFSinterface\diff_out.SchDoc				Sheet 3 of 3			

