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California Institute of Technology				DCN NO. E030020-00-C				
Massachusetts Institute of Technology Document Change Notice (DCN)				DAC	E 1	of.	2	
Document Change N	ouce	(DCN)		IAG		<u> </u>		
DOCUMENT No. (DOC-REV-GP.ID)		TITLE					NEW REV	
D000347-B1	Mode C	Cleaner Servo Boa	ard				B2	
CHANGE DESCRIPTION (FROM	/TO):							
 (1). Remove C44, C90, C92, C97, C98, C100, C101. (2). Remove R85, R150, R164, R165, R171, R173, R174, R175 R176, R177, R178, R180, R182. (3). Remove U38 (LT1125), JP2, JP10, The Elliptical Filter and Notch Filter Daughter Boards. (4). Change C52 to a jumper wire. (5). Change Cadd4 (3.3pF) to 10pF. (6). Change C24 (20nF) and C31 (20nF) to 10nF. (7). Change R94 (10KΩ) and R105 (3.16KΩ) to 1KΩ. (8). Change R149 (1.6KΩ), R163 (7.5KΩ), R179 (7.5KΩ), and R181 (7.5KΩ) to 100Ω. (9). Change R103 (1.59KΩ) to 1.58KΩ. (10).Change R125 (1KΩ), R127 (1KΩ), R126 (4KΩ), R109 (27KΩ), and R110 (79.6KΩ) to 2KΩ. (11).Change R172 (1KΩ) and R168 (500Ω) to 0.0 Ω. 								
REASON FOR CHANGE: E – Log changes.								
ACTION: x Incorporate Change								
DISPOSITION OF HARDWARE (IDENTIFY SERIAL NUMBERS)				DCN DISTRIBUTION				
□ No hardware was affected (record change only): Barish					Coles		Coyne	
List S/Ns which comply already: None Lazzarini Lindquist Shoemaker Stapfer					-	Sanders Tyler		
☐ List S/Ns to be reworked/scrapped: ALL Weiss Whitcomb						Matherny		
☐ List S/N's to be built with this change:								
☐ List S/Ns to be retested per this change: ALL								
SAFETY, COST, SCHEDULE, REQUIREMEN	NTS IMPACT	T? -X- NO ☐ YES	(If YES,	enter CR (C	CB) or T	CP (T	RB) #)	
APPROVALS:	DATE	OTHER APPROVALS	(SPECIF	Y)			DATE	
ORIGINATOR: Mohana Mageswaran	05-23-03							
TASK LEADER: Rich Abbott								
GROUP LEADER: Dennis Coyne								
DCC RELEASE:								



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CHANGE DESCRIPTION (FROM/TO):

- (12). Change U4 (LT1125), U5 (LT1125), U10 (LT1125), U14 (LT1125), U15 (LT1125), U17 (LT1125), U22 (LT1125), U24 (LT1125), U25 (LT1125), U34 (LT1125), U35 (LT1125) to LT1216.
- (13). Change U11 (LT1128) and U28 (LT1128) to AD829. And add a capacitor 27pF between pin 5 and ground.
- (14). Add a 10pF capacitor in parallel with R126.
- (15). Add a 20pF capacitor in parallel with R113.
- (16). Add a 33pF capacitor in parallel with R59.
- (17). Add 51pF capacitors in parallel with R5, R7, R27, R29, R34, R46, and R58.
- (18). Add 100pF capacitors in parallel with R77 and R81.
- (19). Connect JP8-2 to ground.
- (20). Elliptic Filter and Notch Filter Daughter Boards are not installed.
- (21). Set jumpers JP6 and JP3 to the 2-3 connected position.