



**California Institute of Technology  
Massachusetts Institute of Technology**

DCN No. E020653-00-C

# Document Change Notice (DCN)

Sheet 1 of 2

DOCUMENT No. (DOC-REV-GP.ID)	TITLE	NEW REV
D010285-A	Variable Delay Timing Board	A1

**CHANGE DESCRIPTION (FROM/TO):**

**FROM:**

- (1). The Vbb pins of U21 and U29 are tied together.
- (2). The unused inputs of the MC10H115 (U21) are not tied to its Vbb as stated in the data sheet.
- (3). The Vbb pins of U21 and U29 are not bypassed as per the data sheets.

**TO:**

- (1). Cut the trace that connects U21 pin 12 and U29 pin 8
- (2). Connect U21 pins 12, 13 and 17 together.

**REASON FOR CHANGE:** Didn't use the IC's U21 and U29 as per the Data sheets.

**ACTION:**       Incorporate Change       X Attach DCN to Drawings       Other Action (specify):

DISPOSITION OF HARDWARE (IDENTIFY SERIAL NUMBERS)	DCN DISTRIBUTION																					
<input type="checkbox"/> No hardware was affected (record change only):	<table border="0"> <tr> <td>Barish</td> <td>Coles</td> <td>Coyne</td> </tr> <tr> <td>Lazarini</td> <td>Lindquist</td> <td>Sanders</td> </tr> <tr> <td>Shoemaker</td> <td>Stapfer</td> <td>Tyler</td> </tr> <tr> <td>Weiss</td> <td>Whitcomb</td> <td>Matherny</td> </tr> <tr> <td>Raab</td> <td></td> <td></td> </tr> <tr> <td>McCarthy</td> <td>Wooley</td> <td>B. Abbott</td> </tr> <tr> <td>Fritschel</td> <td>Zucker</td> <td></td> </tr> </table>	Barish	Coles	Coyne	Lazarini	Lindquist	Sanders	Shoemaker	Stapfer	Tyler	Weiss	Whitcomb	Matherny	Raab			McCarthy	Wooley	B. Abbott	Fritschel	Zucker	
Barish		Coles	Coyne																			
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McCarthy	Wooley	B. Abbott																				
Fritschel	Zucker																					
<input type="checkbox"/> List S/Ns which comply already: S/N: 131 to 140																						
<input type="checkbox"/> List S/Ns to be reworked/scrapped: All Rev. A S/Ns should be reworked																						
<input type="checkbox"/> List S/N's to be built with this change: All future																						
<input type="checkbox"/> List S/Ns to be retested per this change: All																						
<input type="checkbox"/> Some boards were shipped prior to the change.																						

**SAFETY, COST, SCHEDULE, REQUIREMENTS IMPACT?**      -X- NO       YES (If YES, enter CR (CCB) or TCP (TRB) #)

APPROVALS:	DATE	OTHER APPROVALS (SPECIFY)	DATE
<b>ORIGINATOR:</b> Mohana Mageswaran	10-23-02		
<b>TASK LEADER:</b> Rich Abbott	10-23-02		
<b>GROUP LEADER:</b> Dennis Coyne	11-7-02		

**DCC RELEASE:**

CHANGE DESCRIPTION (FROM/TO):

(3). Solder a 0.1 uF SMT capacitor between U21-12, U29-8 and ground.

(See the Fig 1. for the PCB Board change)

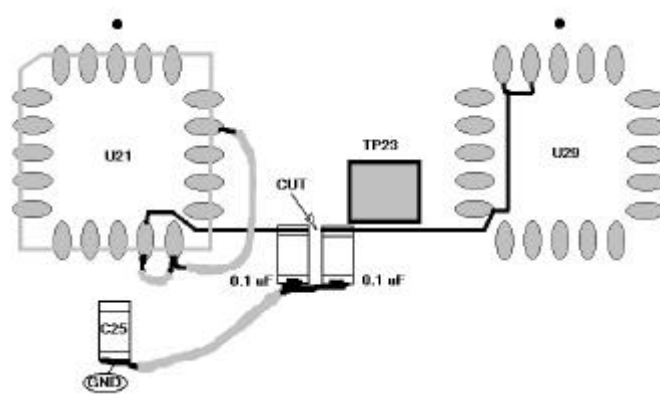


Fig 1: The PCB Board (D010285-A) change to D010285-A1