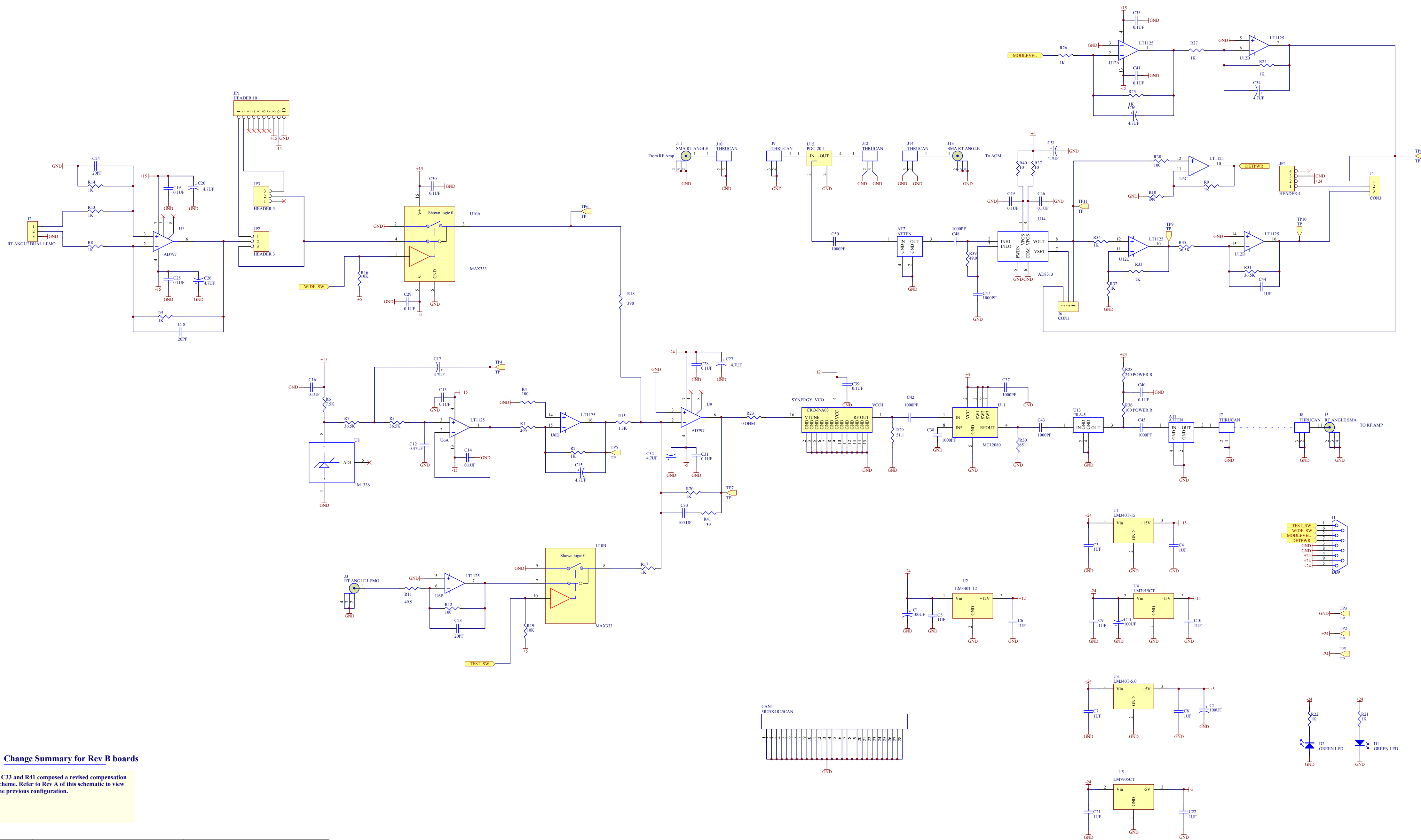


PSL - Frequency Stabilization

80 MHz VCO Rev. B

Engineering Change Order History		
ECO number	Date	Originator
1		
2		
3		
4		
5		



Change Summary for Rev B boards

* C33 and R41 composed a revised compensation scheme. Refer to Rev A of this schematic to view the previous configuration.

REV	DATE	DRAWN BY	CHECKED BY	DCC	DCN / DESCRIPTION
B					E010167-00-C