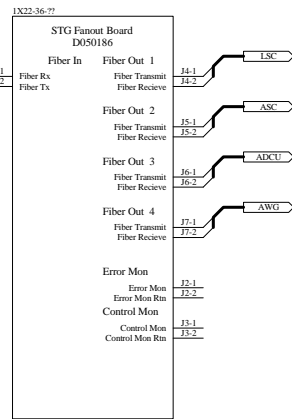
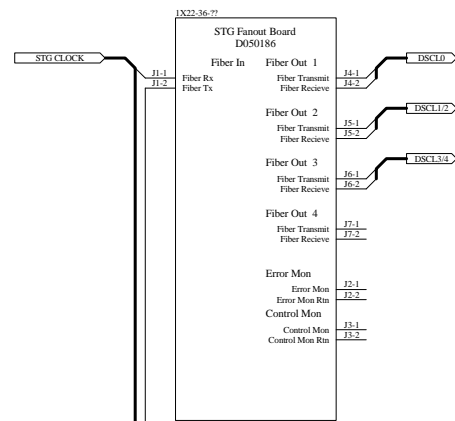


Rev.	Description	Date
00	Initial release of drawing.	10/7/1999
A	Updated to as-built before fiber based timing system installation.	10/2/2006
B	Updated to as-built after fiber based timing system installation.	10/2/2006

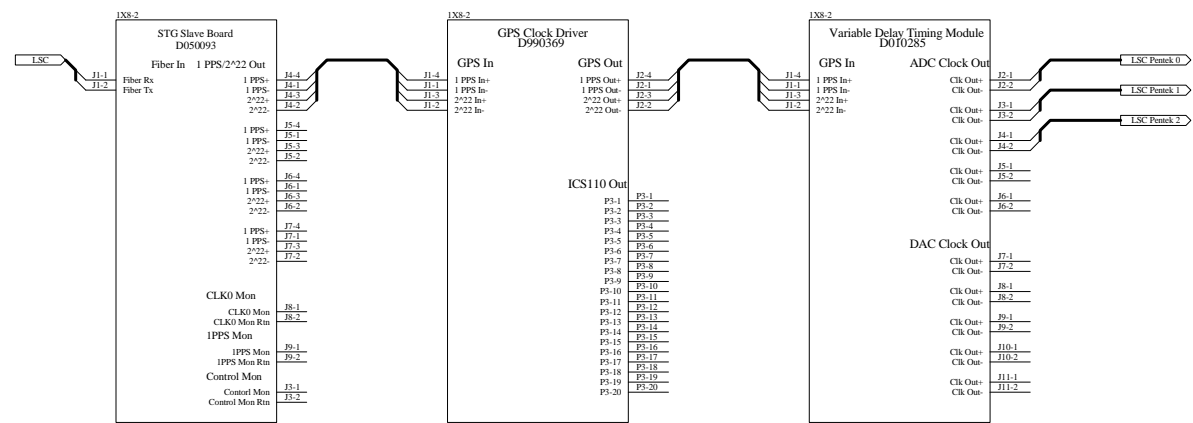
  

Title <b>Hanford Timing System Cable Diagram</b>		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology LIGO	
Size: D DCC Number: D980354-B-C SCL: PCB Revision: B Date: 10/2/2006 Time: 7:46:33 AM	Drawn by: R. Myers Checked by: R. Myers	File: C:\Documents and Settings\jmyers\My Documents\LIGO\Research\SystemTiming\SystemRev_B_Overview_SchDoc Sheet 1 of 8	

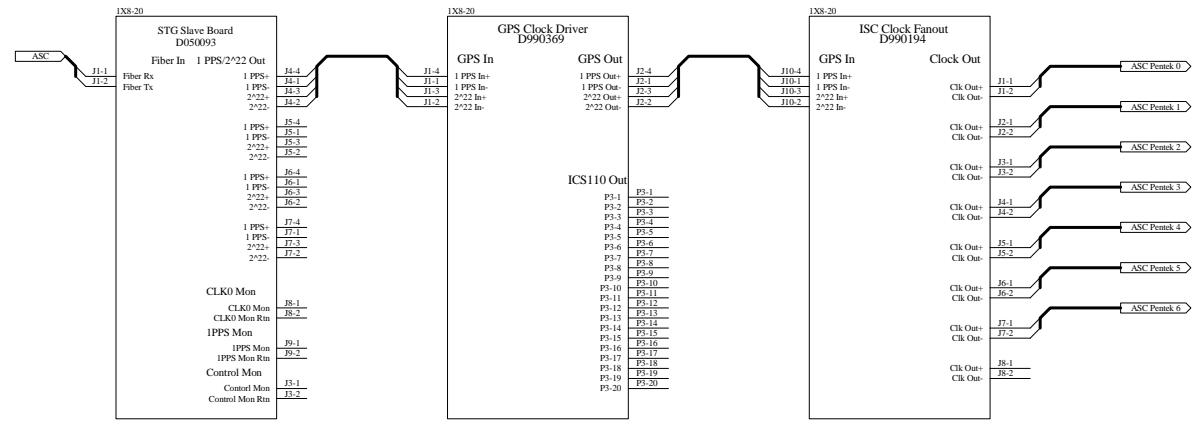




# LVEA Fanout

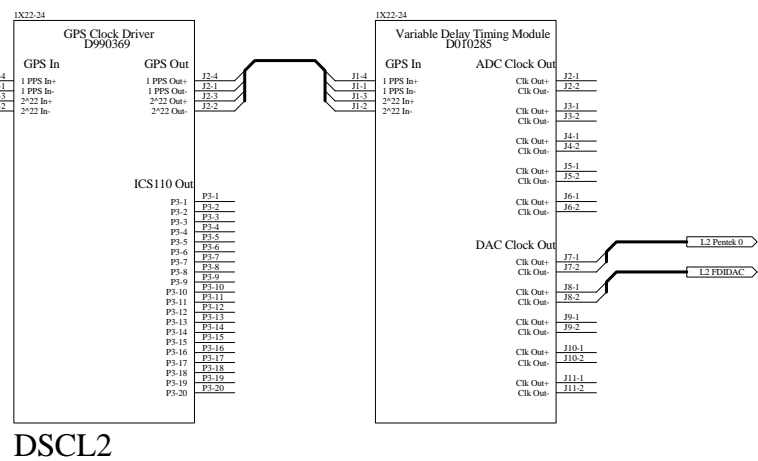
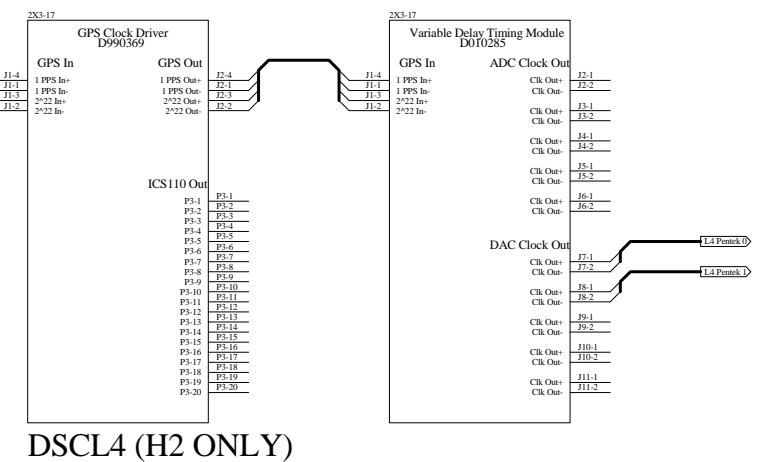
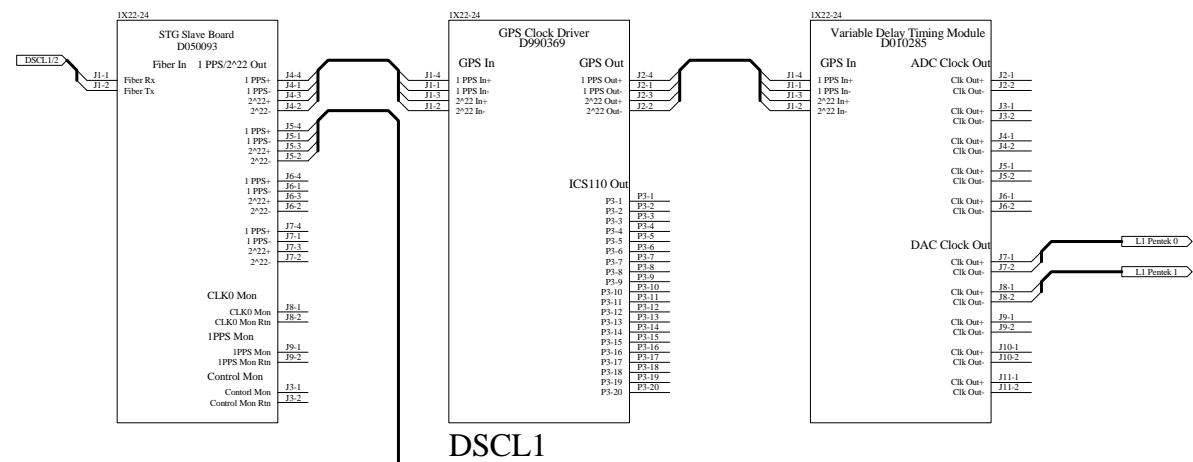
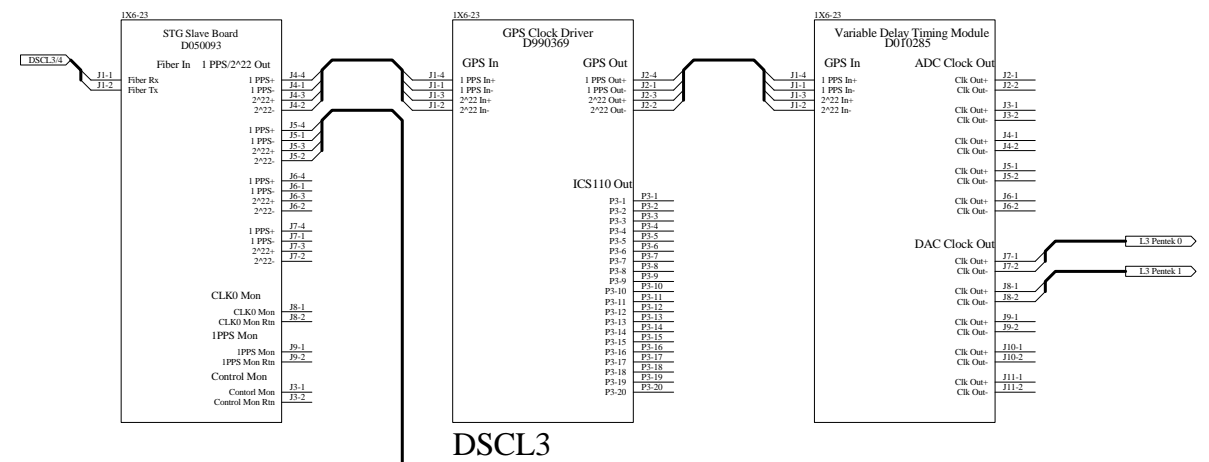
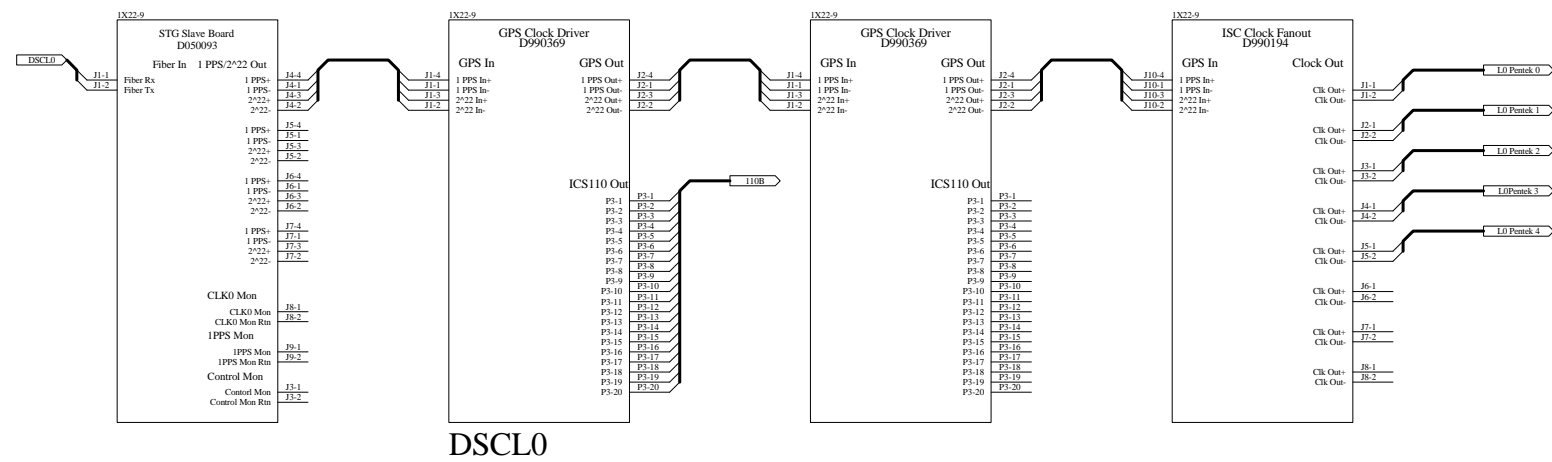


LSC

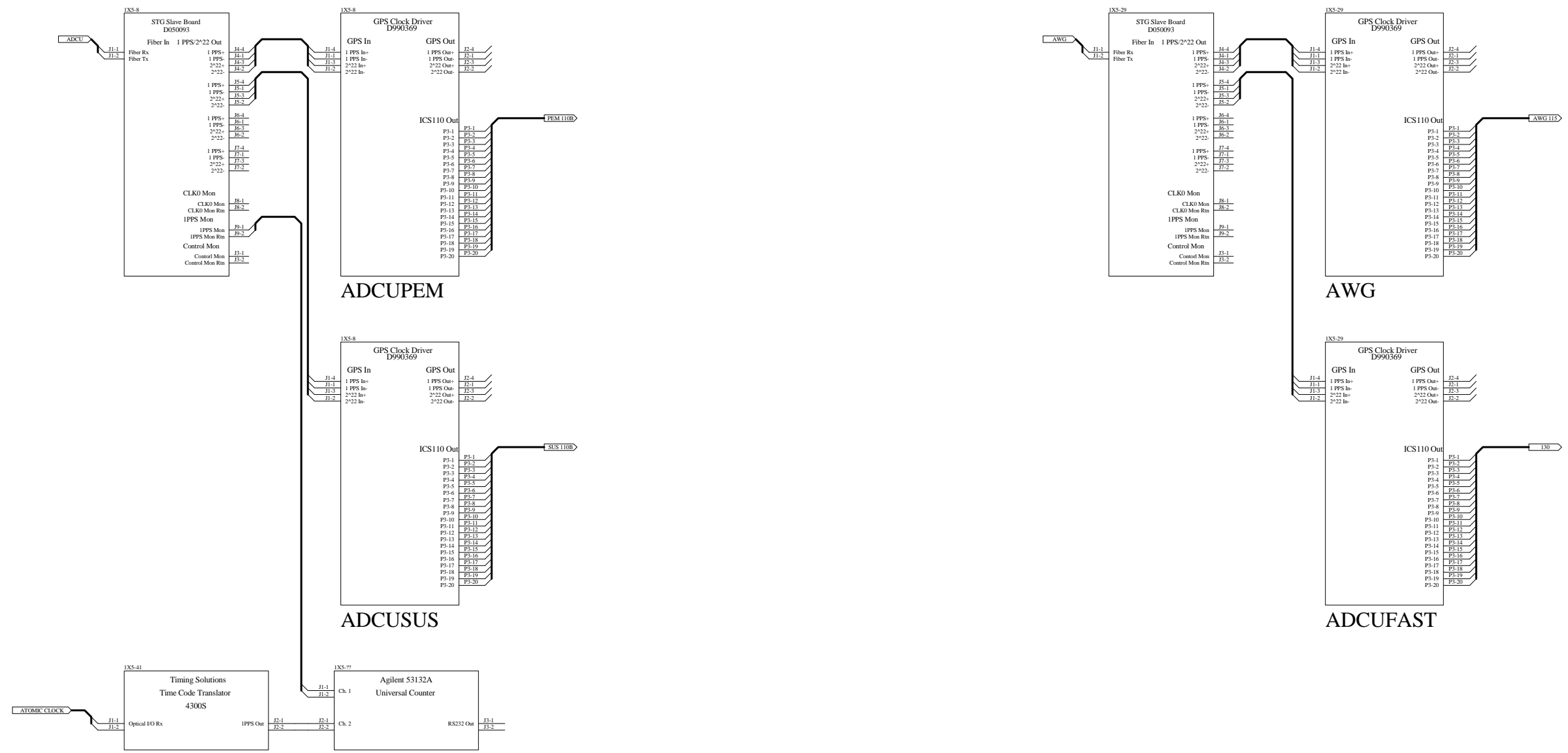


ASC

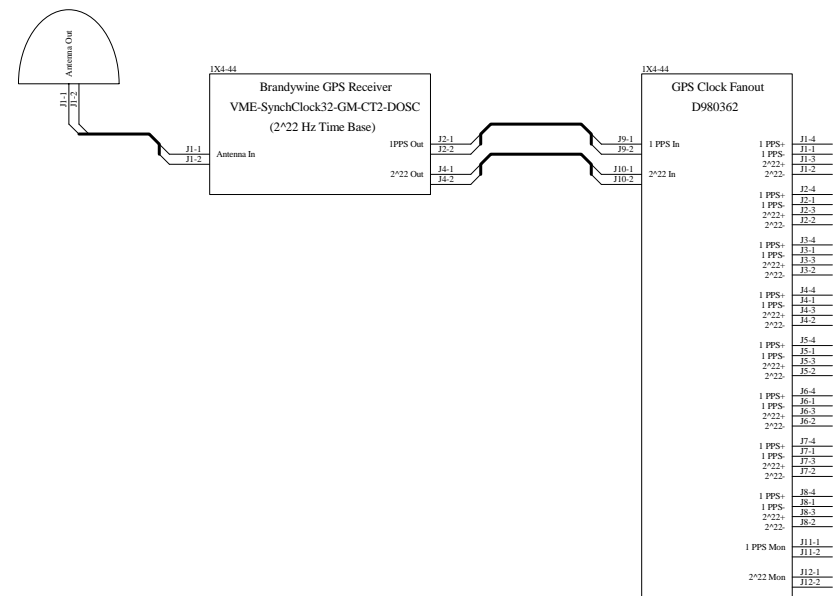
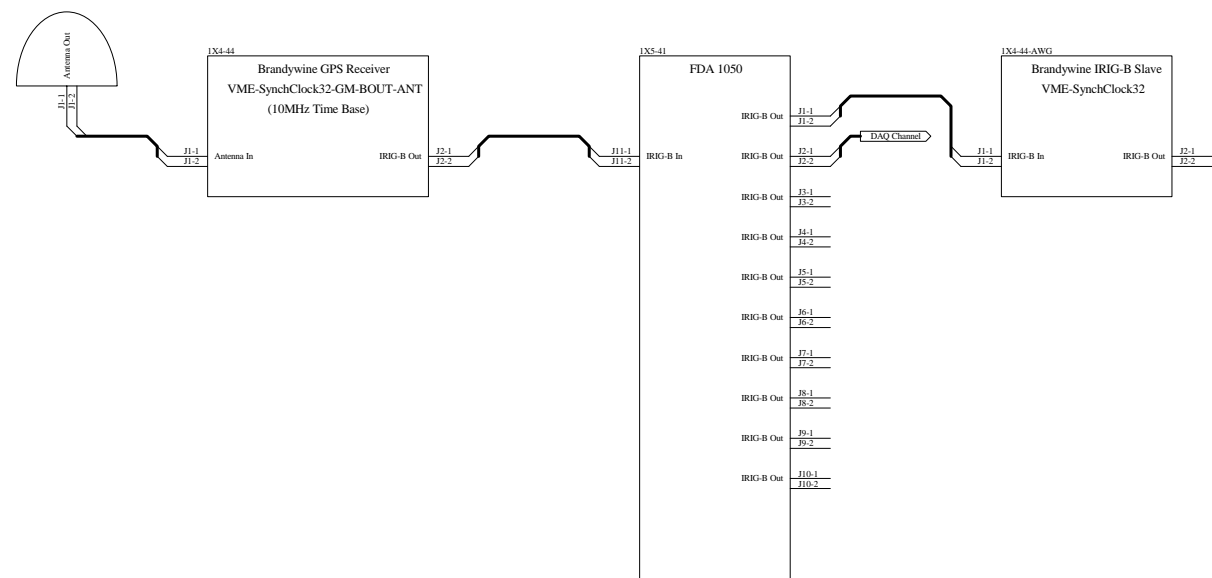
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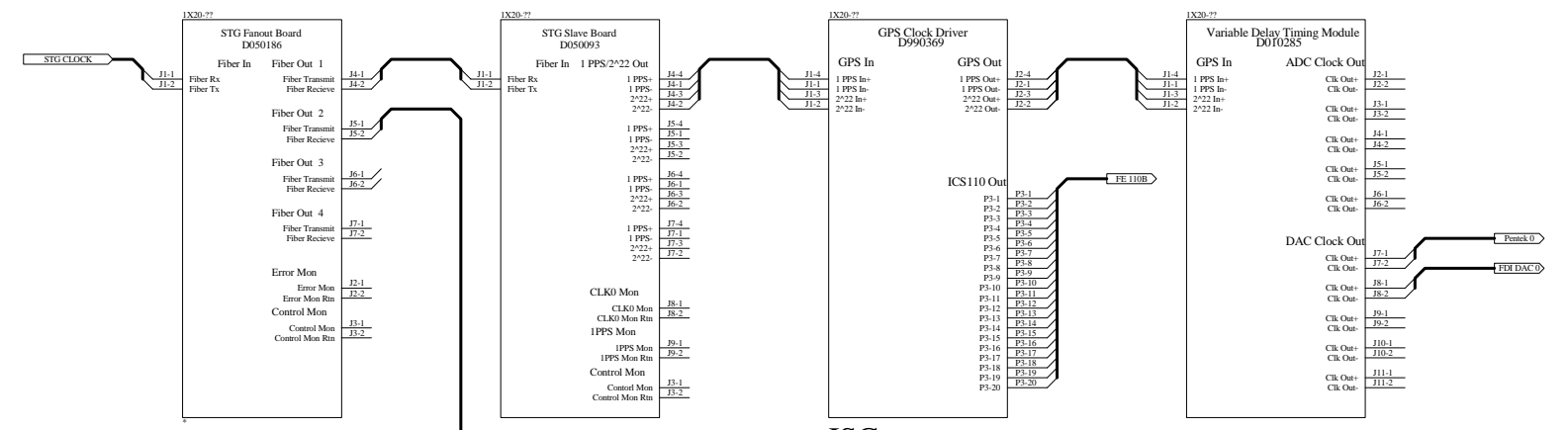
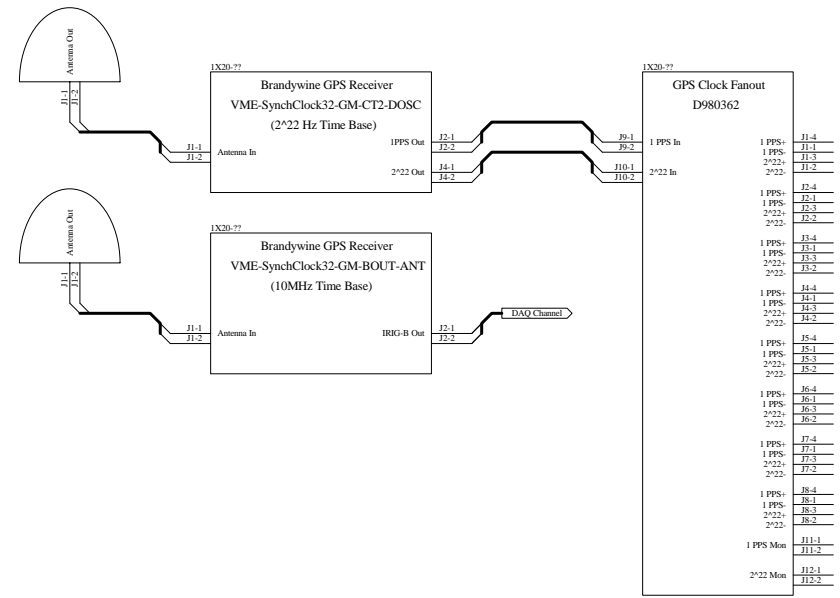
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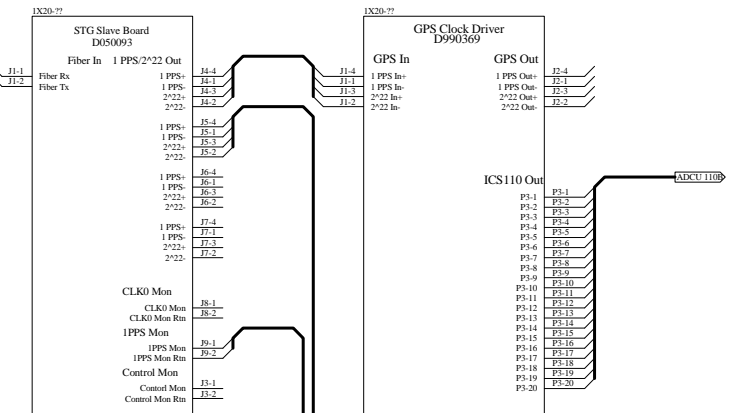
# LVEA ADCU



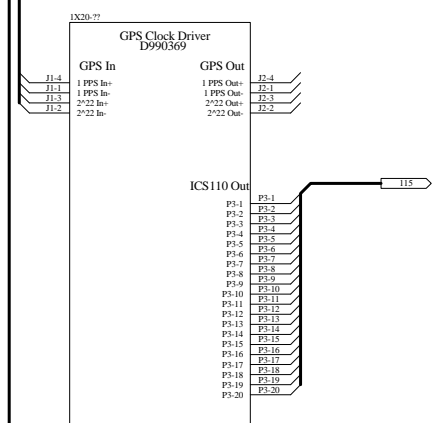
# LVEA GPS



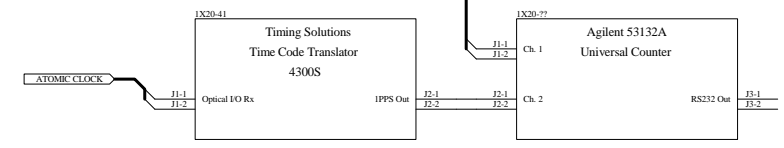
ISC



ADCU



AWG



# Outbuildings