

ASX-1000 NIM/Channel Assignments (NIM 1 thru 4)

ASX-1000		Destination		
NIM-Chan.	Fibre Type	Bldg.	Rack	Device
1-1	SM	Mid X	1X18	ES-3810
1-2	MM	LVEA	1X22	ES-3810
1-4	MM			
2-2	MM	LVEA	2X3	ES-3810
2-4	MM			
3-1	SM	End X	1X21	ES-3810
3-2	MM	OSB		DAQ Svr 1
3-3	MM	OSB		DAQ Svr 2
3-4	MM	OSB		CDS Svr 1
4-2	MM	OSB		CDS Svr 2
4-3	MM	OSB		CDS FWall
4-4	MM	OSB	MSR-2	ES-3810

ASX-1000 NIM/Channel Assignments (NIM 5 thru 8)

ASX-1000		Destination		
NIM-Chan.	Fibre Type	Bldg.	Rack	Device
5-1	SM	Mid Y	1Y19	ES-3810
5-2	MM	OSB		FB-1
5-3	MM	OSB		FB-2
5-4	MM			
6-2	MM			
6-3	MM			
6-4	MM	OSB	Console 7	UltraSparc
7-1	SM	End Y	1Y22	ES-3810
7-2	MM	OSB	Console 1	UltraSparc
7-3	MM	OSB	Console 2	UltraSparc
7-4	MM	OSB	Console 3	UltraSparc
8-2	MM	OSB	Console 4	UltraSparc
8-3	MM	OSB	Console 5	UltraSparc
8-4	MM	OSB	Console 6	UltraSparc

Notes:

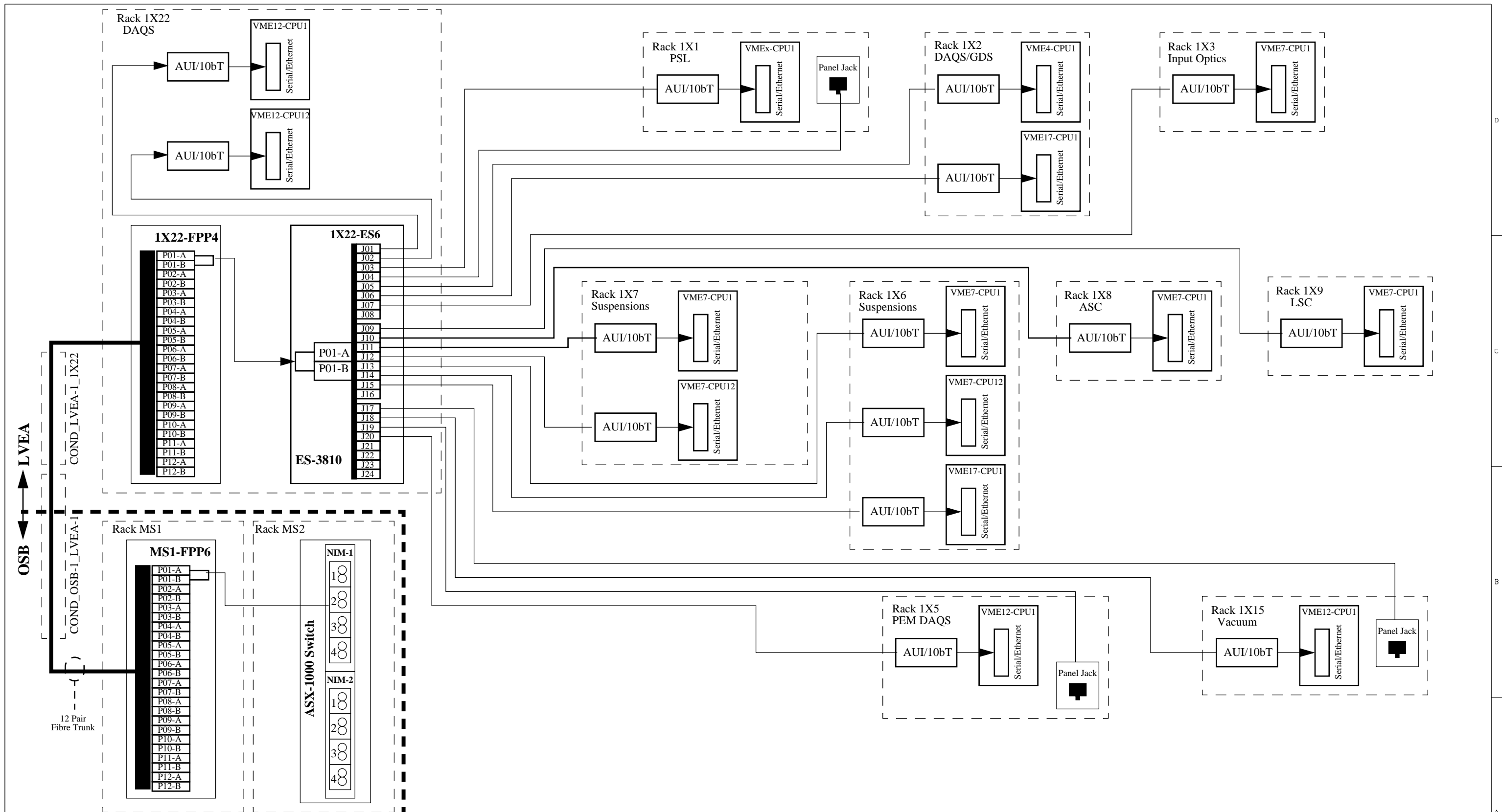
- 1) Equipment to be housed in rack 1MS2 (in Mass Storage Room), adjacent to Fibre Optic Trunk Rack 1MS1.
- 2) ES-3810 provides 24 switched ethernet ports to CDS computers in OSB. Port assignments are TBD.

UNLESS OTHERWISE SPECIFIED
 DIMENSIONS ARE IN INCHES
 TOLERANCES:
 FRACTIONAL ±
 ANGULAR MAX ± BEND ±
 TWO PLACE DECIMAL ±
 THREE PLACE DECIMAL ±
 FINISHED SURFACE RMS
 BREAK CORNERS IN OUT.
 REMOVE ALL BURRS

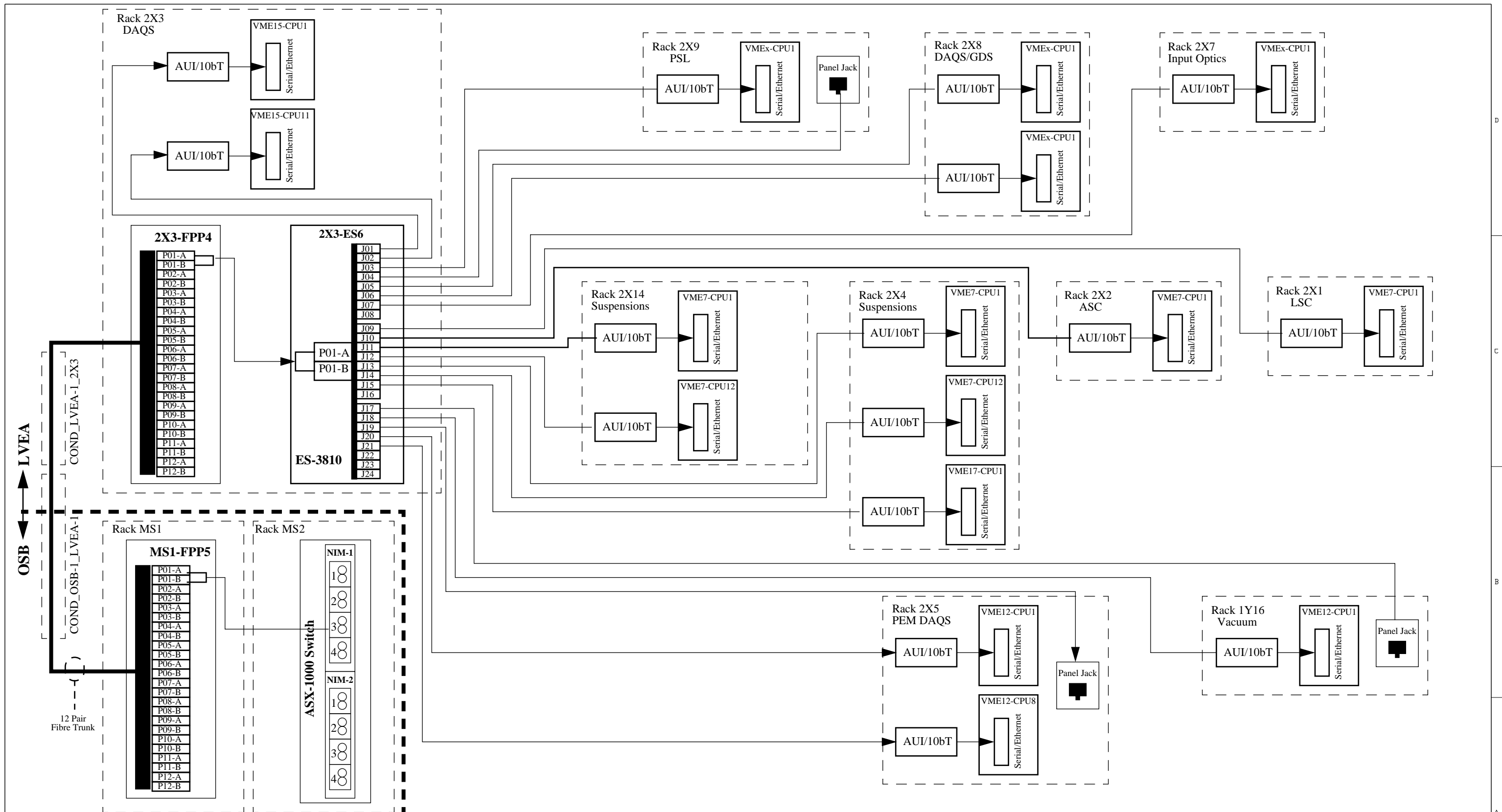
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LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY
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 Hanford Control & Monitoring Network
 Main Networking Rack / ATM Connections)

DWG. NO.	DESCRIPTION	DWG. NO.	DESCRIPTION	USED ON:	REV.	DESCRIPTION	SHEETS EFFECTED	DATE	SCALE	SIZE	DWG. NO.	REV.
				NEXT ASS'Y:		ISSUE DESCRIPTION			DEC	B	D970583-01-C	



				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR/MACH ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS ON OUT. REMOVE ALL BURRS				CURRENT REVISION APPROVAL			
				DRAWN R. Bork CHECKED				GROUP SIGNATURE DATE			
				DO NOT SCALE THIS DRAWING				SCALE			
DWG. NO.				DESCRIPTION				SHEETS EFFECTED DATE			
REFERENCE DRAWINGS				NEXT ASS'Y				ISSUE DESCRIPTION			
6				5				4			
								3			
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<p style="text-align: center;">LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY</p> <p style="text-align: center;">Hanford Control & Monitoring Network LVEA Sector 1</p> <p style="text-align: center;">SCALE: B DWG. NO. D970583-01-C REV. 3 of 5</p>											



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 TOLERANCES:
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 ANGULAR/MACH ± BEND ±
 TWO PLACE DECIMAL ±
 THREE PLACE DECIMAL ±
 FINISHED SURFACE RMS
 BREAK CORNERS ON OUT.
 REMOVE ALL BURRS

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Hanford Control & Monitoring Network
LVEA Sector 2

SCALE: _____ SHEET: **D970583-01-C** REV: **0**

DWG. NO.	DESCRIPTION	DWG. NO.	DESCRIPTION	REV.	DESCRIPTION	SHEETS EFFECTED	DATE
6	REFERENCE DRAWINGS	5					

Ethernet Switch 1X22-ES6 Connection Assignments: (LVEA Sector 1)

ES-3810 Connection	Cable Number	To Device
J1	CAB_1X22-ES6-1_1X22_VME15-CPU1	DAQS Processor
J2	CAB_1X22-ES6-2_1X22_VME15-CPU12	DAQS Processor
J3	CAB_1X22-ES6-3_1X1_VMExx-CPU1	PSL Processor
J4	CAB_1X22-ES6-4_1X1-EPJ1	Panel Jack
J5	CAB_1X22-ES6-5_1X2_VME5-CPU1	DAQS Processor
J6	CAB_1X22-ES6-6_1X2_VME15-CPU1	GDS Processor
J7	CAB_1X22-ES6-7_1X3_VME7-CPU1	IO Processor
J8		
J9	CAB_1X22-ES6-9_1X9_VME7-CPU1	LSC Processor
J10	CAB_1X22-ES6-10_1X8_VME7-CPU1	ASC Processor
J11	CAB_1X22-ES6-11_1X7_VME7-CPU1	Suspension Processor
J12	CAB_1X22-ES6-12_1X7_VME7-CPU12	Suspension Processor
J13	CAB_1X22-ES6-13_1X6_VME7-CPU1	Suspension Processor
J14	CAB_1X22-ES6-14_1X6_VME7-CPU12	Suspension Processor
J15	CAB_1X22-ES6-15_1X6_VME17-CPU1	Suspension Processor
J16		
J17	CAB_1X22-ES6-17_1X15-EPJ1	Panel Jack
J18	CAB_1X22-ES6-18_1X15-VME7-CPU1	VCMS Processor
J19	CAB_1X22-ES6-19_1X5-EPJ1	Panel Jack
J20	CAB_1X22-ES6-20_1X5_VME12-CPU1	PEM Processor
J21		
J22		
J23		
J24		

Ethernet Switch 2X3-ES6 Connection Assignments: (LVEA Sector 2)

ES-3810 Connection	Cable Number	To Device
J1	CAB_2X3-ES6-1_2X3_VME15-CPU1	DAQS Processor
J2	CAB_2X3-ES6-2_2X3_VME15-CPU12	DAQS Processor
J3	CAB_2X3-ES6-3_2X9_VMExx-CPU1	PSL Processor
J4	CAB_2X3-ES6-4_2X9-EPJ1	Panel Jack
J5	CAB_2X3-ES6-5_2X8_VME5-CPU1	DAQS Processor
J6	CAB_2X3-ES6-6_2X8_VME15-CPU1	GDS Processor
J7	CAB_2X3-ES6-7_2X7_VME7-CPU1	IO Processor
J8		
J9	CAB_2X3-ES6-9_2X1_VME7-CPU1	LSC Processor
J10	CAB_2X3-ES6-10_2X2_VME7-CPU1	ASC Processor
J11	CAB_2X3-ES6-11_2X14_VME7-CPU1	Suspension Processor
J12	CAB_2X3-ES6-12_2X14_VME7-CPU12	Suspension Processor
J13	CAB_2X3-ES6-13_2X4_VME7-CPU1	Suspension Processor
J14	CAB_2X3-ES6-14_2X4_VME7-CPU12	Suspension Processor
J15	CAB_2X3-ES6-15_2X4_VME17-CPU1	Suspension Processor
J16		
J17	CAB_2X3-ES6-17_1Y16-EPJ1	Panel Jack
J18	CAB_2X3-ES6-18_1Y16-VME7-CPU1	VCMS Processor
J19	CAB_2X3-ES6-19_2X5-EPJ1	Panel Jack
J20	CAB_2X3-ES6-20_2X5_VME12-CPU1	PEM Processor
J21		
J22		
J23		
J24		

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 FRACTIONAL ±
 ANGULAR MACH ± BEND ±
 TWO PLACE DECIMAL ±
 THREE PLACE DECIMAL ±
 FINISHED SURFACE RMS
 BREAK CORNERS IN OUT.
 REMOVE ALL BURRS

DO NOT SCALE THIS DRAWING
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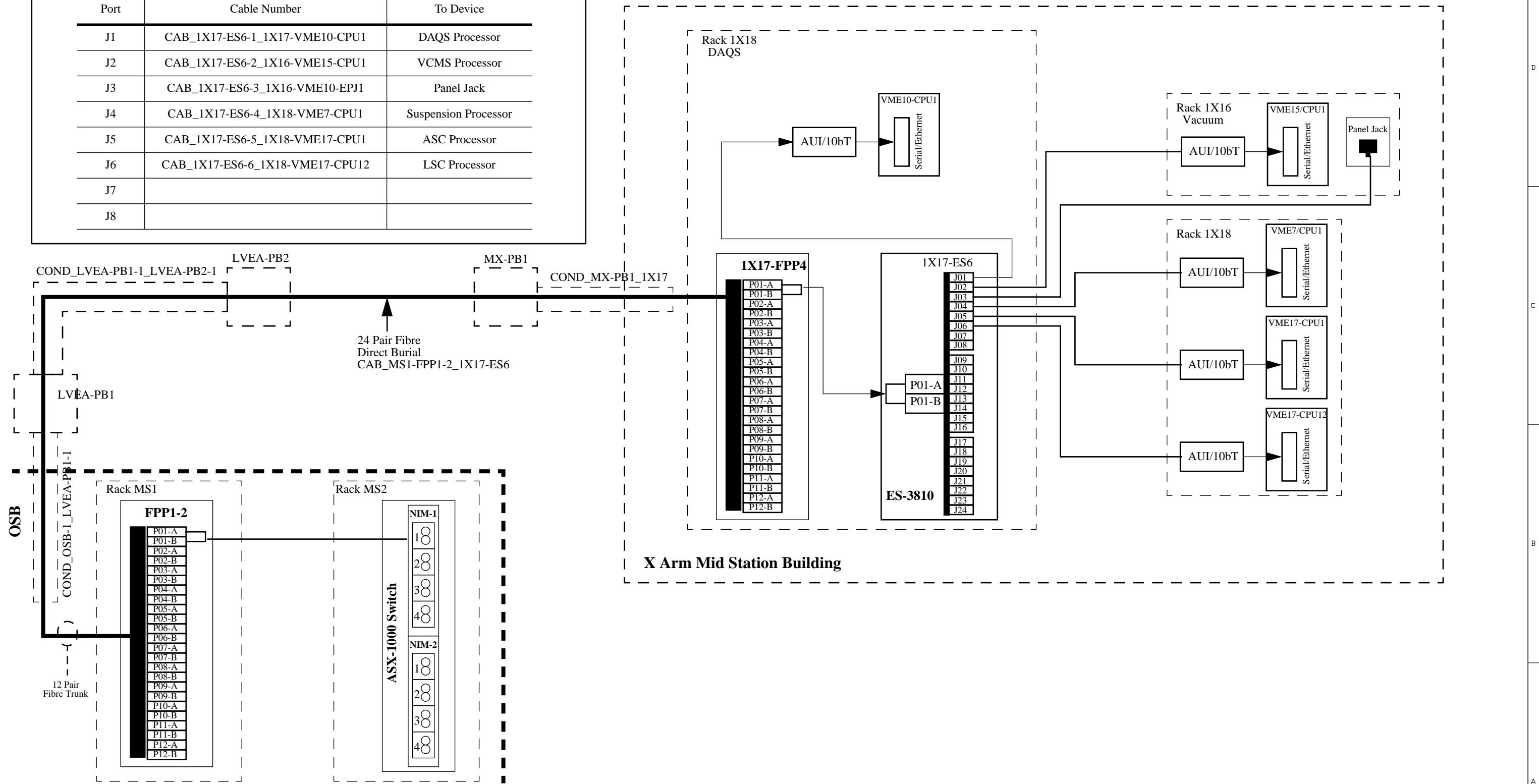
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Hanford Control & Monitoring Network
 LVEA Cable Assignments

SCALE SIZE DWG. NO. REV.
 B D970583-01-C
 CAB FILE SCALE SHEETS OF 9 STD VER. DT

ES-3810 (X Arm Mid Station) Connection Assignments:

Port	Cable Number	To Device
J1	CAB_1X17-ES6-1_1X17-VME10-CPU1	DAQS Processor
J2	CAB_1X17-ES6-2_1X16-VME15-CPU1	VCMS Processor
J3	CAB_1X17-ES6-3_1X16-VME10-EPJ1	Panel Jack
J4	CAB_1X17-ES6-4_1X18-VME7-CPU1	Suspension Processor
J5	CAB_1X17-ES6-5_1X18-VME17-CPU1	ASC Processor
J6	CAB_1X17-ES6-6_1X18-VME17-CPU12	LSC Processor
J7		
J8		



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 ANGULARMACH ± BEND ±
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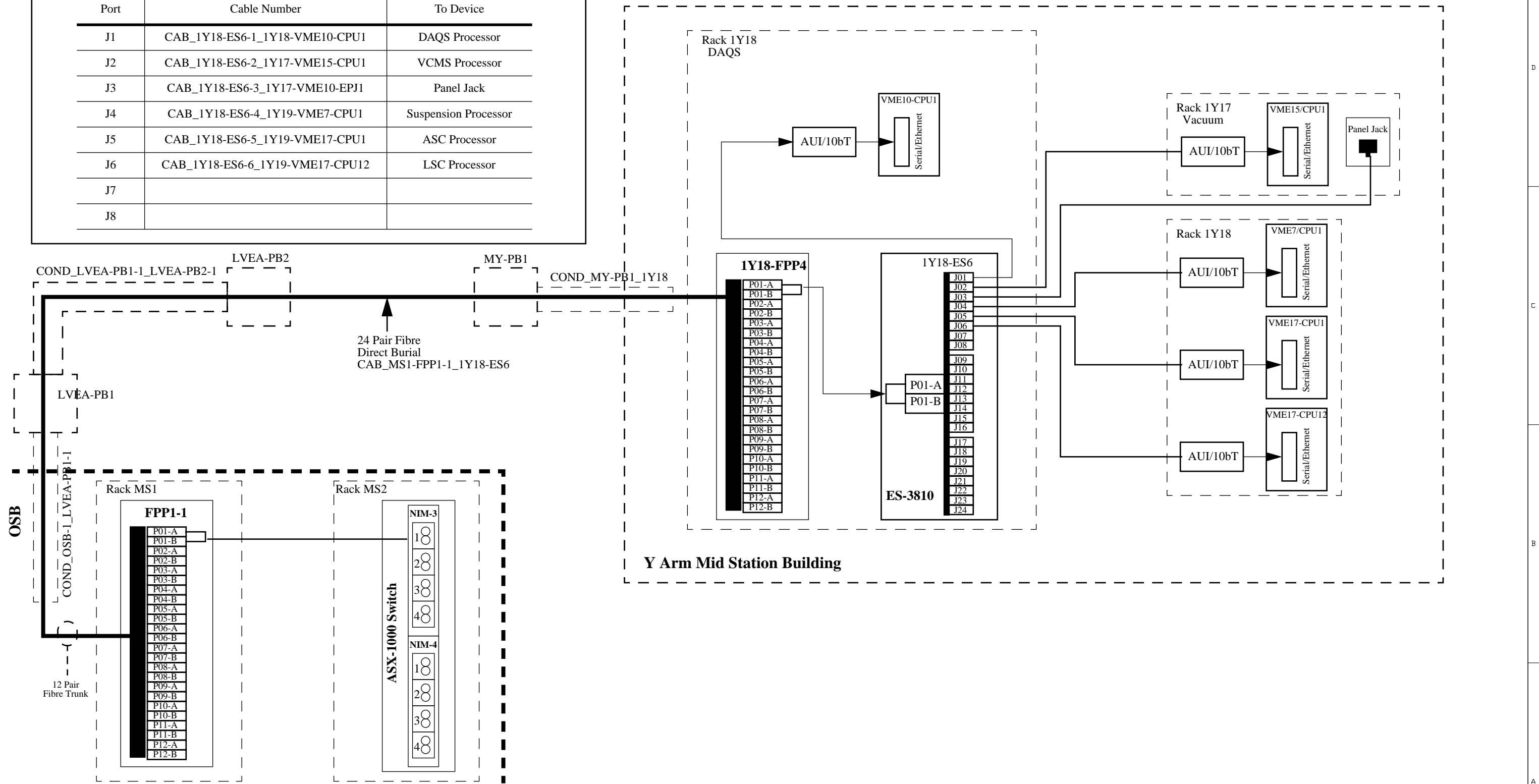
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REV	DESCRIPTION	SHEETS EFFECTED	DATE

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Hanford Control & Monitoring Network
X Arm Mid Station

DWG. NO.	DESCRIPTION	DWG. NO.	DESCRIPTION	USED ON:	REV	DESCRIPTION	SHEETS EFFECTED	DATE	SCALE	SIZE	DWG. NO.	REV.
6	REFERENCE DRAWINGS	5		NEXT ASS'Y		ISSUE DESCRIPTION			DEC	B	D970583-01-C	6 of 9

ES-3810 (Y Arm Mid Station) Connection Assignments:

Port	Cable Number	To Device
J1	CAB_1Y18-ES6-1_1Y18-VME10-CPU1	DAQS Processor
J2	CAB_1Y18-ES6-2_1Y17-VME15-CPU1	VCMS Processor
J3	CAB_1Y18-ES6-3_1Y17-VME10-EPJ1	Panel Jack
J4	CAB_1Y18-ES6-4_1Y19-VME7-CPU1	Suspension Processor
J5	CAB_1Y18-ES6-5_1Y19-VME17-CPU1	ASC Processor
J6	CAB_1Y18-ES6-6_1Y19-VME17-CPU12	LSC Processor
J7		
J8		



Y Arm Mid Station Building

UNLESS OTHERWISE SPECIFIED
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 ANGULAR/MACH ± BEND ±
 TWO PLACE DECIMAL ±
 THREE PLACE DECIMAL ±
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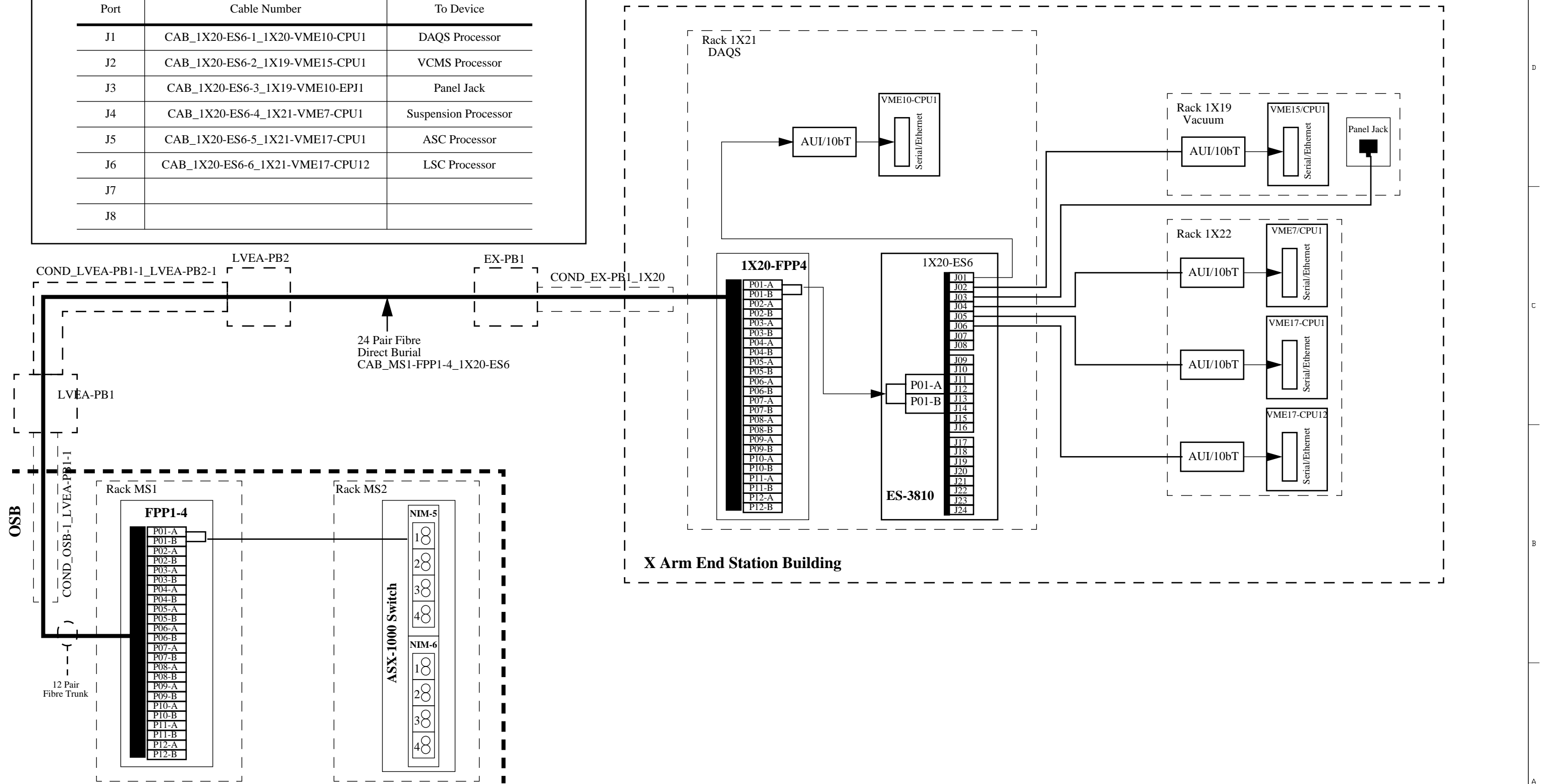
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CURRENT REVISION APPROVAL			
DRAWN	GROUP	SIGNATURE	DATE
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LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY			
Hanford Control & Monitoring Network Y Arm Mid Station			
SCALE	SIZE	DWG. NO.	REV.
	B	D970583-01-C	
CAD FILE	SCALE	SHEET	VER.
		7 of 9	01

ES-3810 (X Arm End Station) Connection Assignments:

Port	Cable Number	To Device
J1	CAB_1X20-ES6-1_1X20-VME10-CPU1	DAQS Processor
J2	CAB_1X20-ES6-2_1X19-VME15-CPU1	VCMS Processor
J3	CAB_1X20-ES6-3_1X19-VME10-EPJ1	Panel Jack
J4	CAB_1X20-ES6-4_1X21-VME7-CPU1	Suspension Processor
J5	CAB_1X20-ES6-5_1X21-VME17-CPU1	ASC Processor
J6	CAB_1X20-ES6-6_1X21-VME17-CPU12	LSC Processor
J7		
J8		



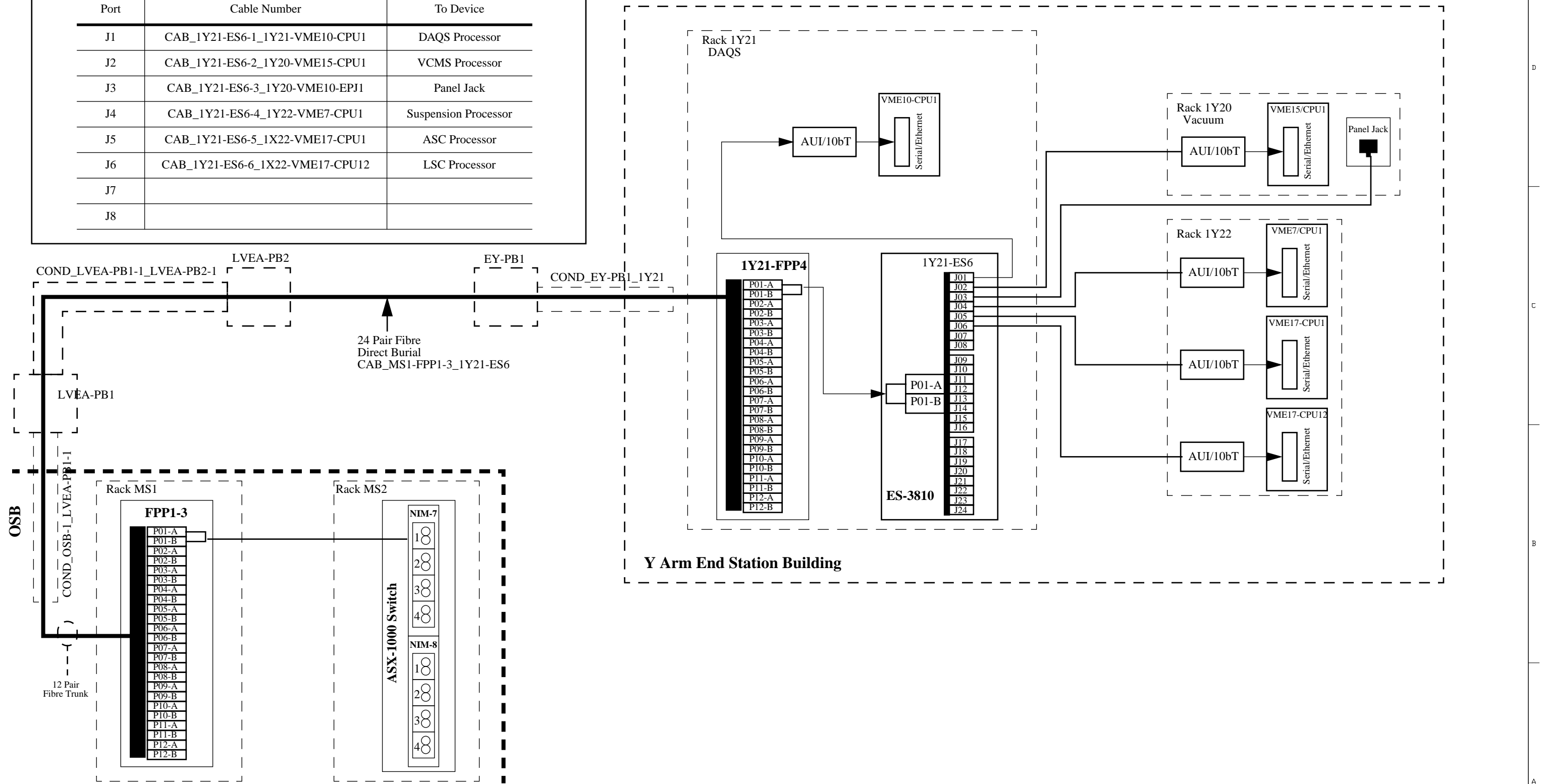
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 THREE PLACE DECIMAL ±
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REV	DESCRIPTION	SHEETS EFFECTED	DATE	CURRENT REVISION APPROVAL	
				DRAWN R. Bork	CHECKED
				SIGNATURE	DATE

LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY			
Hanford Control & Monitoring Network X Arm End Station			
SCALE	SIZE DWG. NO. B D970583-01-C	SHEET	REV.
DWG. NO.	DESCRIPTION	8 of 9	STD

ES-3810 (X Arm Mid Station) Connection Assignments:

Port	Cable Number	To Device
J1	CAB_1Y21-ES6-1_1Y21-VME10-CPU1	DAQS Processor
J2	CAB_1Y21-ES6-2_1Y20-VME15-CPU1	VCMS Processor
J3	CAB_1Y21-ES6-3_1Y20-VME10-EPJ1	Panel Jack
J4	CAB_1Y21-ES6-4_1Y22-VME7-CPU1	Suspension Processor
J5	CAB_1Y21-ES6-5_1X22-VME17-CPU1	ASC Processor
J6	CAB_1Y21-ES6-6_1X22-VME17-CPU12	LSC Processor
J7		
J8		



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CURRENT REVISION APPROVAL			
REV	DESCRIPTION	SHEETS EFFECTED	DATE

LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY			
Hanford Control & Monitoring Network Y Arm End Station			
SCALE	SIZE	DWG. NO.	REV.
	B	D970583-01-C	
CAB FILE	SCALE	SHEET	VER.
		9 of 9	01

DWG. NO.	DESCRIPTION	DWG. NO.	DESCRIPTION	USED ON:	REV.	DESCRIPTION	SHEETS EFFECTED	DATE	DEC	SCALE	STD
6	REFERENCE DRAWINGS	5		NEXT ASS'Y							