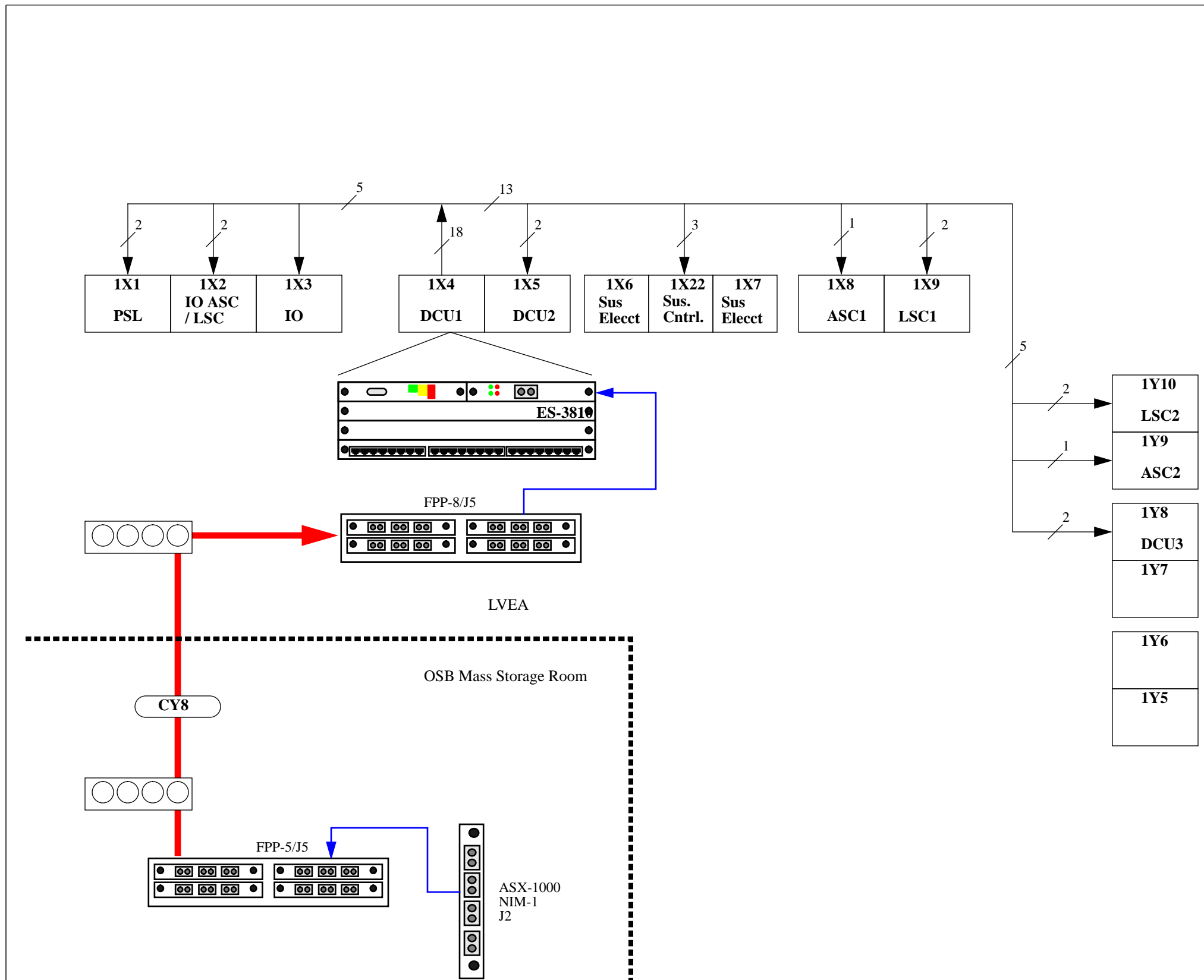
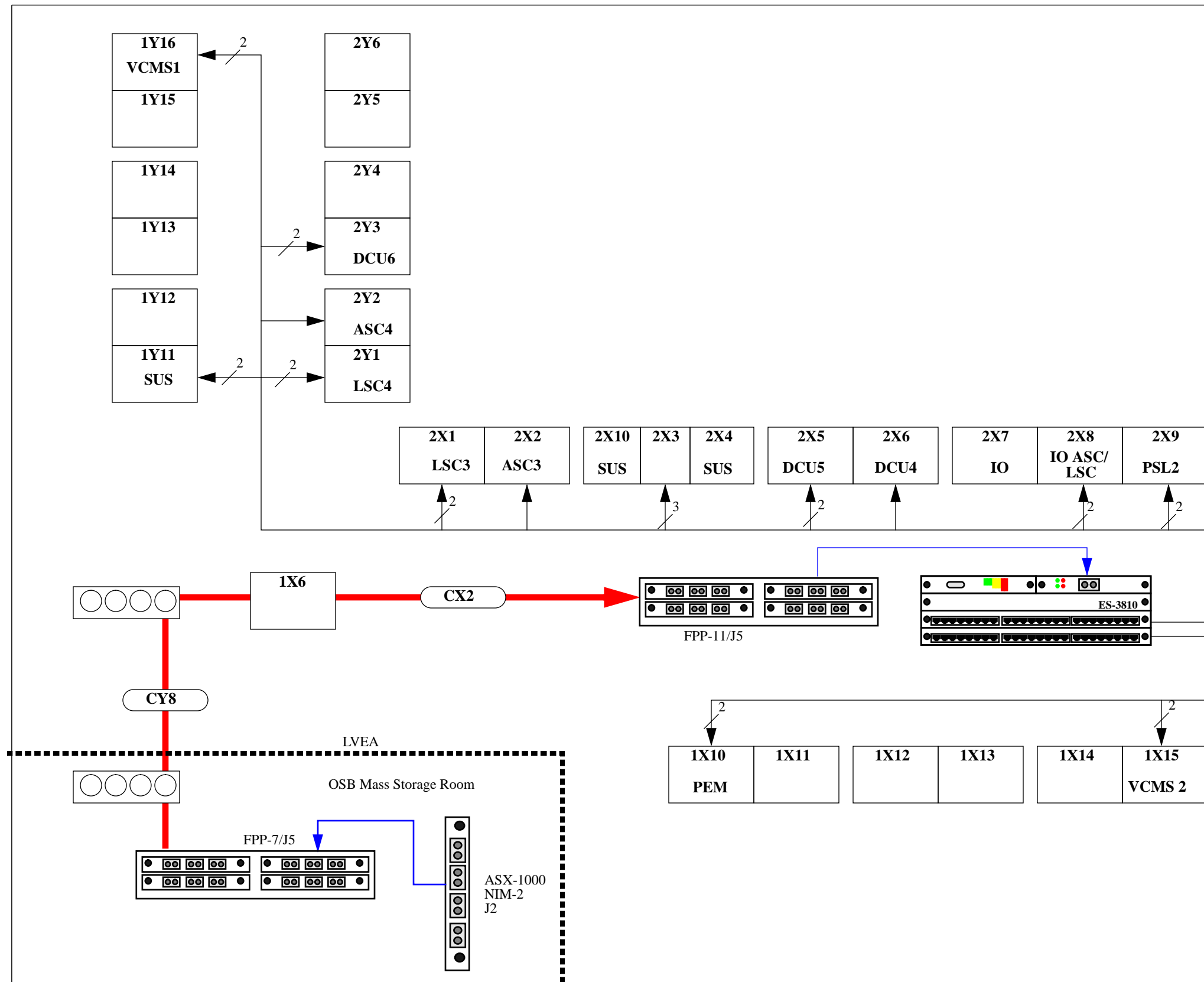


ES-3810 (LVEA Sector 1) Connection Assignments:

ES-3810 Connection	Cable Number	To Rack	To Device
J1		1X1	PSL Processor
J2		1X1	Rack Outlet
J3		1X2	IO ASC Processor
J4		1X2	IO LSC Processor
J5		1X3	Rack Outlet
J6			Spare
J7		1X4	DAQ Processor
J8		1X5	DAQ Processor
J9		1X5	Rack Outlet
J10		1X22	IO Processor
J11		1X22	CO Processor
J12		1X22	Rack Outlet
J13		1X8	ASC Processor
J14		1X9	LSC Processor
J15		1X9	Rack Outlet
J16		1Y10	LSC Processor
J17		1Y10	Rack Outlet
J18		1Y9	ASC Processor
J19		1Y8	DAQ Processor
J20		1Y8	Rack Outlet
J21			Spare
J22			Spare
J23			Spare
J24			Spare



				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR/MACH ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS IN OUT. REMOVE ALL BURRS				CURRENT REVISION APPROVAL				LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY			
				DO NOT SCALE THIS DRAWING				DRAWN R. Bork				Hanford Control & Monitoring Network LVEA Sector 1			
				USED ON:				CHECKED				SCALE			
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REFERENCE DRAWINGS				NEXT ASS'Y:				SHEETS EFFECTED				DATE			
6				5				4				3			
												D970583-00-C			
												3 of 5			
												STD			
												VER. 01			



ES-3810 (LVEA Sector 2) Connection Assignments:

ES-3810 Connection	Cable Number	To Rack	To Device
J1		2X9	PSL Processor
J2		2X9	Rack Outlet
J3		2X8	IO ASC Processor
J4		2X8	IO LSC Processor
J5		2X6	DAQ Processor
J6		2X5	DAQ Processor
J7		2X5	Rack Outlet
J8		2X3	IO Processor
J9		2X3	CO Processor
J10		2X3	Rack Outlet
J11		2X2	ASC Processor
J12		2X1	LSC Processor
J13		2X1	Rack Outlet
J14		2Y1	LSC Processor
J15		2Y1	Rack Outlet
J16		2Y2	ASC Processor
J17		2Y3	DAQ Processor
J18		2Y3	Rack Outlet
J19		1Y11	Fold Mirror Susp.
J20		1Y11	Rack Outlet
J21		1Y16	VCMS1 Processor
J22		1Y16	Rack Outlet
J23		1X15	VCMS2 Processor
J24		1X15	Rack Outlet
J25		1X10	PEM DAQ Processor
J26		1X10	Rack Outlet
J27-J48			Spares

UNLESS OTHERWISE SPECIFIED
 DIMENSIONS ARE IN INCHES
 TOLERANCES:
 FRACTIONAL: ±
 ANGULAR/MACH: ± BEND ±
 TWO PLACE DECIMAL: ±
 THREE PLACE DECIMAL: ±
 FINISHED SURFACE RMS:
 BREAK CORNERS IN: OUT:
 REMOVE ALL BURRS

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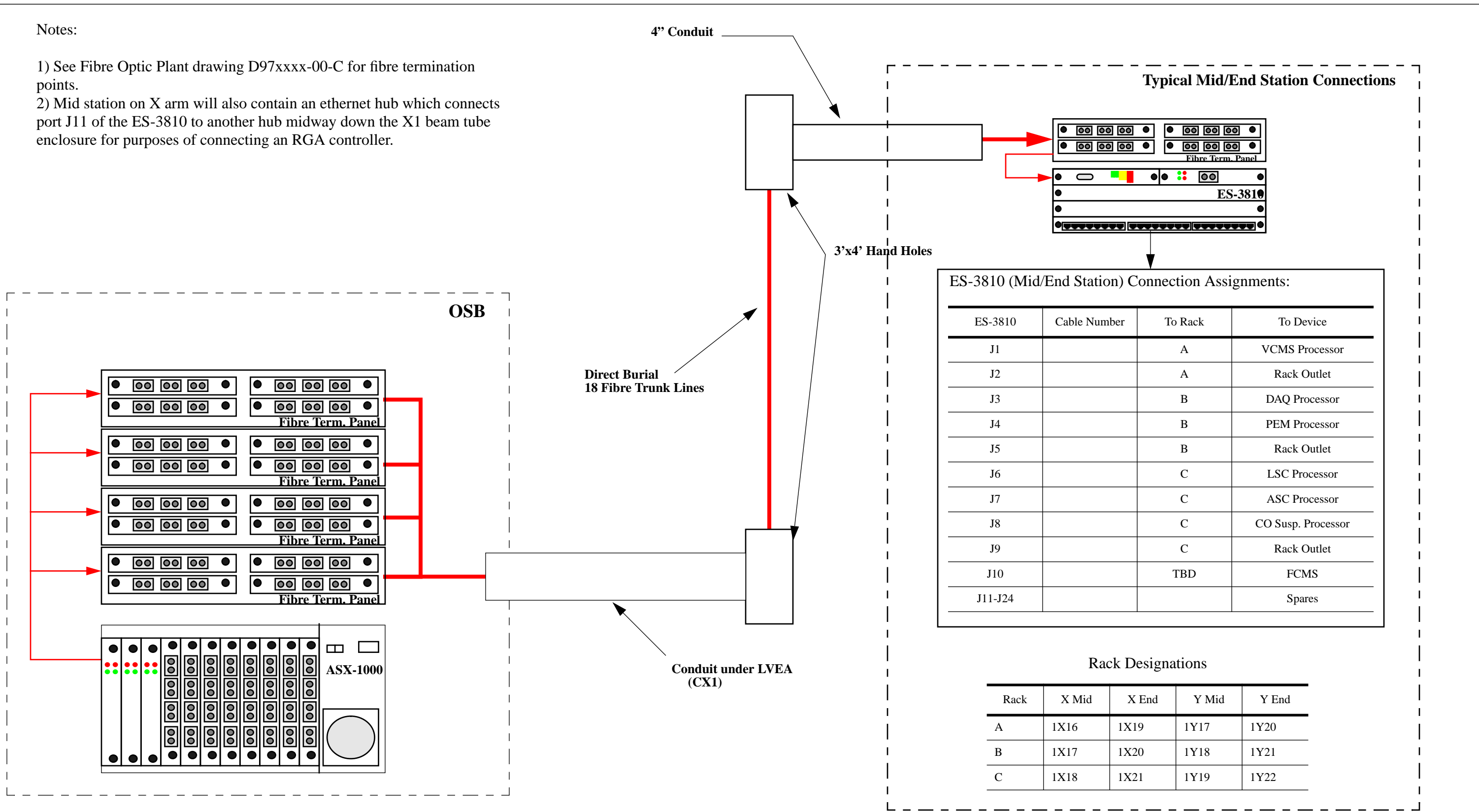
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Hanford Control & Monitoring Network
 LVEA Sector 2

DWG. NO.	DESCRIPTION	DWG. NO.	DESCRIPTION	REV.	DESCRIPTION	SHEETS EFFECTED	DATE	SCALE	SIZE	DWG. NO.	REV.
6	REFERENCE DRAWINGS	5							B	D970583-00-C	0
										4 of 5	0

Notes:

- 1) See Fibre Optic Plant drawing D97xxxx-00-C for fibre termination points.
- 2) Mid station on X arm will also contain an ethernet hub which connects port J11 of the ES-3810 to another hub midway down the X1 beam tube enclosure for purposes of connecting an RGA controller.



ES-3810 (Mid/End Station) Connection Assignments:

ES-3810	Cable Number	To Rack	To Device
J1		A	VCMS Processor
J2		A	Rack Outlet
J3		B	DAQ Processor
J4		B	PEM Processor
J5		B	Rack Outlet
J6		C	LSC Processor
J7		C	ASC Processor
J8		C	CO Susp. Processor
J9		C	Rack Outlet
J10		TBD	FCMS
J11-J24			Spares

Rack Designations

Rack	X Mid	X End	Y Mid	Y End
A	1X16	1X19	1Y17	1Y20
B	1X17	1X20	1Y18	1Y21
C	1X18	1X21	1Y19	1Y22

UNLESS OTHERWISE SPECIFIED
 DIMENSIONS ARE IN INCHES
 TOLERANCES:
 FRACTIONAL ±
 ANGULAR ± BEND ±
 TWO PLACE DECIMAL ±
 THREE PLACE DECIMAL ±
 FINISHED SURFACE RMS
 BREAK CORNERS IN OUT.
 REMOVE ALL BURRS

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 SIGNATURE: [Signature]
 DATE: [Signature]

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**Hanford Control & Monitoring Network
 Mid & End Stations**

SCALE: [Blank] SHEET: B DWG. NO. D970583-00-C REV. 0
 EAB FILE: [Blank] SCALE: [Blank] SHEET: 5 of 5 STD VER. 01

DWG. NO.	DESCRIPTION	DWG. NO.	DESCRIPTION	USED ON:	REV.	DESCRIPTION	SHEETS EFFECTED	DATE	DEC.	SCALE	REV.
6		5		NEXT ASS'Y:							0