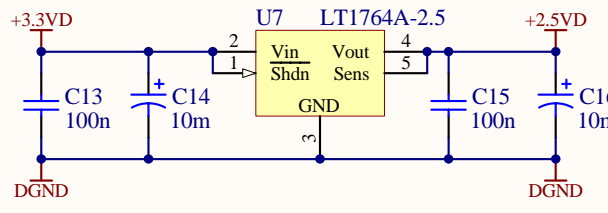
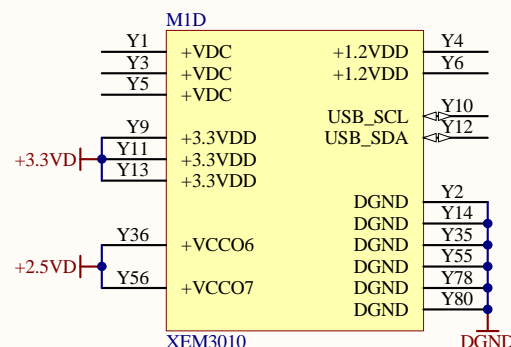
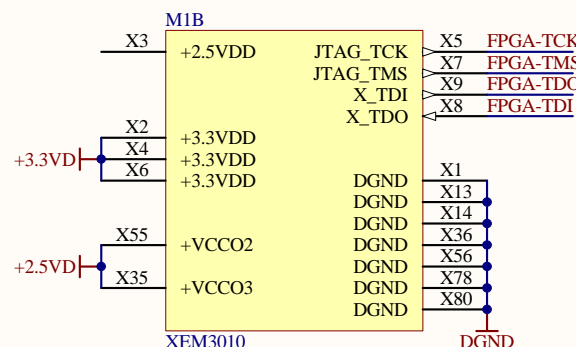
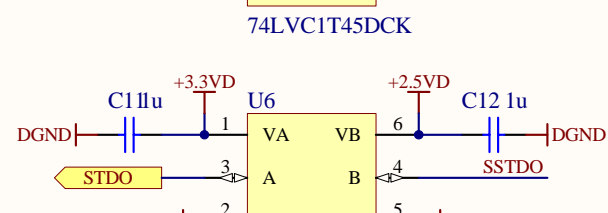
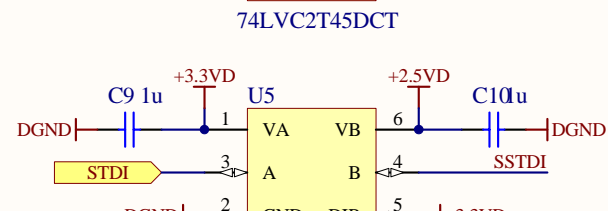
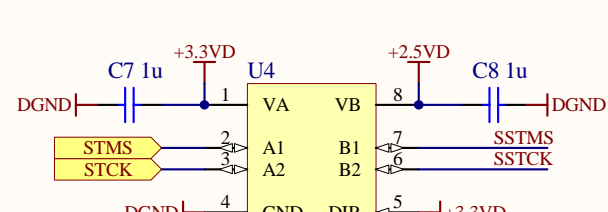
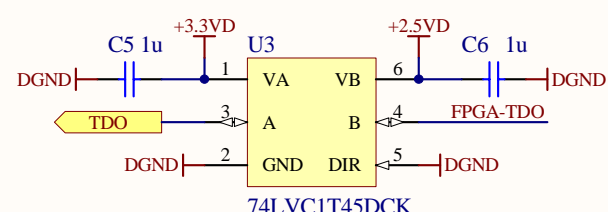
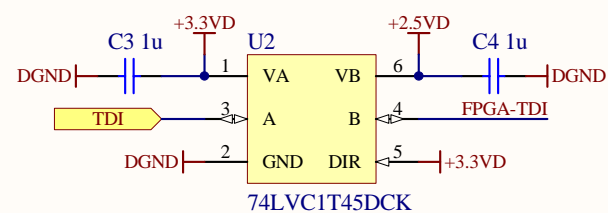
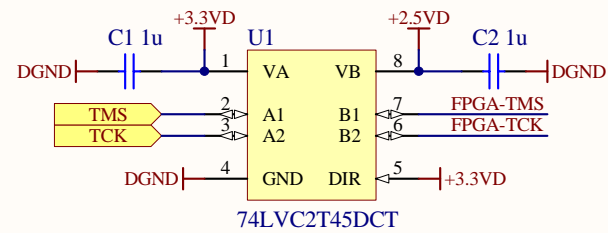
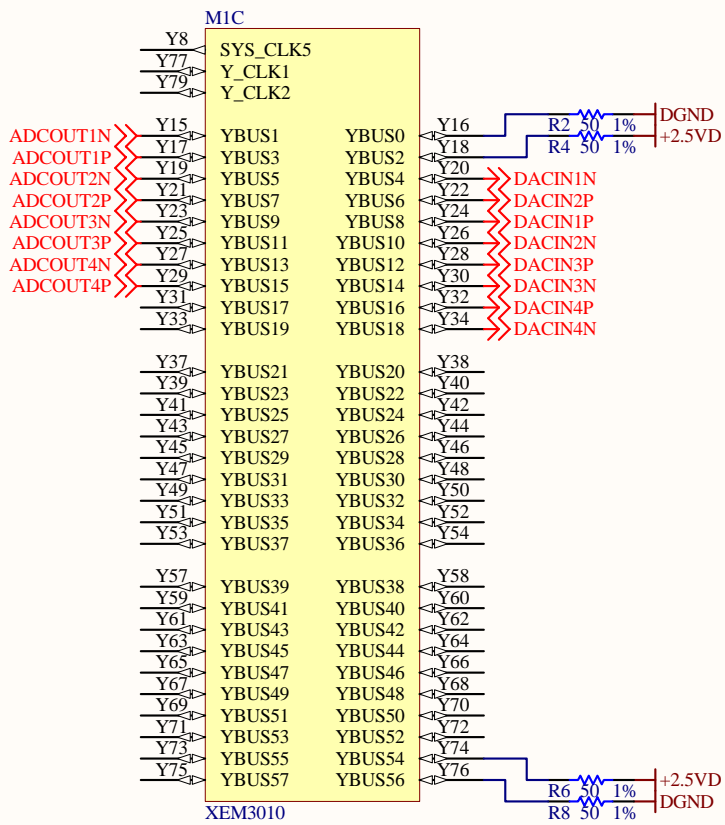
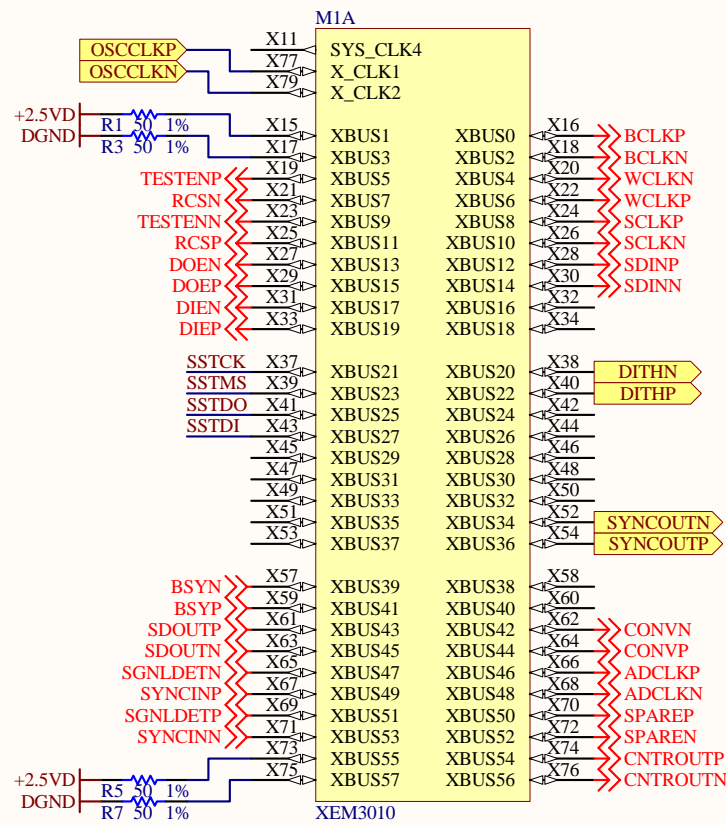
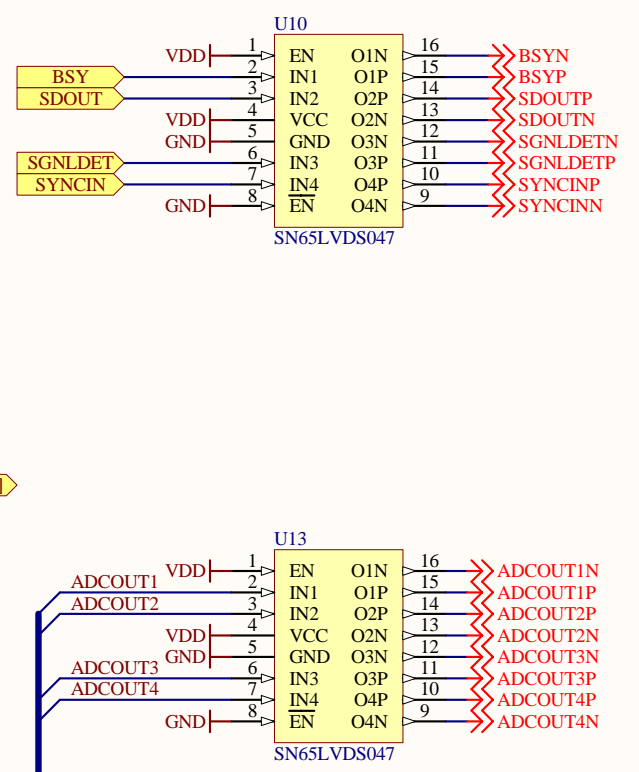
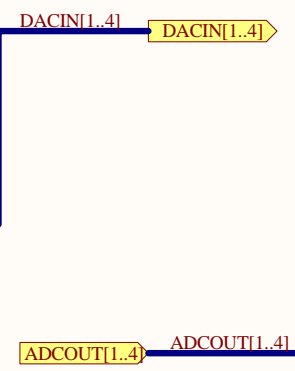
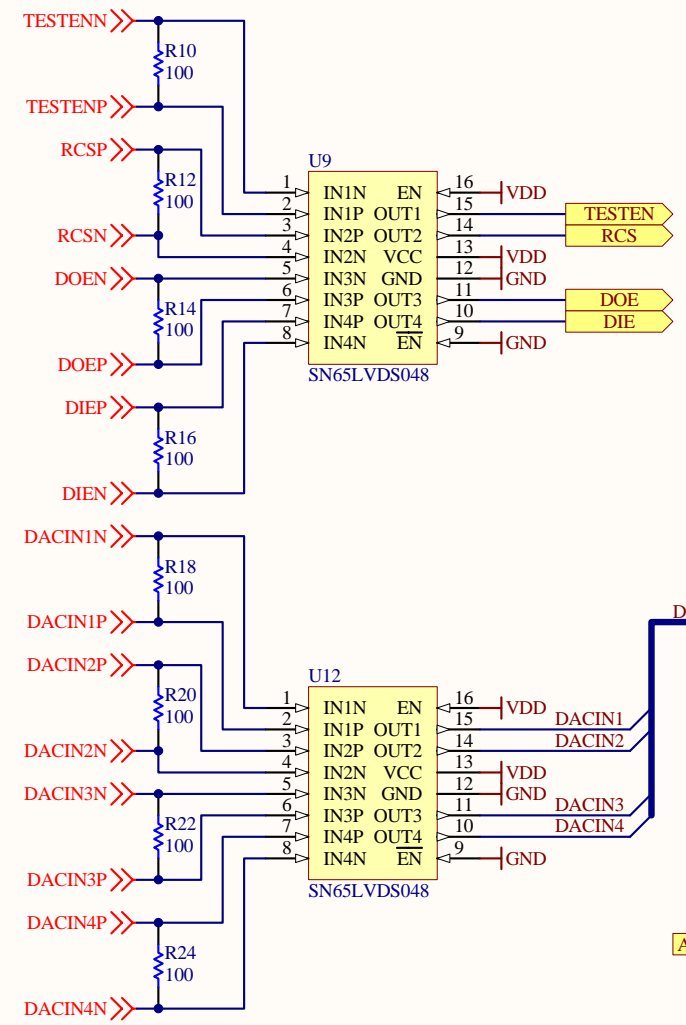
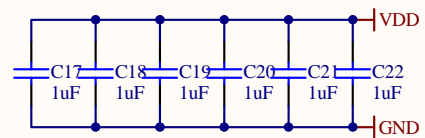
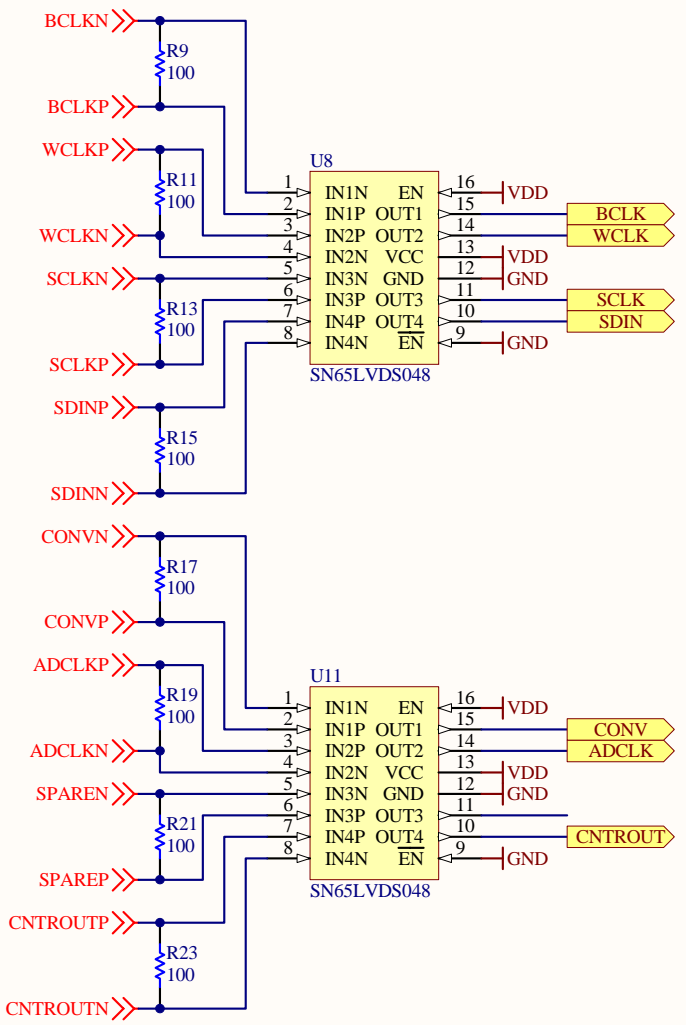


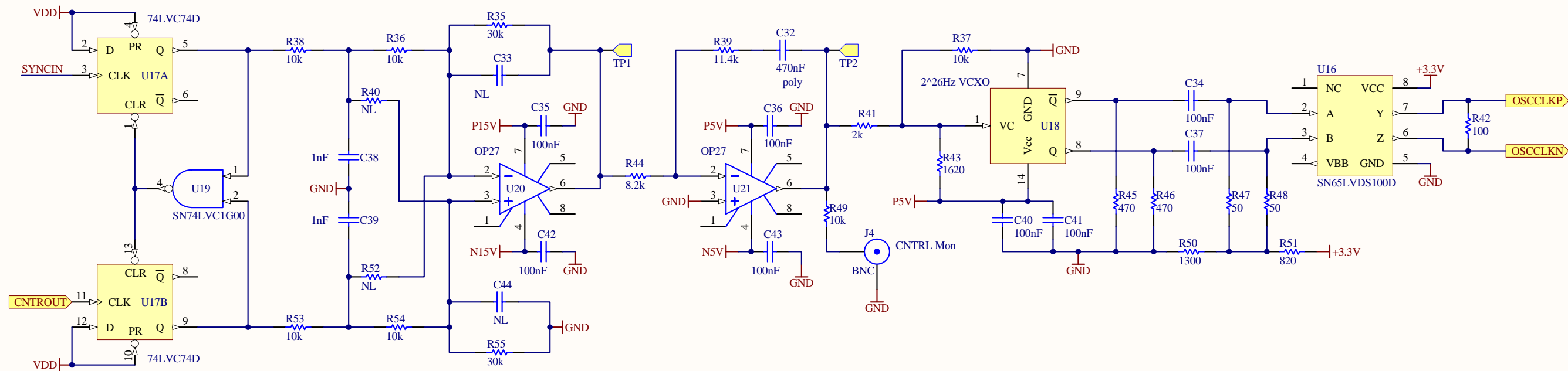
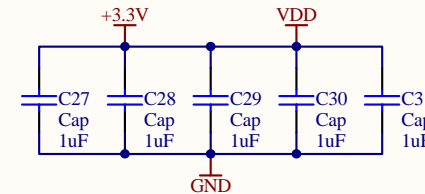
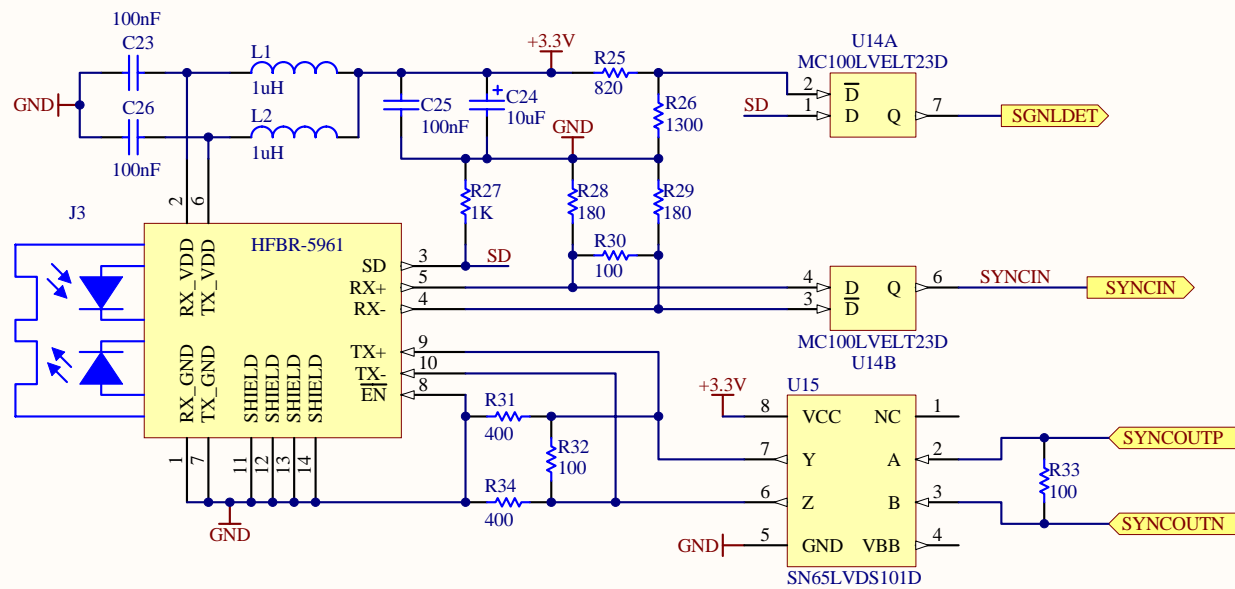
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| Title     |                          |  | CTB/ Block Diagram |               |                                 |
| Size<br>B | Number<br>D060118        |  |                    | Revision<br>A |                                 |
| Date:     | 4/20/2006                |  |                    | Sheet         | of                              |
| File:     | C:\User\...\CTB00.SchDoc |  |                    | Drawn By:     | <a href="#">Paul Schwinberg</a> |



|                       |                          |                           |
|-----------------------|--------------------------|---------------------------|
| Title                 |                          |                           |
| CTB/ FPGA Connections |                          |                           |
| Size                  | Number                   | Revision                  |
| B                     | D060118                  | A                         |
| Date:                 | 4/20/2006                | Sheet of                  |
| File:                 | C:\User\...\CTB01.SchDoc | Drawn By: Paul Schwinberg |



|       |                          |           |                       |  |  |
|-------|--------------------------|-----------|-----------------------|--|--|
| Title |                          |           | CTB/ LVDS Translators |  |  |
| Size  | Number                   | Revision  |                       |  |  |
| B     | D060118                  | A         |                       |  |  |
| Date: | 4/20/2006                | Sheet     | of                    |  |  |
| File: | C:\User\...\CTB02.SchDoc | Drawn By: | Paul Schwinberg       |  |  |



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|---|----------------------------------|----------------------|
| Title<br><b>CTB/ Oscillator Locking</b> |                                  |                      |
| Size<br>B                               | Number<br><b>D060118</b>         | Revision<br><b>A</b> |
| Date:<br>4/20/2006                      | Sheet 2 of 4                     |                      |
| File:<br>C:\User\...\CTB03.SchDoc       | Drawn By: <b>Paul Schwinberg</b> |                      |

A

B

C

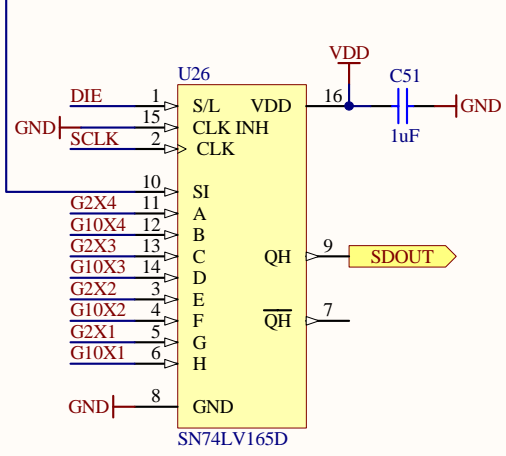
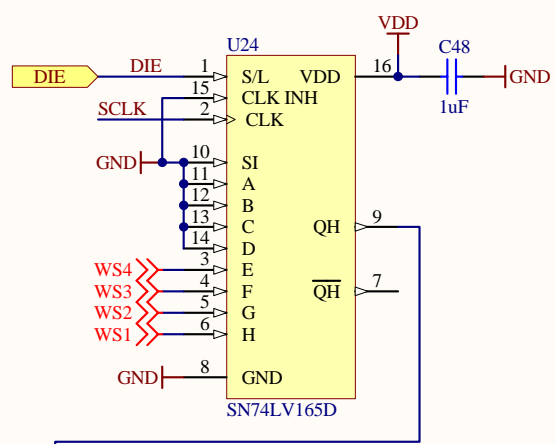
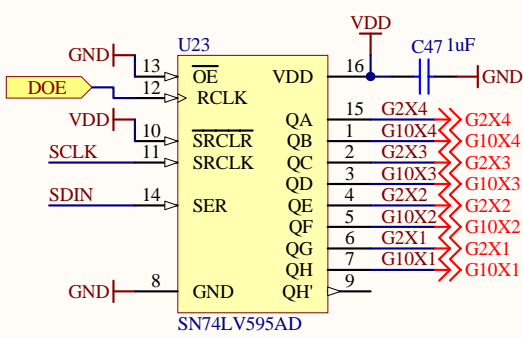
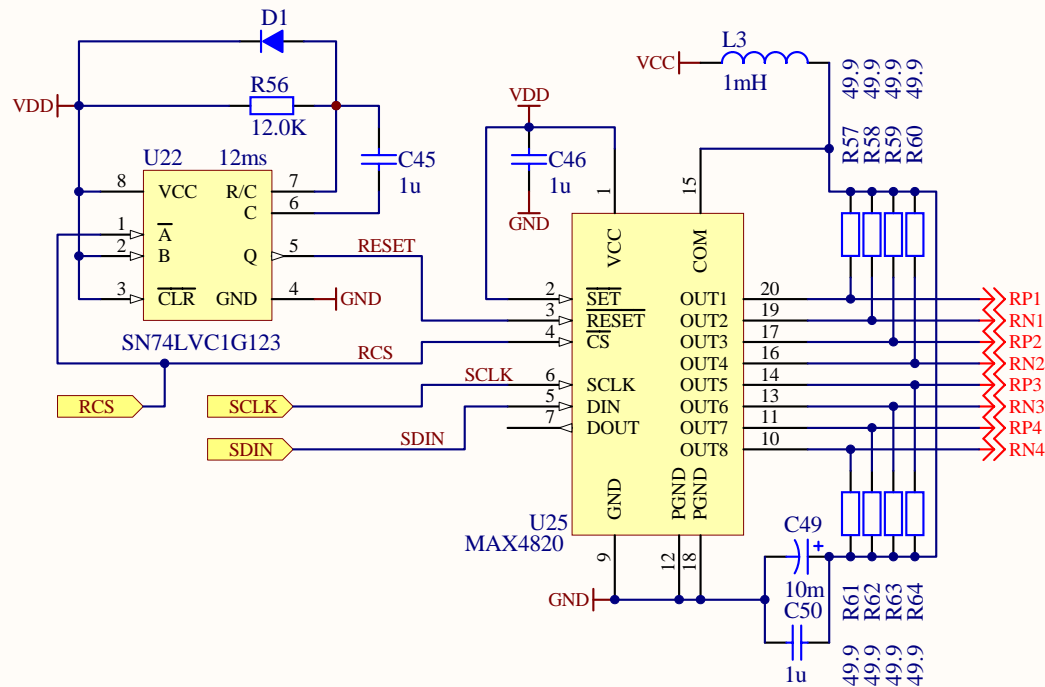
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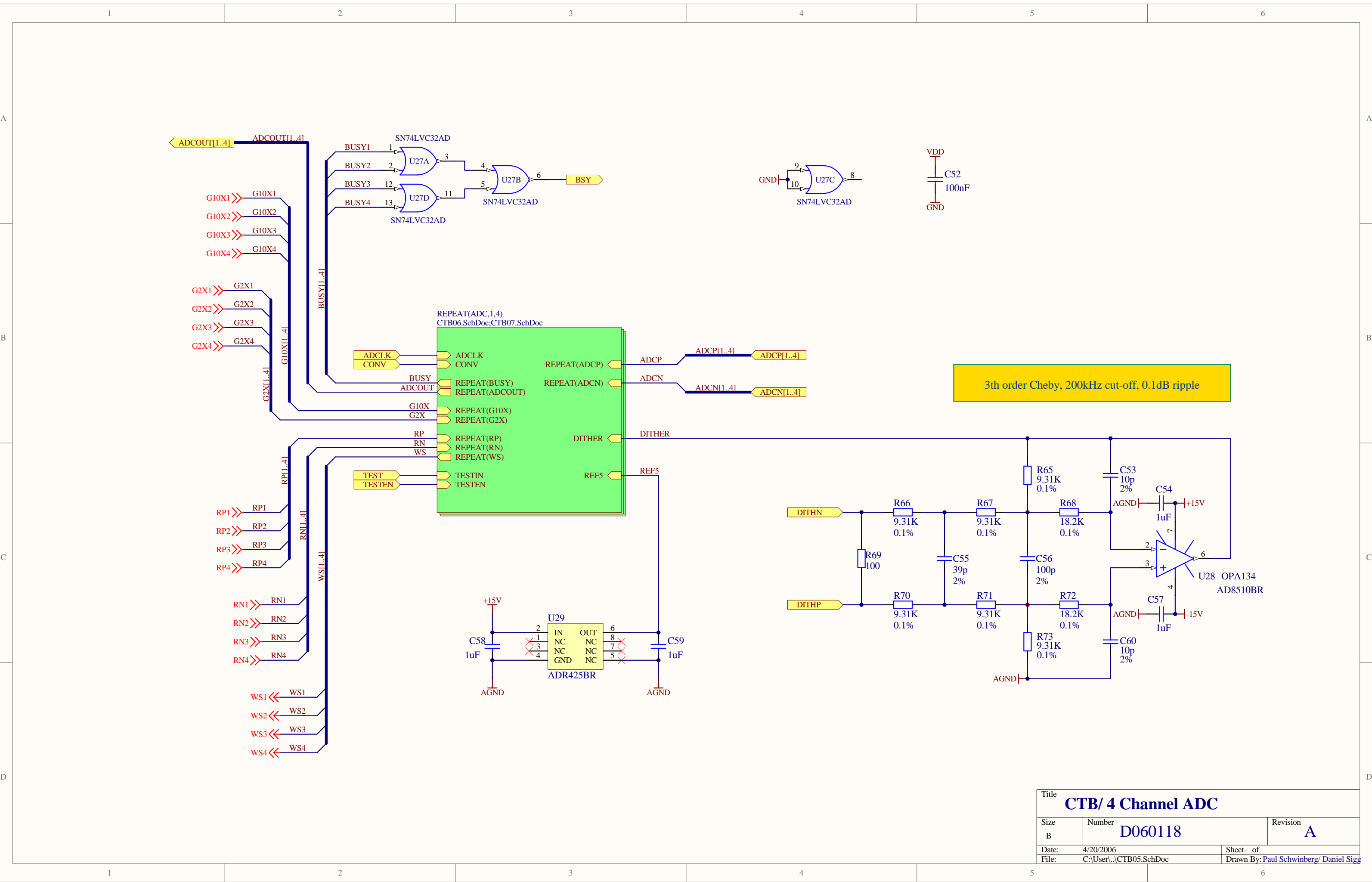
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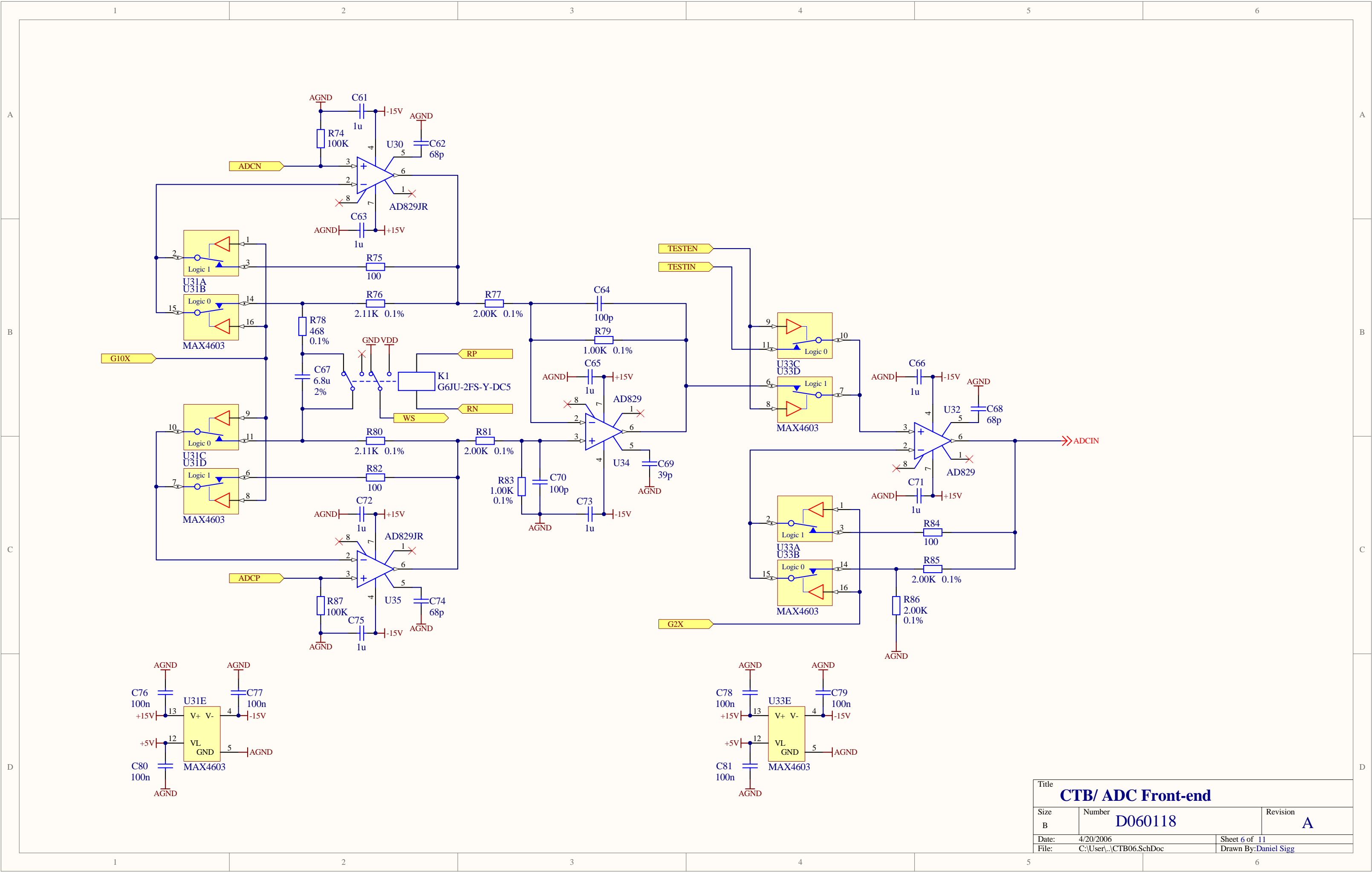
C

D



|  |                          |  |
|--|--------------------------|--|
| Title<br><b>CTB/ ADC Front End Control</b> |                          |  |
| Size<br>B                                  | Number<br><b>D060118</b> | Revision<br><b>A</b>                   |
| Date:                                      | 4/20/2006                | Sheet of                               |
| File:                                      | C:\User\...\CTB04.SchDoc | Drawn By: Paul Schwinberg/ Daniel Sigg |



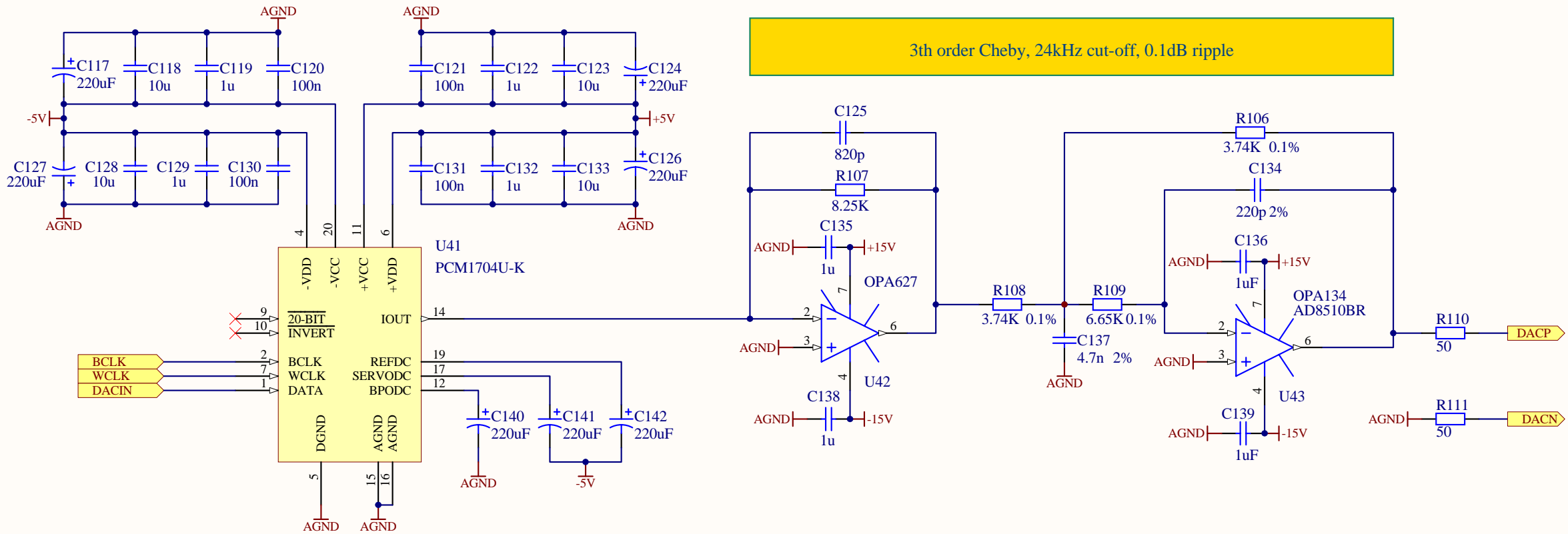


|                                    |                          |                       |
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| Title<br><b>CTB/ ADC Front-end</b> |                          |                       |
| Size<br>B                          | Number<br><b>D060118</b> | Revision<br><b>A</b>  |
| Date:                              | 4/20/2006                | Sheet 6 of 11         |
| File:                              | C:\User\...\CTB06.SchDoc | Drawn By: Daniel Sigg |

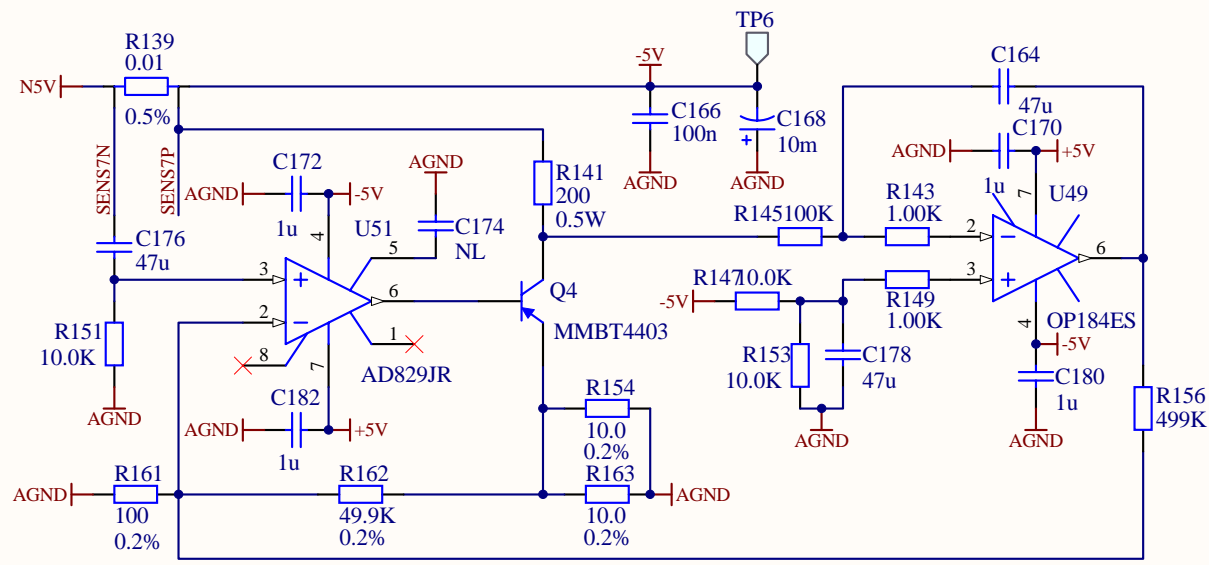
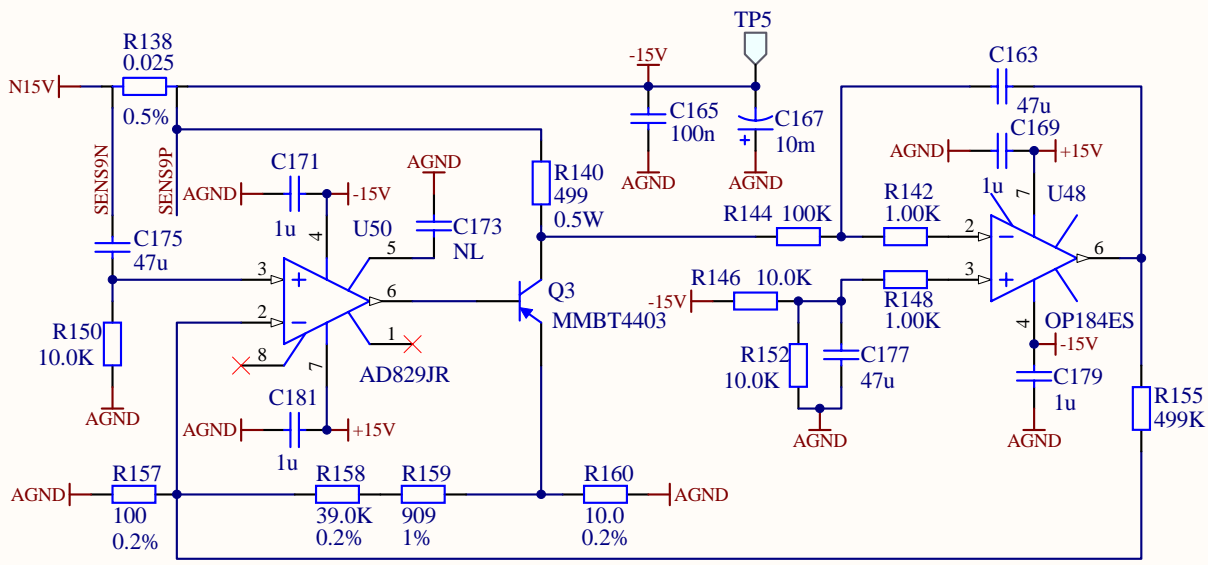
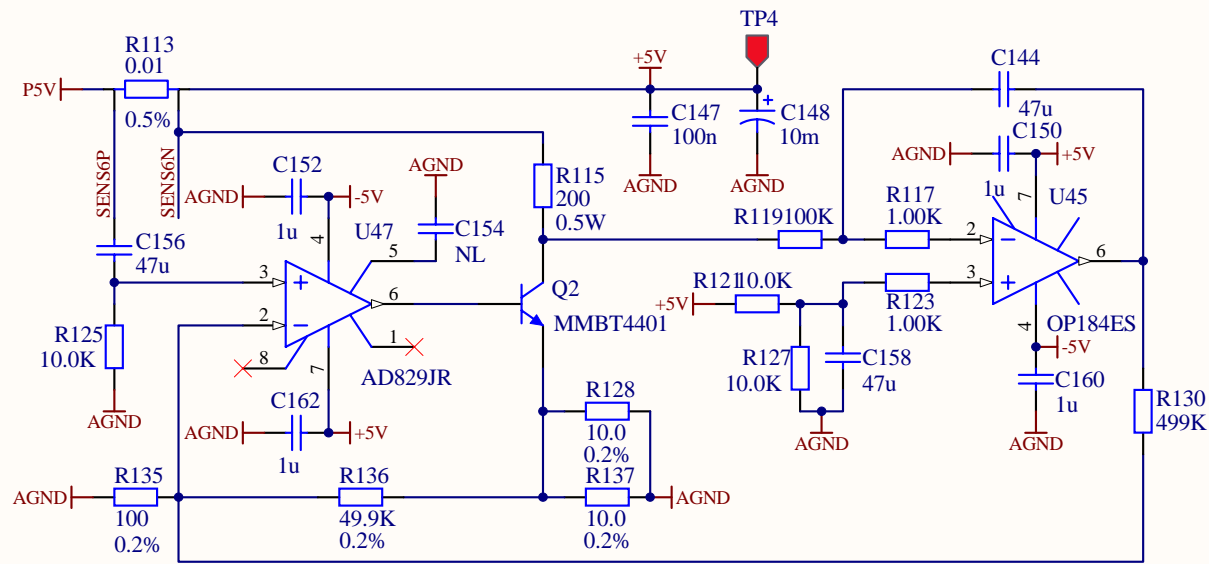
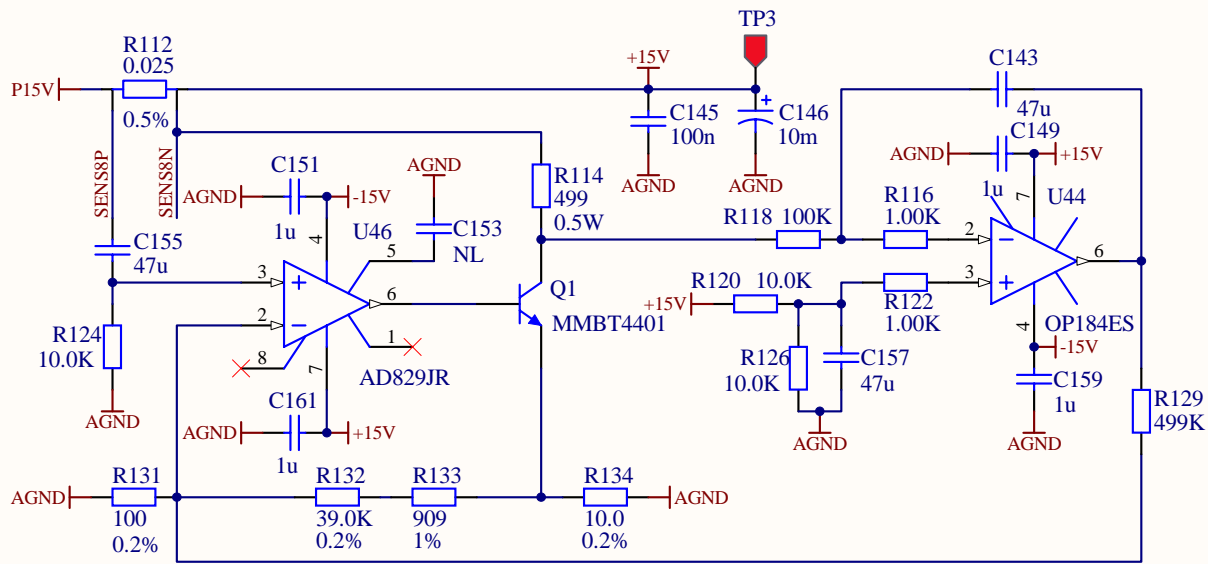


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| Title<br><b>CTB/ ADC</b> |                          |  |
| Size<br>B                | Number<br><b>D060118</b> | Revision<br><b>A</b>                   |
| Date:                    | 4/20/2006                | Sheet 7 of 11                          |
| File:                    | C:\User\...\CTB07.SchDoc | Drawn By: Daniel Sigg/ Paul Schwinberg |





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|--------------------------|--------------------------|--|
| Title<br><b>CTB/ DAC</b> |                          |  |
| Size<br>B                | Number<br><b>D060118</b> | Revision<br><b>A</b>                   |
| Date:                    | 4/20/2006                | Sheet 9 of 11                          |
| File:                    | C:\User\...\CTB08.SchDoc | Drawn By: Daniel Sigg/ Paul Schwinberg |



noise eater/current shunt: 40dB min. suppression, +/-100uV max. range

|                         |                          |                                      |
|-------------------------|--------------------------|--------------------------------------|
| Title                   |                          |                                      |
| CTB/ Power Noise Eaters |                          |                                      |
| Size                    | Number                   | Revision                             |
| B                       | D060118                  | A                                    |
| Date:                   | 4/20/2006                | Sheet of                             |
| File:                   | C:\User\...\CTB09.SchDoc | Drawn ByDaniel Sigg/ Paul Schwinberg |

