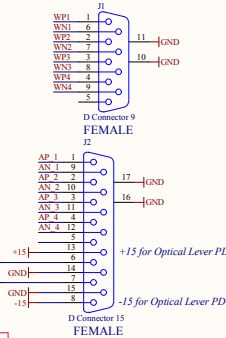
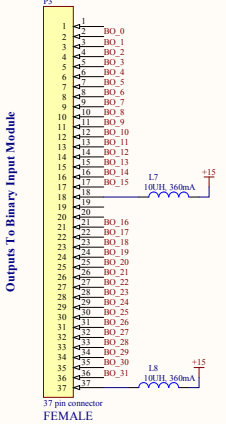
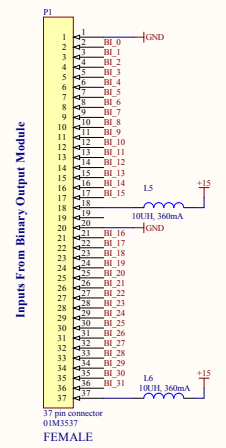
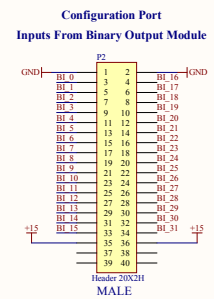
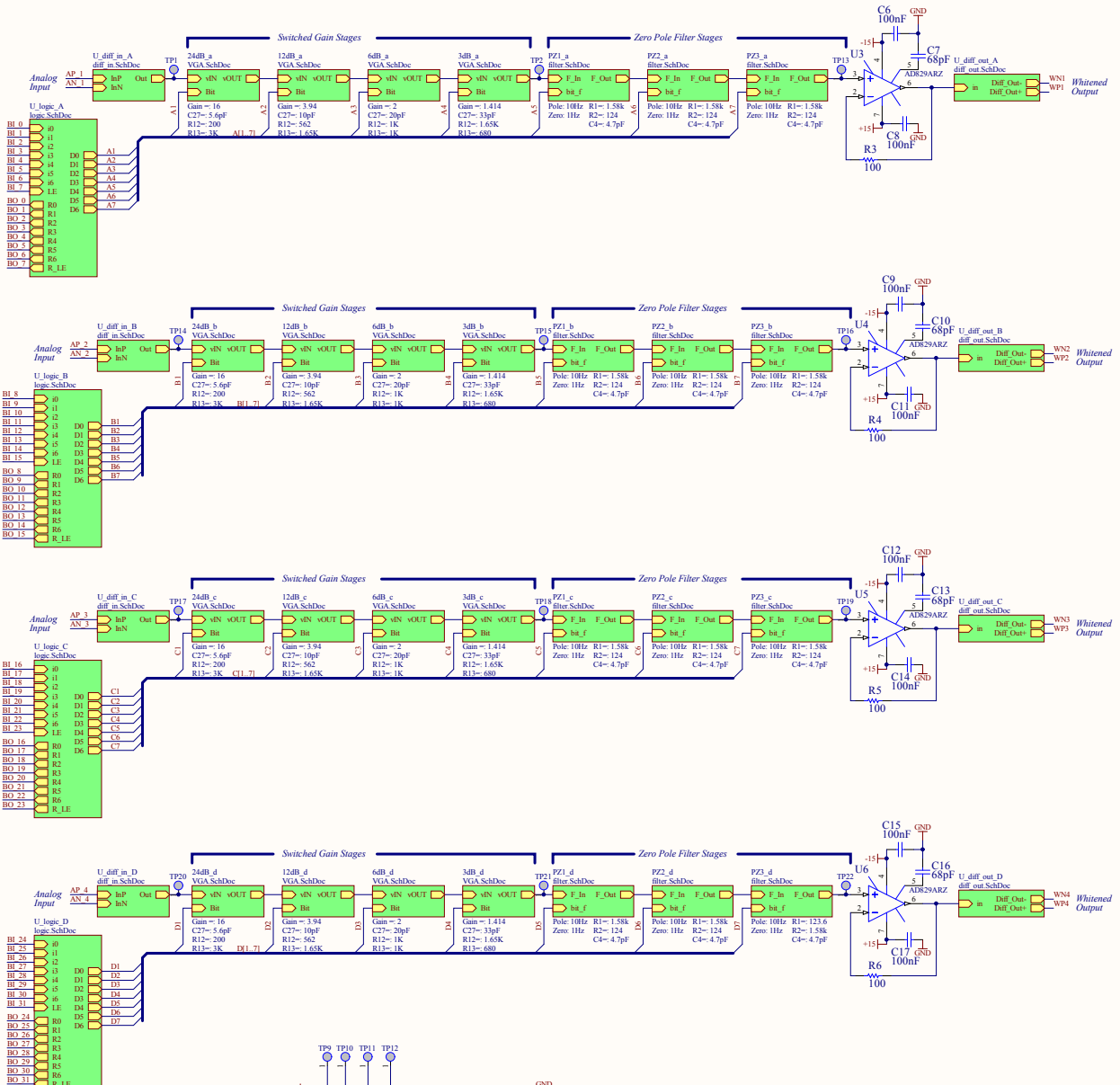


A

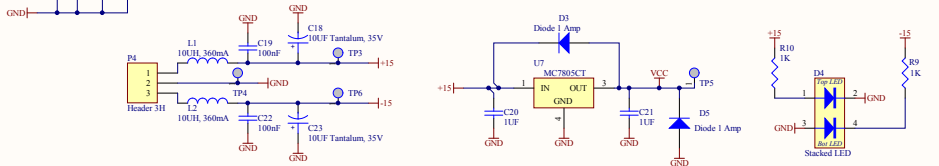
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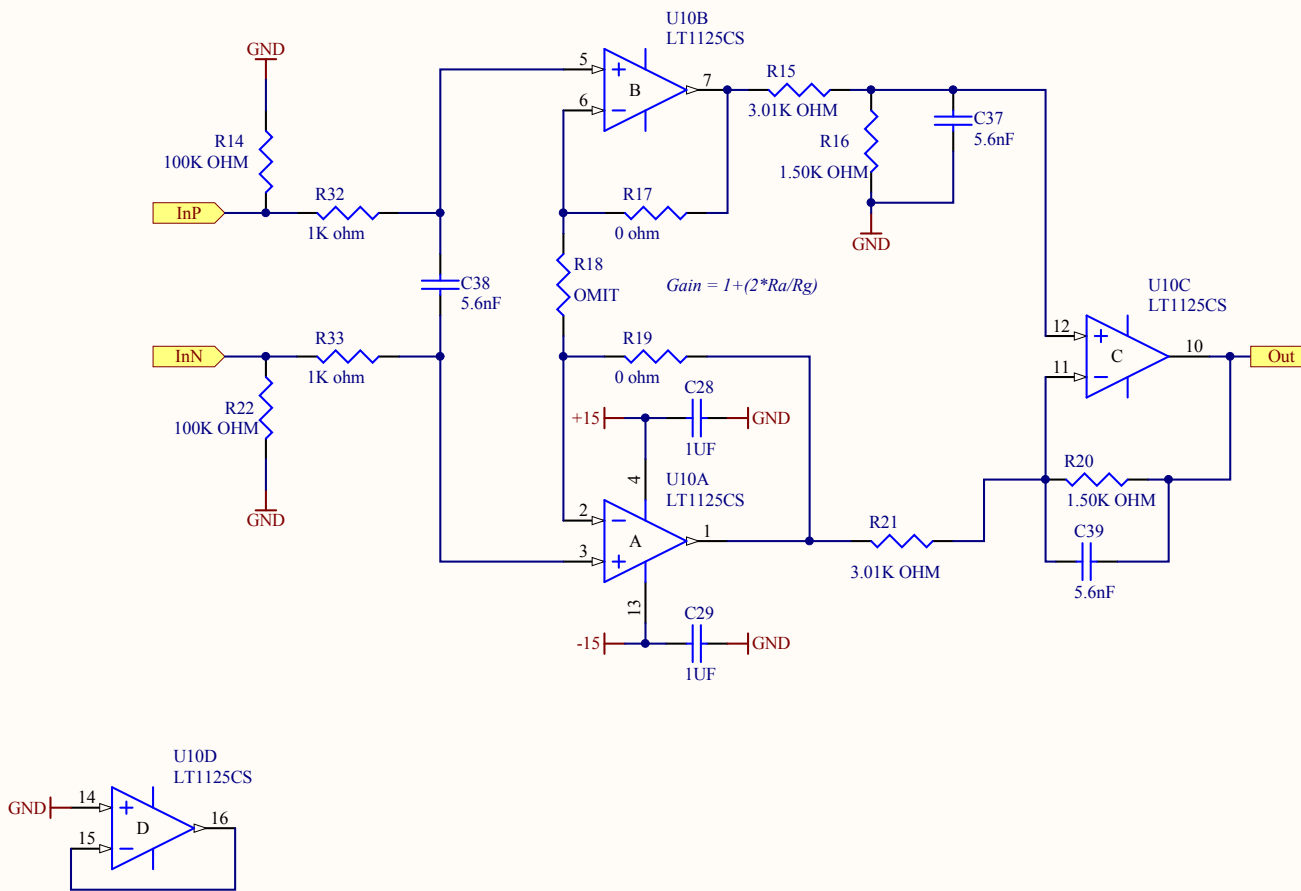
C

D



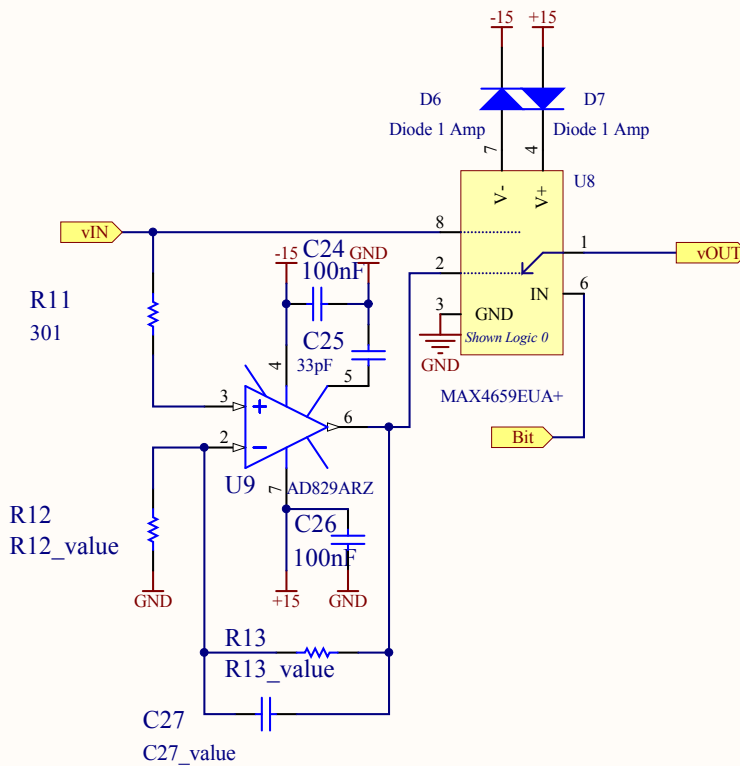
Revision History:
 Rev1 - Initial release, fashioned with problems, trashed. PCB mysteriously named version 2. Threw away PCB
 Rev2 - First actual functioning prototype.
 Rev3 - Fixed missing VCC to DG412 on pin 12. Used wider footprint for R31 in logic section. Grounded pins 2, 7, 10, and 15 on U14 and U15 as they were pulling to the wrong voltage. Removed C34 and C35 as they are no longer needed. Added +/-15 on J2 for Optical Lever PD head power





Last Edited:

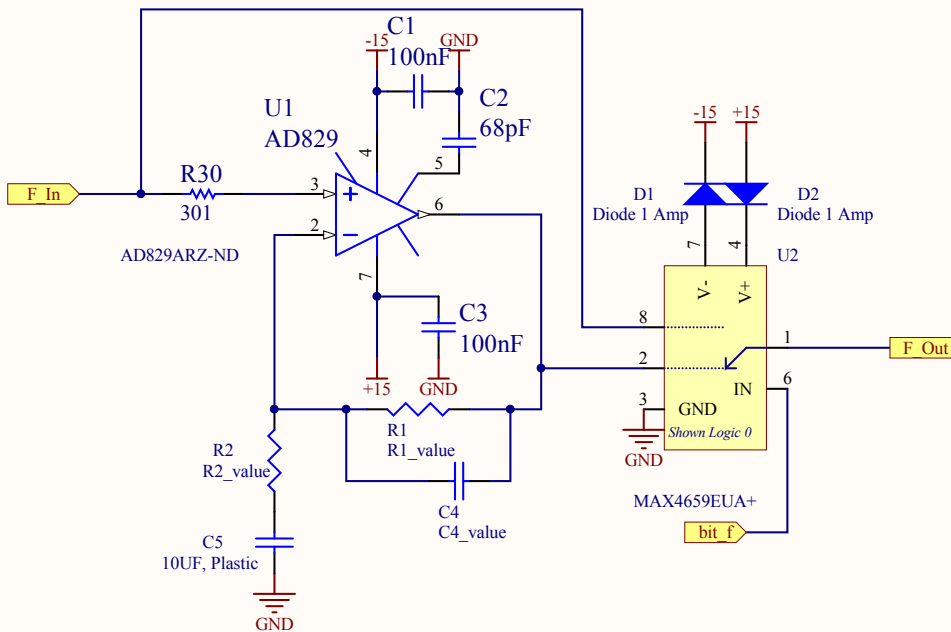
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Size: A	DCC Number: D1001530	Revision: v3	Engineer: R. Abbott	
File: C:\Rich's Files\Mycadfiles\ISC\ISC Whitening Board\iscwhite_v3\diff_in.SchDoc				
				Sheet 2 of 6



Last Edited:

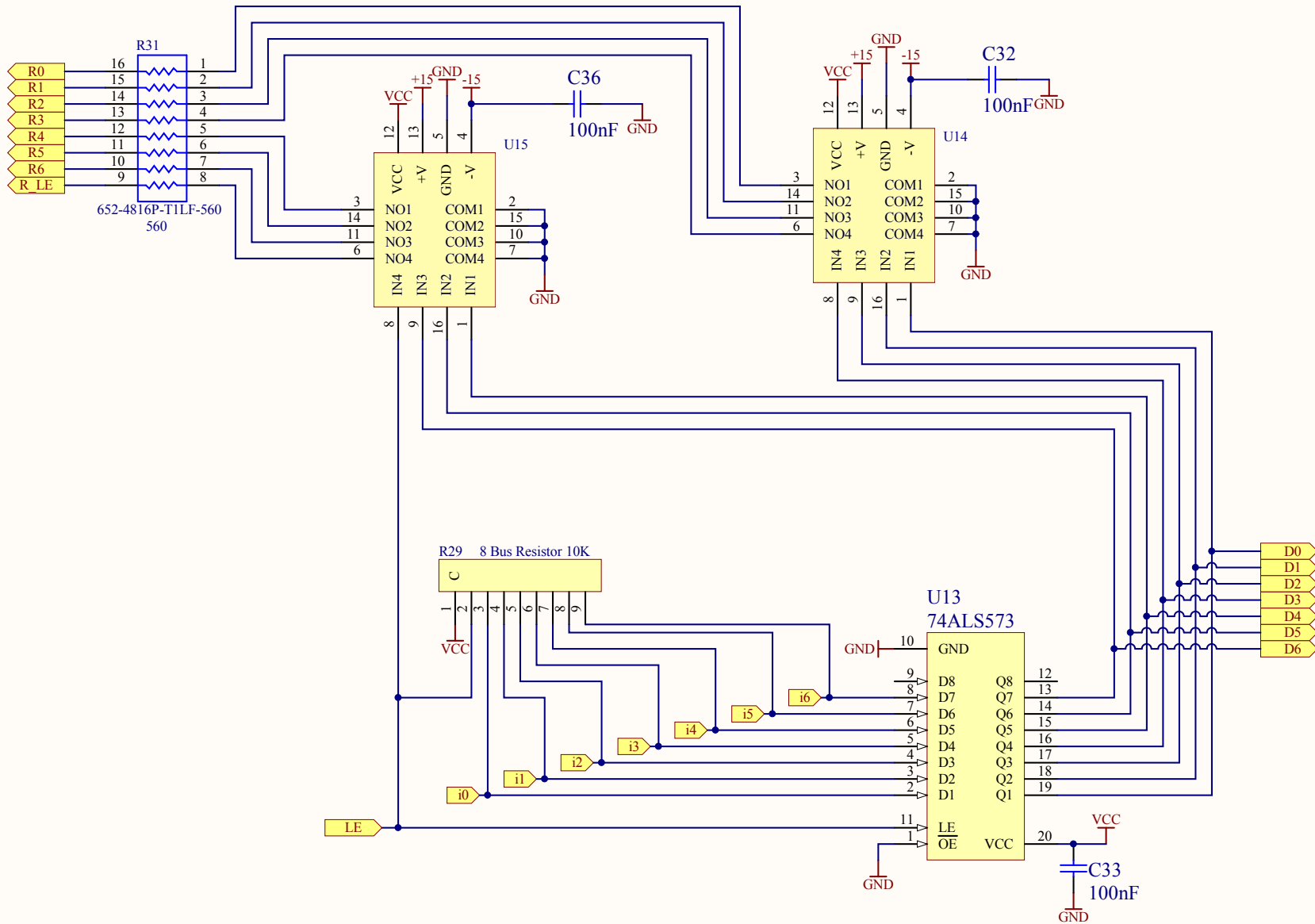
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Size: A	DCC Number: D1001530	Revision: v3	Engineer: R. Abbott	Date: 1/7/2011	
File: C:\Rich's Files\Mycadfiles\ISC\ISC Whitening Board\iscwhite_v3\VGA.SchDoc				Time: 5:33:27 PM	Sheet 3 of 6

Zero @ 1Hz, Pole @ 10Hz

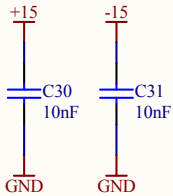
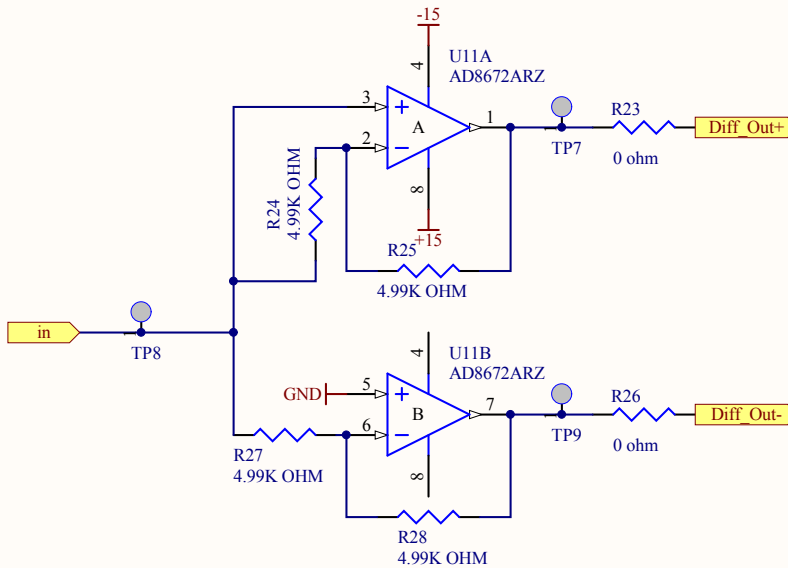


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Title		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO
Zero-pole Stage				
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				Sheet 4 of 6



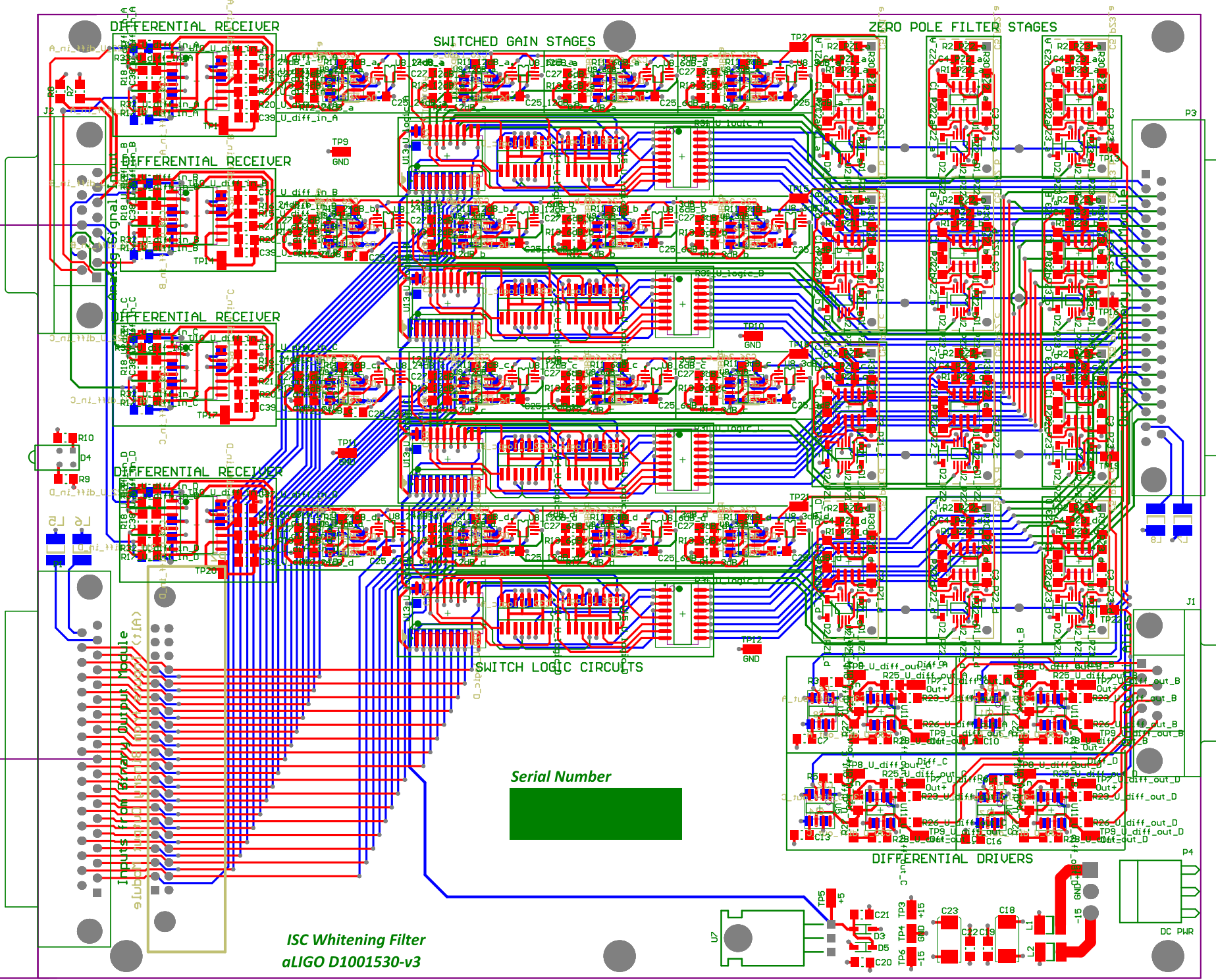
Title Switch Logic Circuit			LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		Last Edited:	
Size: A	DCC Number: D1001530	Revision: v3	Engineer: R. Abbott	Date: 1/7/2011		
File: C:\Rich's Files\Mycadfiles\ISC\ISC Whitening Board\iscwhite_v3\logic.SchDoc				Date: 5:33:28 PM	Sheet 5 of 6	



Last Edited:

Title Differential Driver		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		Date: 1/7/2011 Time: 5:33:28 PM
Size: A	DCC Number: D1001530	Revision: v3	Engineer: R. Abbott	
File: C:\Rich's Files\Mycadfiles\ISC\ISC Whitening Board\iscwhite_v3\diff_out.SchDoc				Sheet 6 of 6

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1591.00



ISC Whiting Filter
aLIGO D1001530-v3

DC PHR

