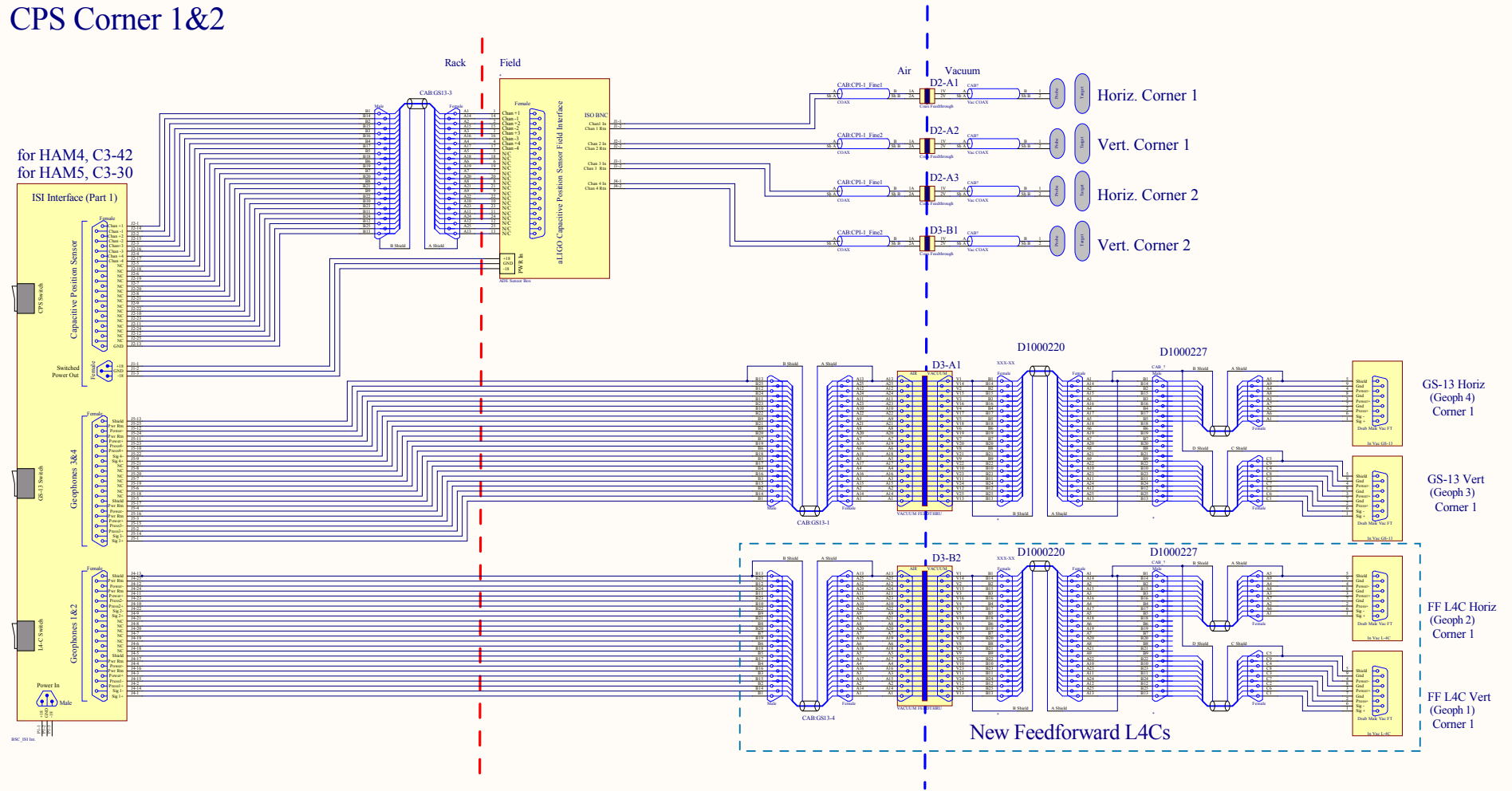


Title: <b>alIGO HAM4 SI HEPI System Schematics</b>			
Size: C	DCC Number: D1000298	Ligo Project California Institute of Technology Massachusetts Institute of Technology	
Drawn by: Ben Abbott	Date: 8/10/2011	Revision: v3	Cannot open file C:\Ben\misc\lan\yloglog_1.jpg
File: C:\created\Ben\Si\Si\Single State SI\HAM4_2_system_schematics\PIER1_S003_00.PMI Sheet 1 of 11			

# Geophones Corner 1

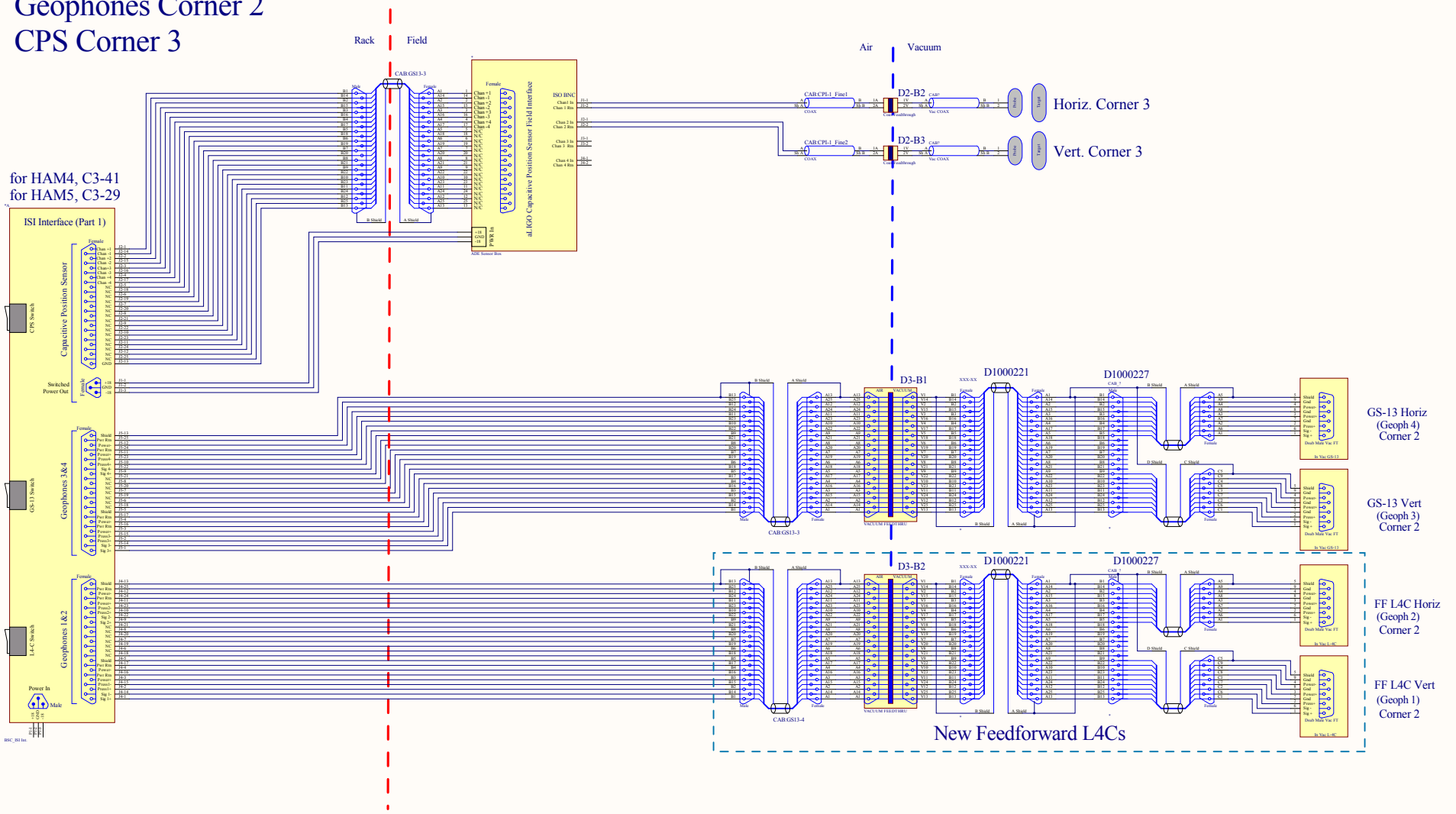
## CPS Corner 1&2

for HAM4, C3-42  
for HAM5, C3-30



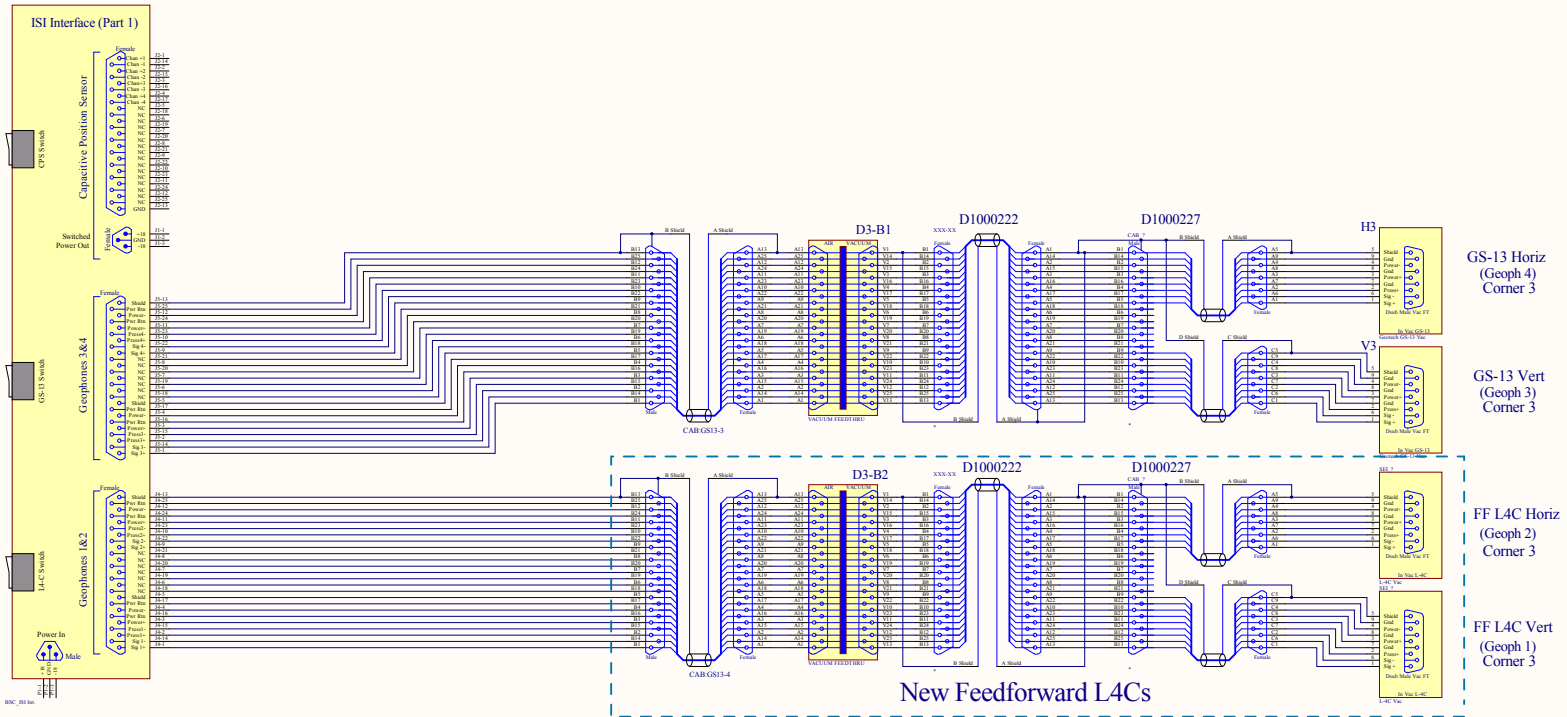
# Geophones Corner 2 CPS Corner 3

for HAM4, C3-41  
for HAM5, C3-29

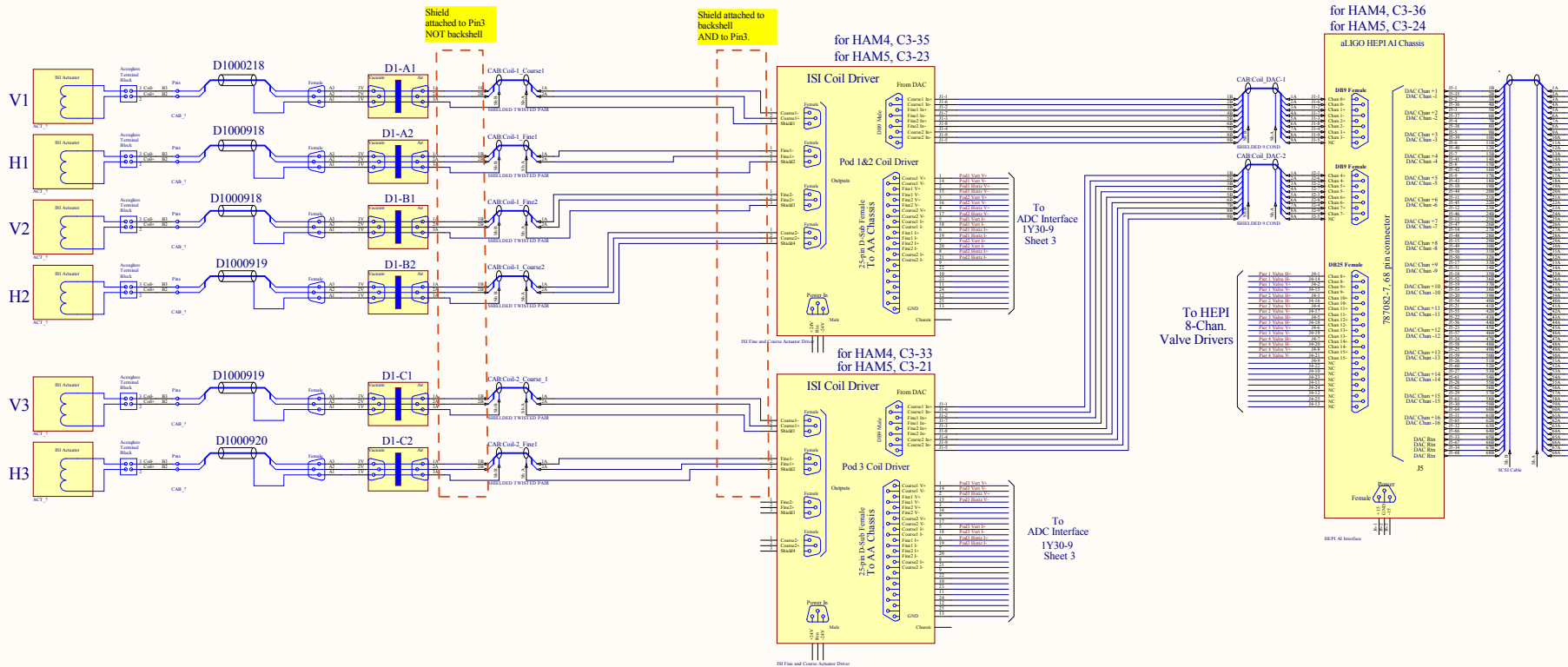


# Geophones Corner 3

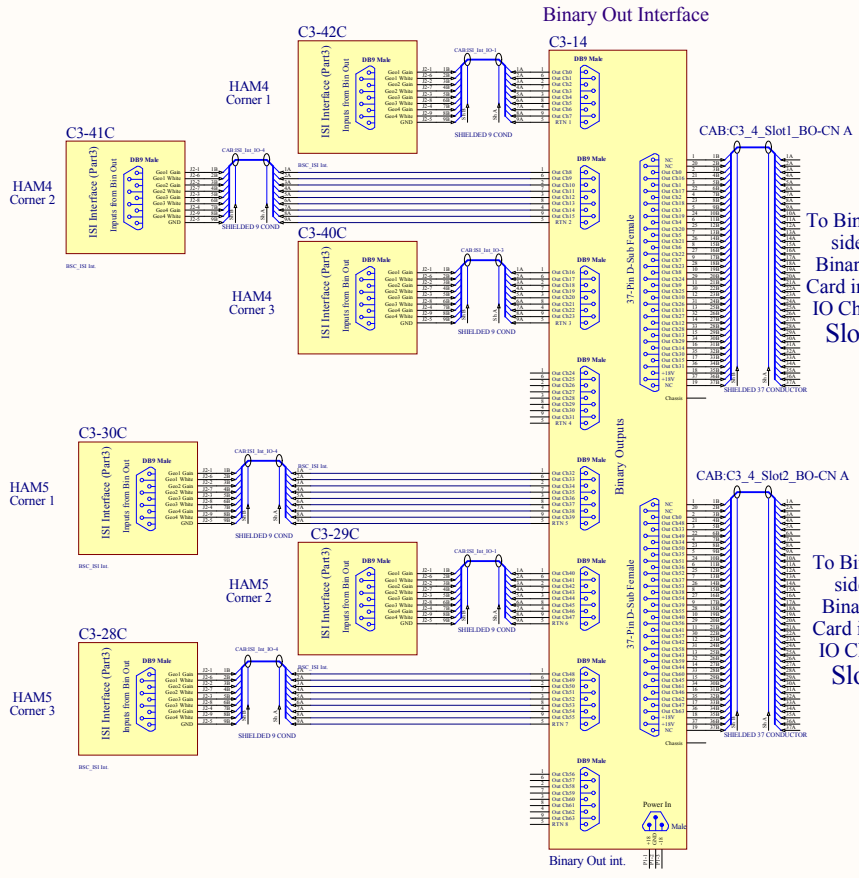
for HAM4, C3-40  
for HAM5, C3-28





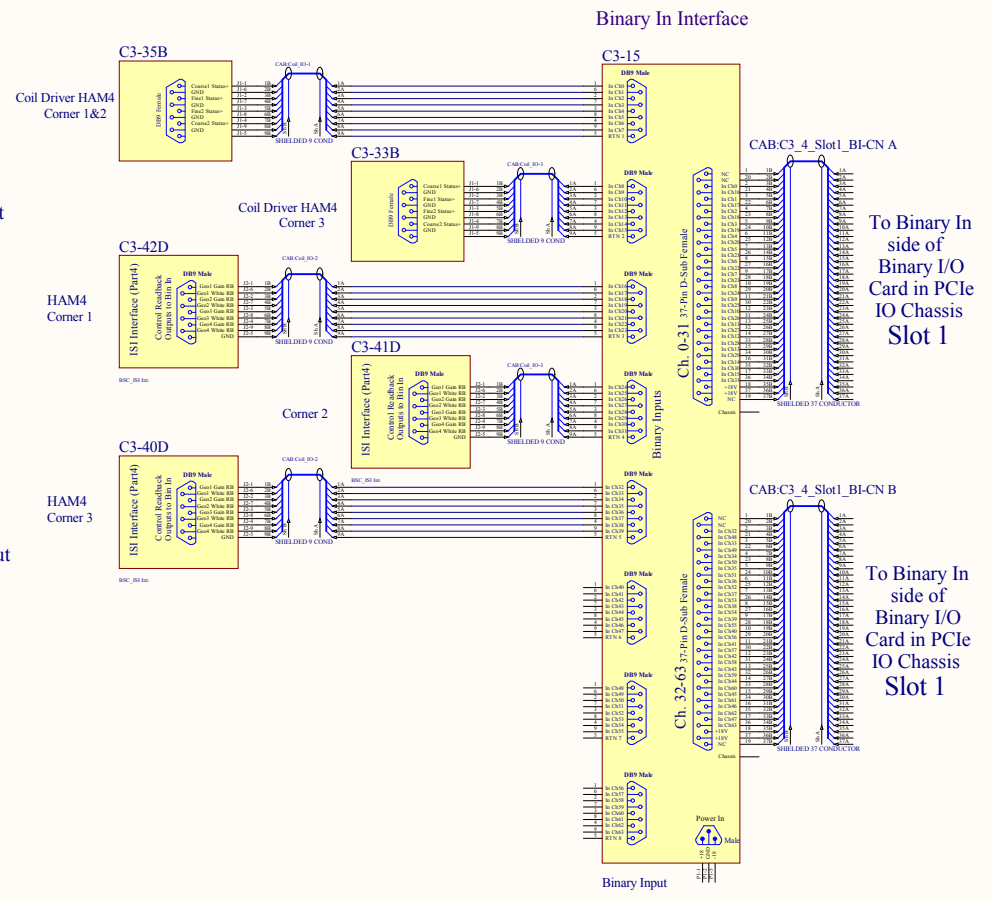


General Standards DAC  
for HAM4, IO Chassis Slot 10  
for HAM5, IO Chassis Slot 11



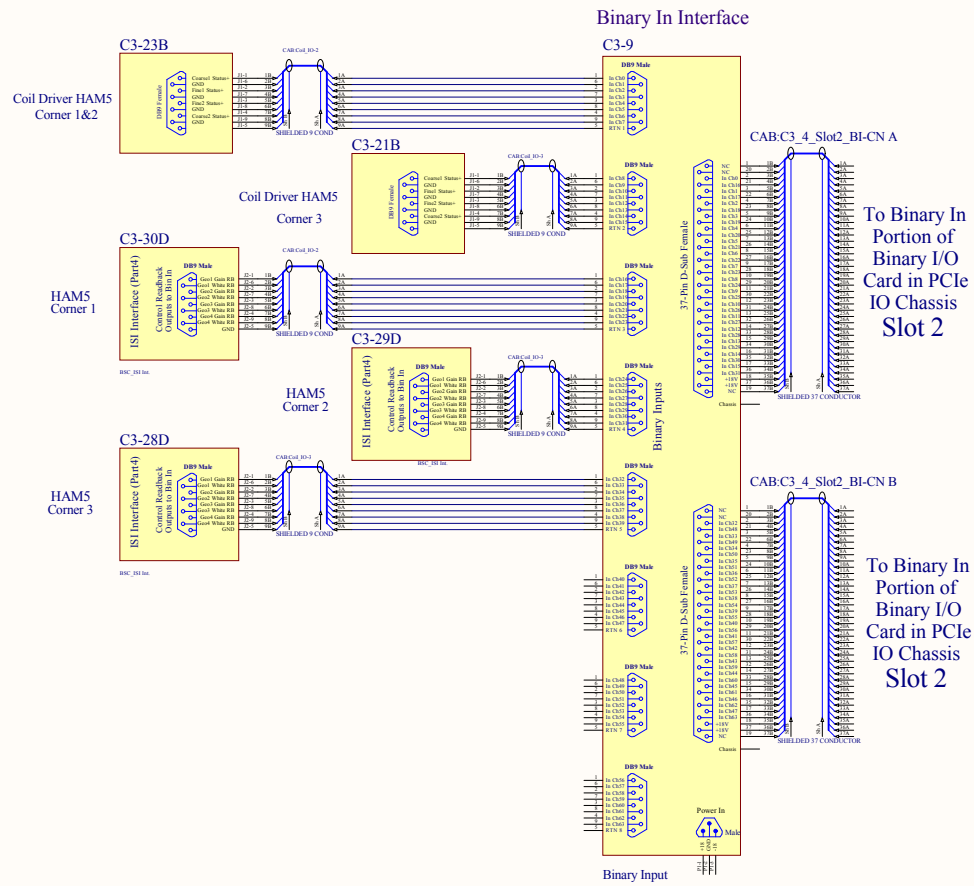
To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 1

To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 2



To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 1

To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 1

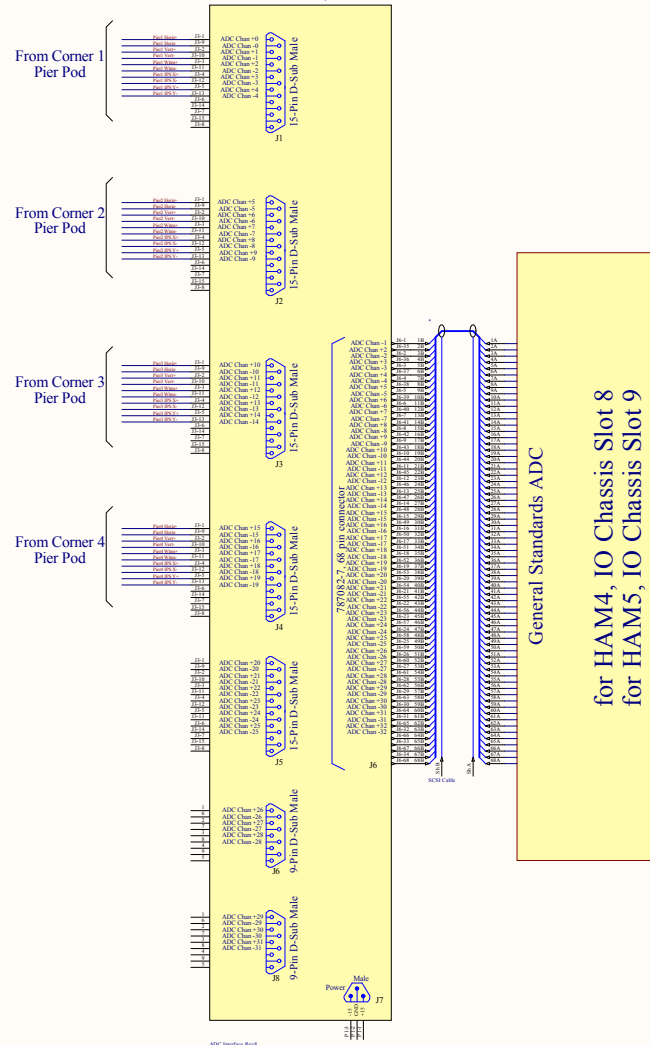


To Binary In Portion of Binary I/O Card in PCIe IO Chassis Slot 2

To Binary In Portion of Binary I/O Card in PCIe IO Chassis Slot 2



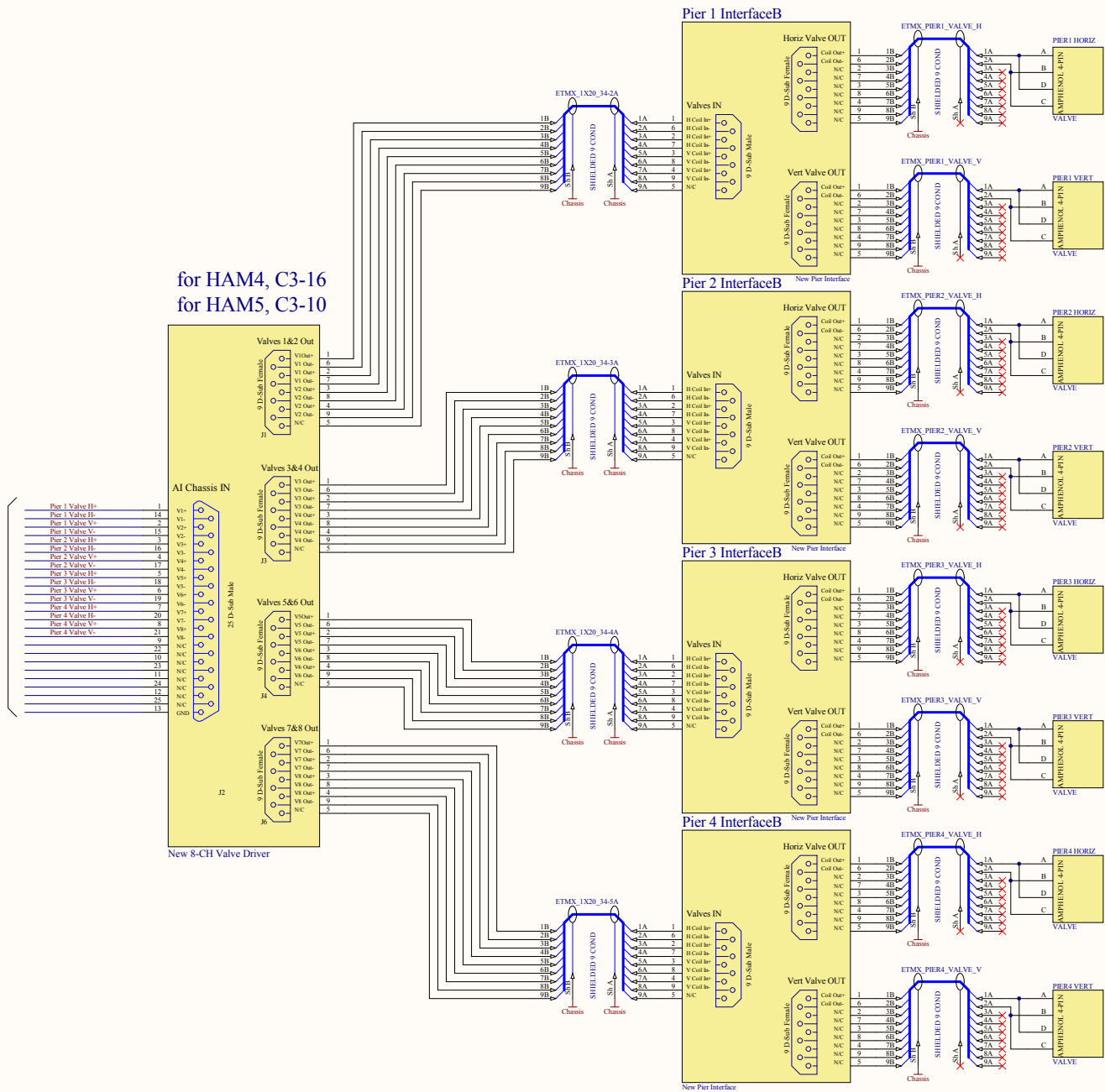
for HAM4, C3-18  
for HAM5, C3-12



General Standards ADC  
for HAM4, IO Chassis Slot 8  
for HAM5, IO Chassis Slot 9

From AI Chassis  
for HAM4, C3-36  
for HAM5, C3-24  
Page 5

for HAM4, C3-16  
for HAM5, C3-10



Title	alIGO HAM ISI System Schematics		
Size	C	DCC Number: D100298	Ligo Project California Institute of Technology Massachusetts Institute of Technology
Drawn by:	Ben Abbott	Date: 8/10/2011	Revision: v3
File: C:\rooted\Ben\Systems\ISI Single State\SI\HAM4_2_system_schematics		Print: 5:03:42 PM	Sheet 10 of 11

