

FET vs. Diode-ring Demodulator Noise Comparison

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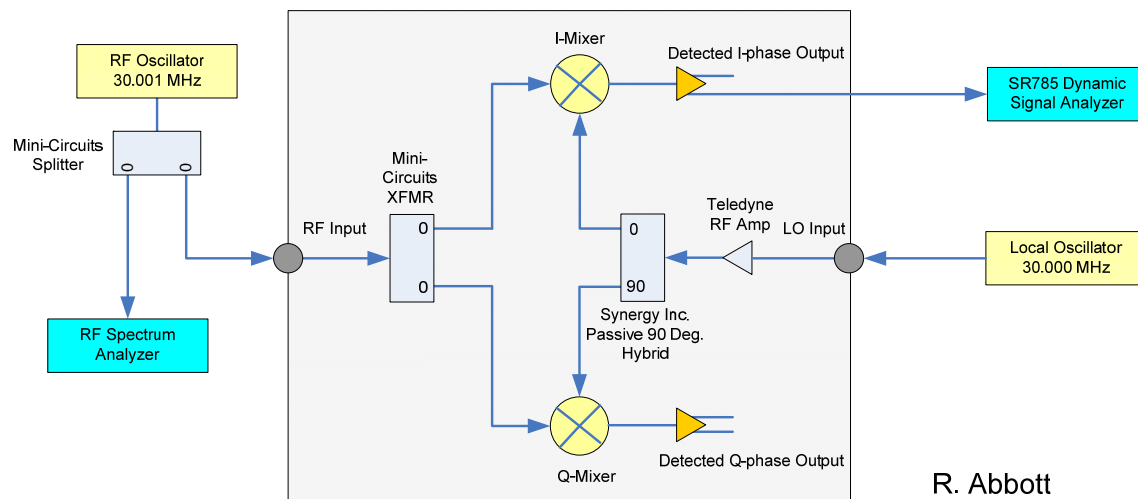
1. Overview

A side by side test has been performed to compare the noise performance of a diode-ring based I&Q demodulator cell against a FET based solution of similar architecture. In each case, the input referred noise is calculated based on the measured down-conversion gain, and the measured output noise spectral density.

2. Test Setup

As shown in figure 1, two signals are heterodyned together in the device under test. The resultant beat note is measured on an audio analyzer. The incident RF is measured by an RF signal analyzer.

Figure 1
Noise Test Setup for FET Mixer vs. Diode Ring Mixer



3. Results

As shown in table 1, relative to the theoretical thermal noise floor (-174dBm/Hz), the FET demodulator has an input referred noise floor 4.32dB higher than the diode ring. Care was taken to keep the test setup identical in order to preserve the validity of the results.

Table 1

Device Under Test (DUT)	FET Ring	Diode Ring
RF Test Frequency	30.001MHz	30.001MHz
LO Test Frequency	30.000MHz	30.000MHz
Incident RF Level	-19.33dBm	-19.33dBm
LO Level at Hybrid Input	20dBm (1)	20dBm
Detected 1kHz Beat-note on SR785	-23.129dBVrms	-20.84dBVrms
dB Gain Audio-out/RF-in (<i>calculated</i>)	9.18dB	11.47dB
SR785 Noise Floor (50 ohms termination)	-165dBVrms/rtHz	-165dBVrms/rtHz
Measured DUT Output Noise, RF-input Terminated in 50 ohms (@1kHz on SR785)	-152dBVrms/rtHz	-154dBVrms/rtHz
Voltage Noise Referred to RF-input (<i>calculated</i>)	-161.18dBVrms/rtHz	-165.5dBVrms/rtHz
RF Noise PSD Referred to RF-input (50 ohms, <i>calculated</i>)	-148dBm/Hz	-152dBm/Hz
dB Above 50 ohm Thermal Noise Floor (-174dBm/Hz, <i>calculated</i>)	25.83dB	21.51dB

4. Notes

This measurement has not been extended to calculate the actual conversion loss of each mixer cell; the actual noise figures quoted are not to be interpreted as conversion loss. No attempt was made to study the shape of the audio noise spectra at frequencies other than the quoted 1 kHz.

(1) 20dBm at the hybrid input of the FET mixer results in 3.8vp-p at each LO pin of the FET quad pack. This means there is 7.6vp-p differential across the LO+ and LO- pins of the FET quad. The FET quad was transformer coupled with a center tapped secondary, 4:1 transformer. Each half of the center tapped secondary was terminated with a 100 ohm resistor.