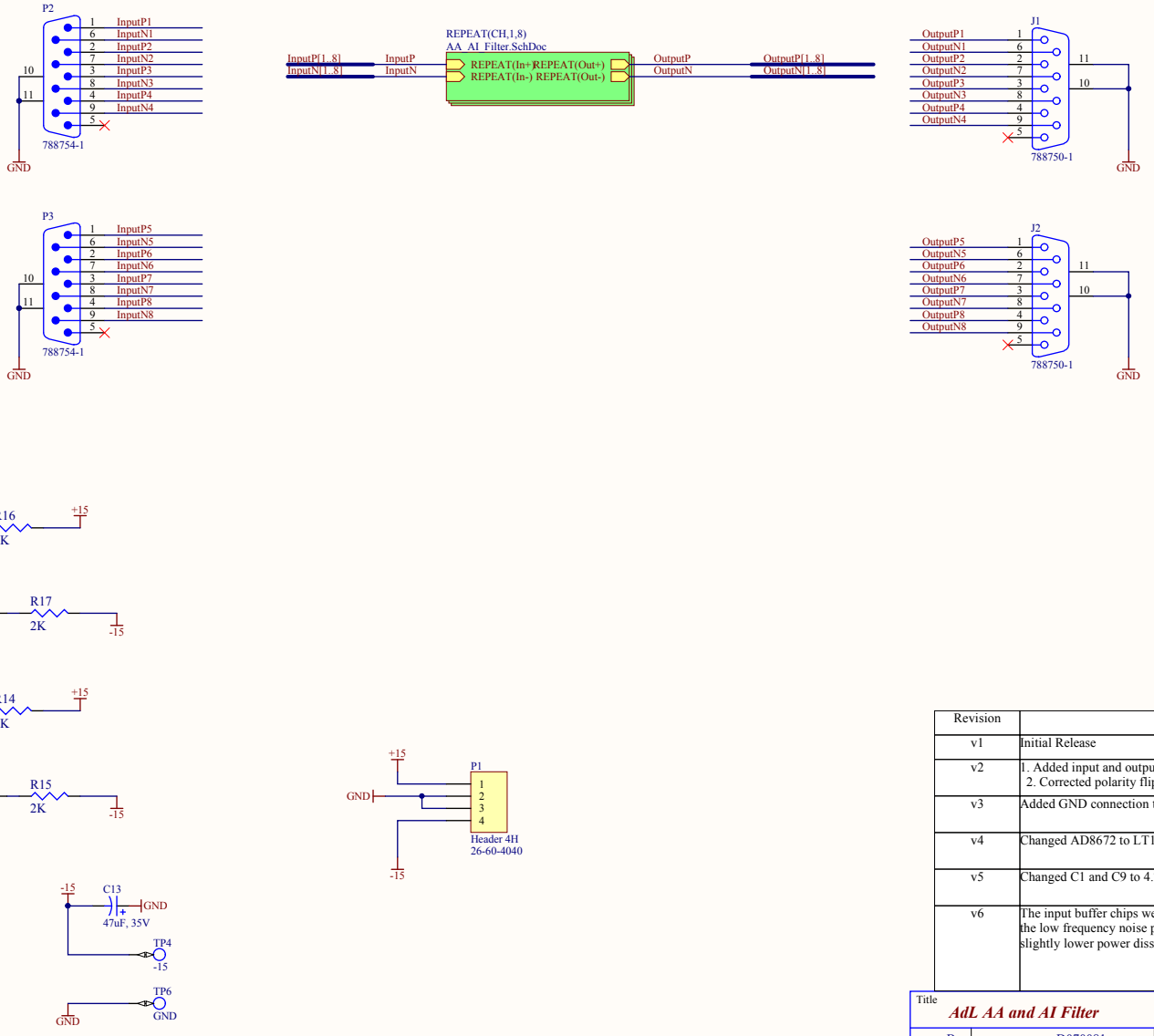
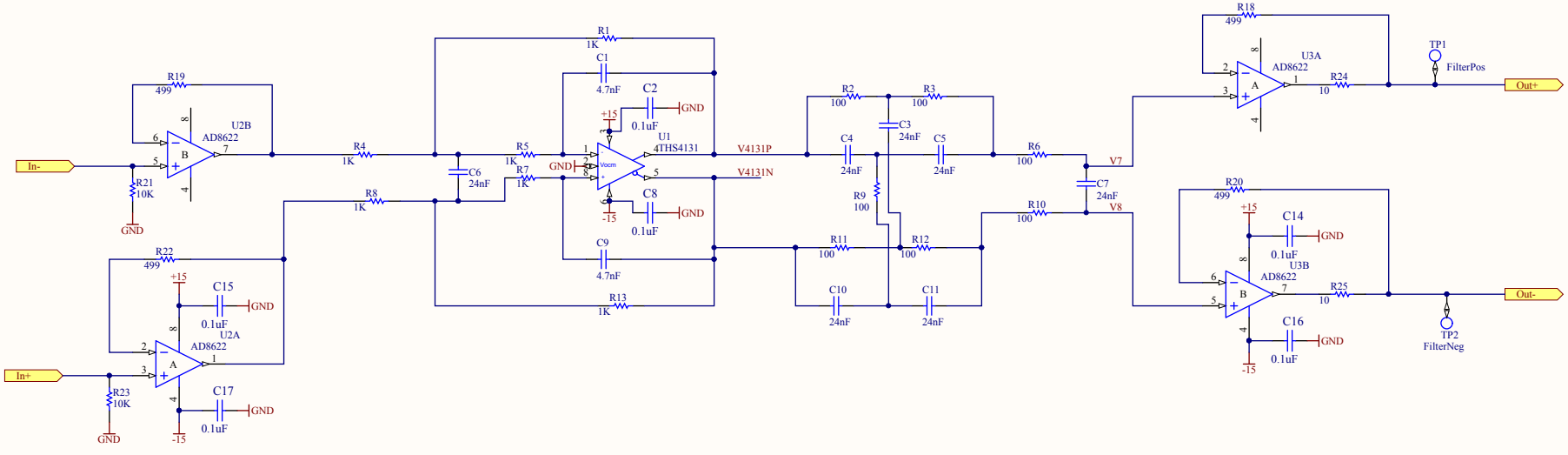


Revision	Description	Date
v1	Initial Release	7/2007
v2	1. Added input and output buffers to each channel. 2. Corrected polarity flip in each channel from version v1.	9/2009
v3	Added GND connection to Vocm pin of THS4131	5/2010
v4	Changed AD8672 to LT1492 low power op amp	10/2010
v5	Changed C1 and C9 to 4.7nF	1/2011
v6	The input buffer chips were changed from LT1492 to AD8622. This solved some of the low frequency noise problems that exist with the LT1492. The AD8622 has slightly lower power dissipation as well.	2/2012

Title: **AdL AA and AI Filter**			*LIGO Project* *California Institute of Technology* *Massachusetts Institute of Technology*	LIGO
Size: B	DCC Number: D070081	SCH / PCB Revision: v6	Engineer: J. Heefner	Date: 6/13/2012 Time: 5:06:29 PM
File: C:\Rich's Files\Mycadfiles\AA Filter\AA_AI_FilterRevV5_Project\AA_AI_Filter_RevV5\Board.SchDoc				Sheet 1 of 2

3rd order 10KHz LPF, notch at 65536Hz



Title: *AdL AA and AI Filter*			*LIGO Project* *California Institute of Technology* *Massachusetts Institute of Technology*	LIGO
Size: B	DCC Number: D070081	SCH / PCB Revision: v5	Engineer: J. Heefner	Date: 6/13/2012 Time: 5:06:29 PM
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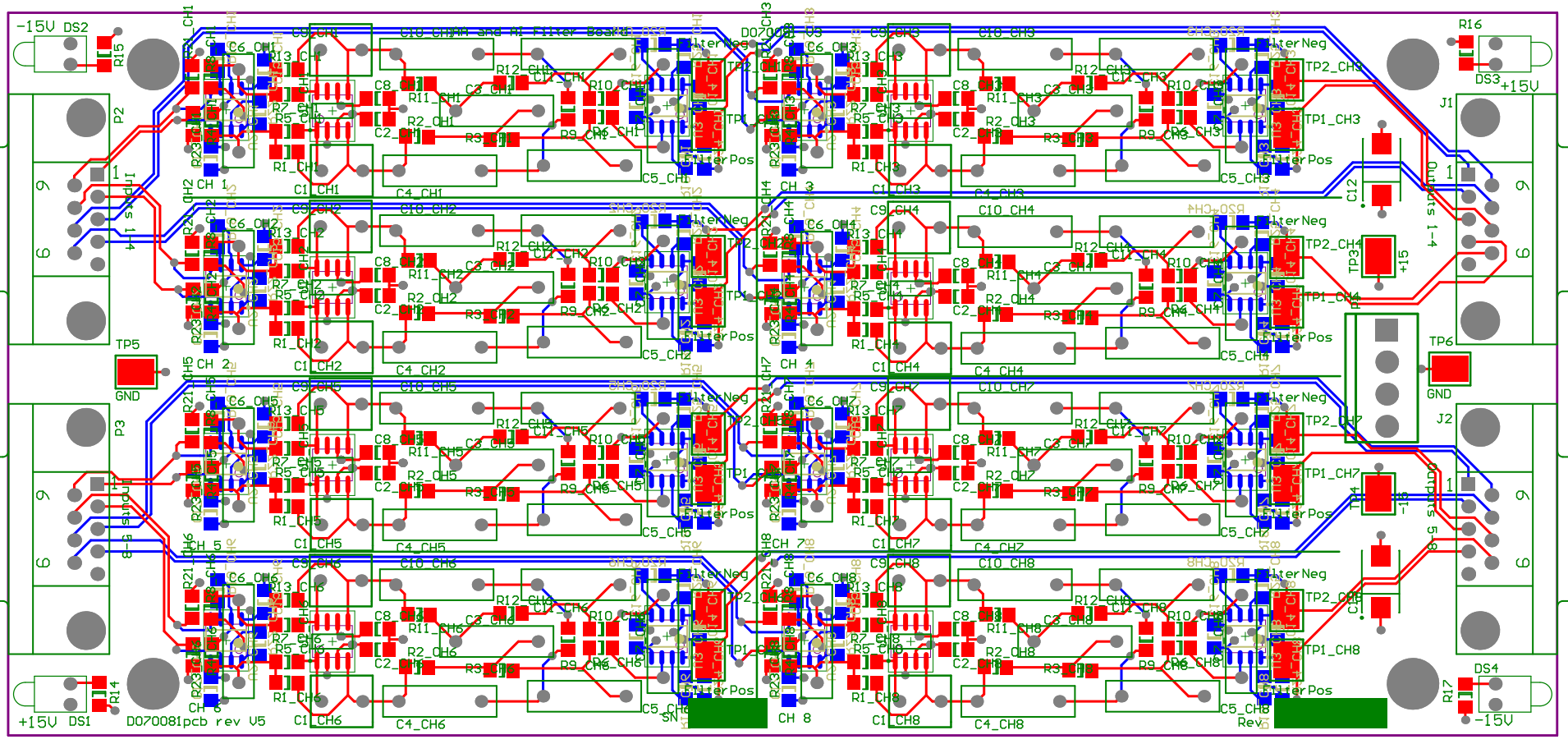
-15V DS2
+15V DS1
DS3 +15V
DS4 -15V
R16
R15
R14
R17
P2
P3
J1
J2
Inputs 1-4
Inputs 5-8
Outputs 1-4
Outputs 5-8
TP5
TP6
GND
TP3 +15
TP4 -15
C12
C13
CH 1
CH 2
CH 3
CH 4
CH 5
CH 6
CH 7
CH 8
DO70081pcb rev V5
SN
Rev
C1_CH1
C4_CH1
C5_CH1
C1_CH2
C4_CH2
C5_CH2
C1_CH3
C4_CH3
C5_CH3
C1_CH4
C4_CH4
C5_CH4
C1_CH5
C4_CH5
C5_CH5
C1_CH6
C4_CH6
C5_CH6
C1_CH7
C4_CH7
C5_CH7
C1_CH8
C4_CH8
C5_CH8
TP1_CH1
TP2_CH1
TP1_CH2
TP2_CH2
TP1_CH3
TP2_CH3
TP1_CH4
TP2_CH4
TP1_CH5
TP2_CH5
TP1_CH6
TP2_CH6
TP1_CH7
TP2_CH7
TP1_CH8
TP2_CH8