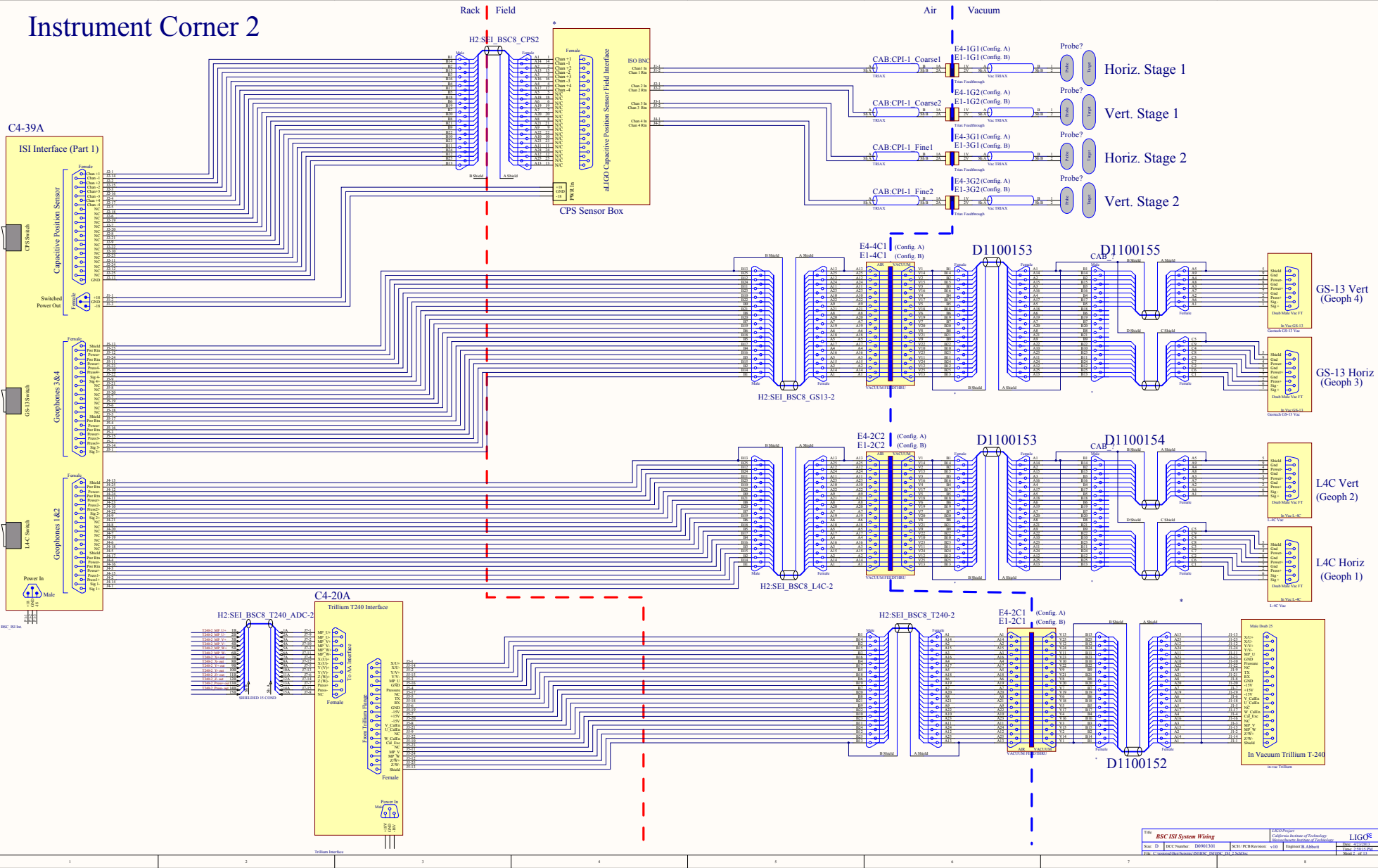
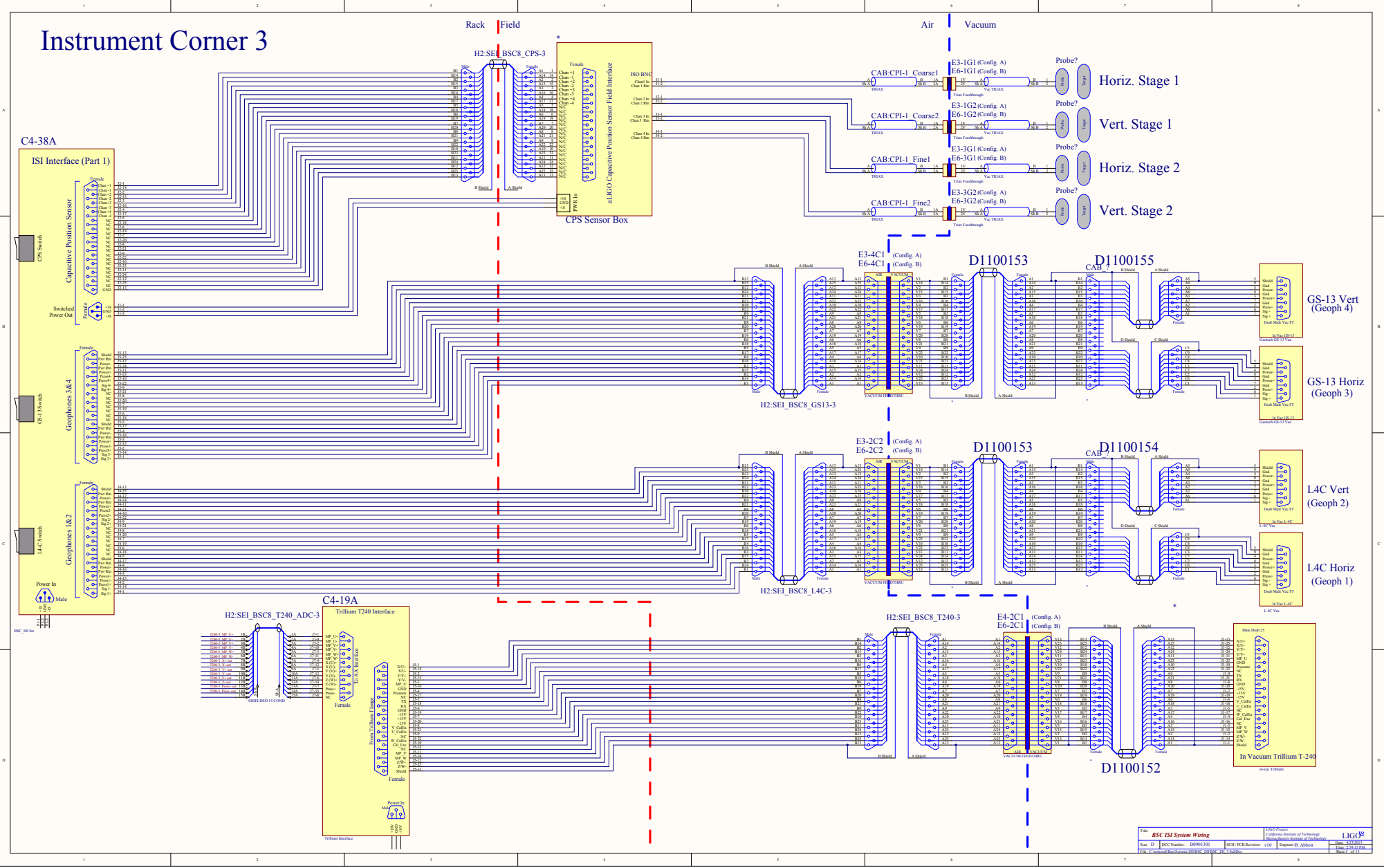


Instrument Corner 2



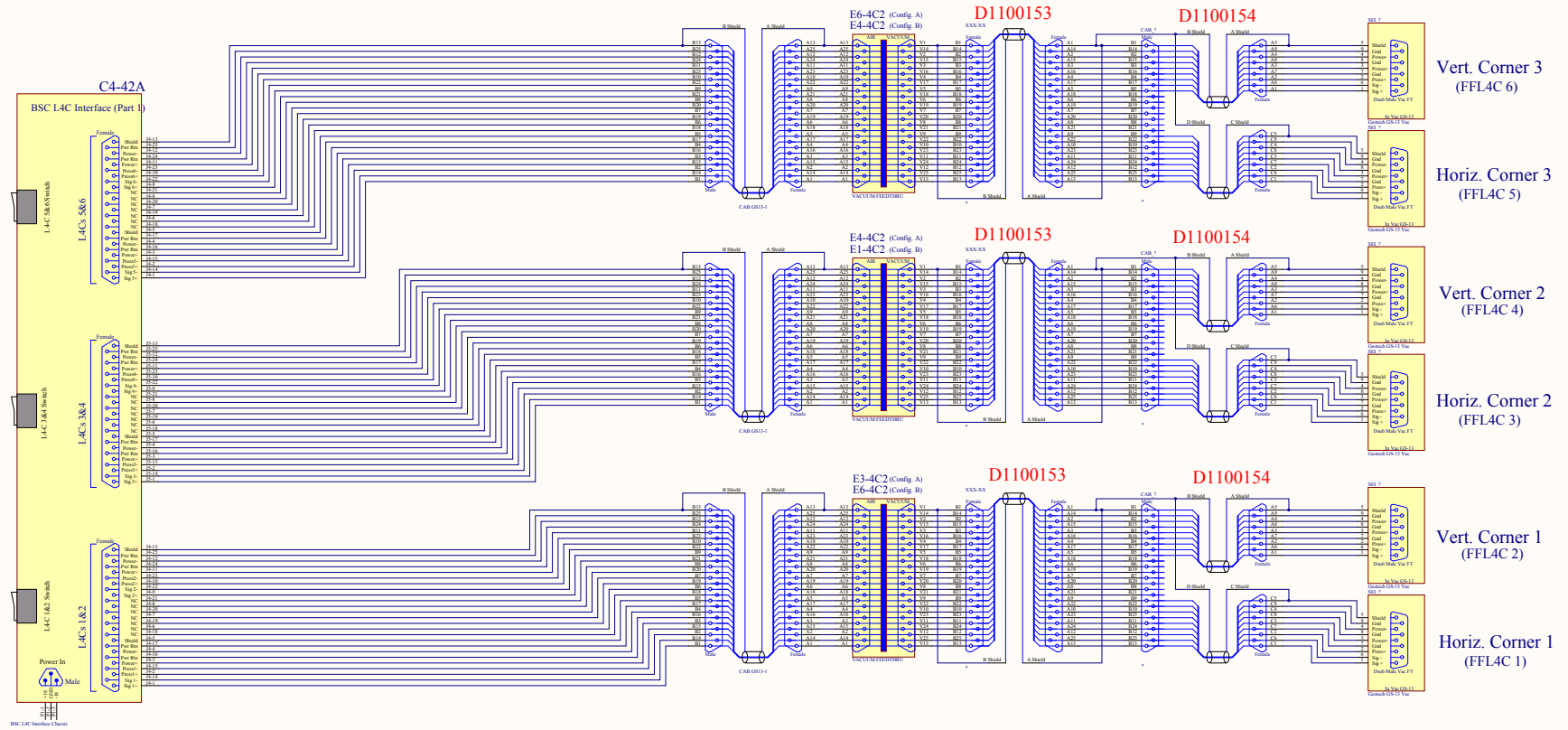
Instrument Corner 3



L4C Feed Forward Geophones

This page has not been implemented yet.

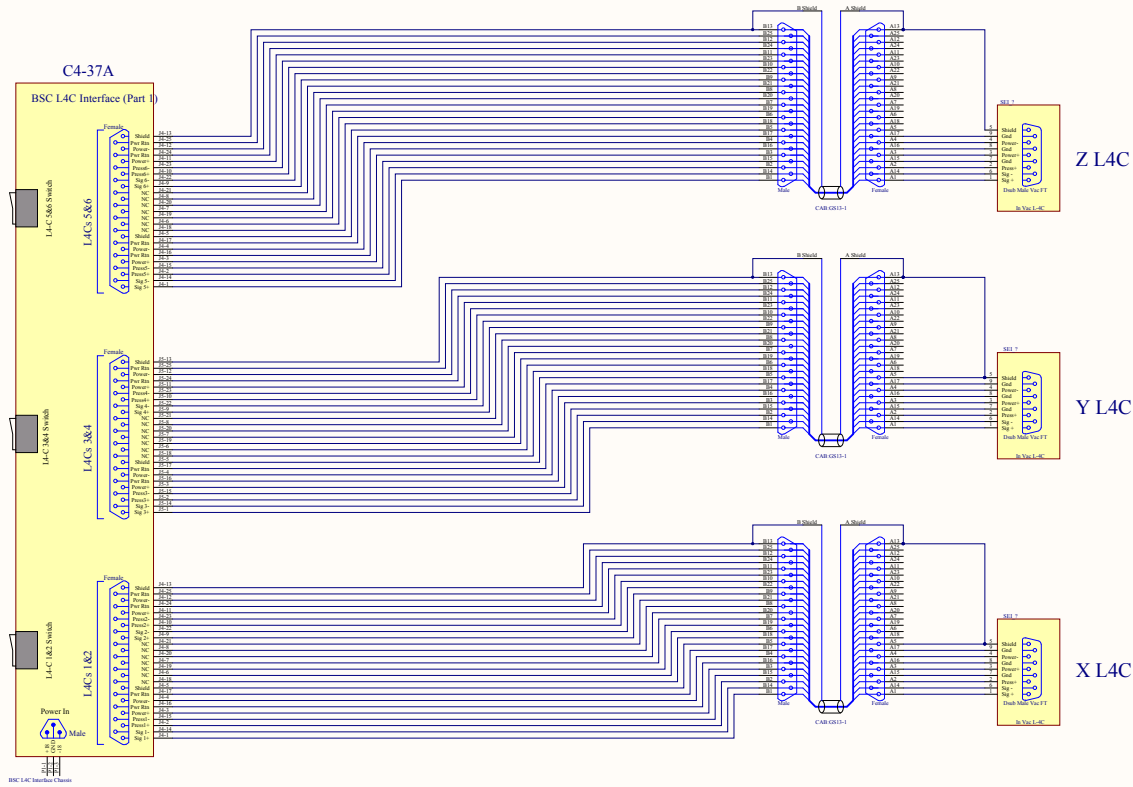
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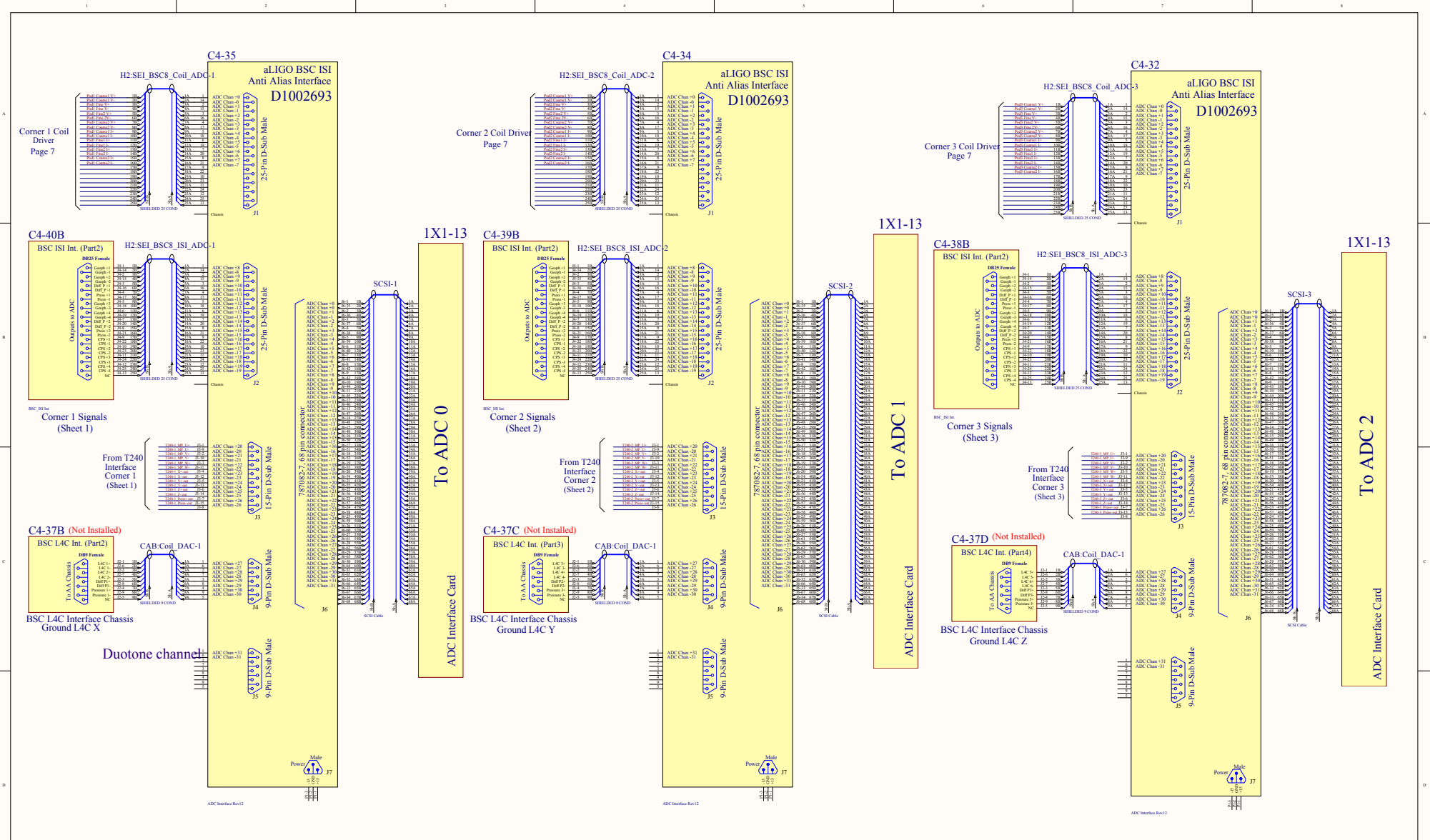


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Rev:	D	DSC Number: 020901.001	4200 Project
Rev. Date:	Rev: 02/20/13	Revision: 1/10	Customer: Bureau of Reclamation Division: Bureau of Technology
Rev. Author:	Rev: 02/20/13	Revision: 1/10	Drawn: John Kim Checked: Alan Approved: Alan

This page has not been implemented yet.

L4C Ground Sense geophones

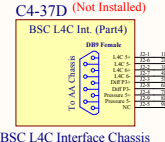
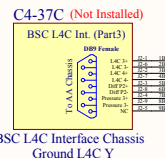
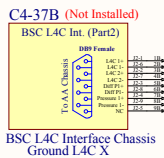
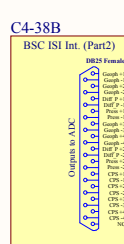
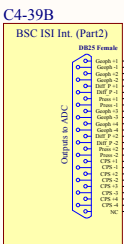
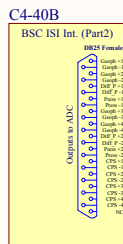




Corner 1 Coil Driver
Page 7

Corner 2 Coil Driver
Page 7

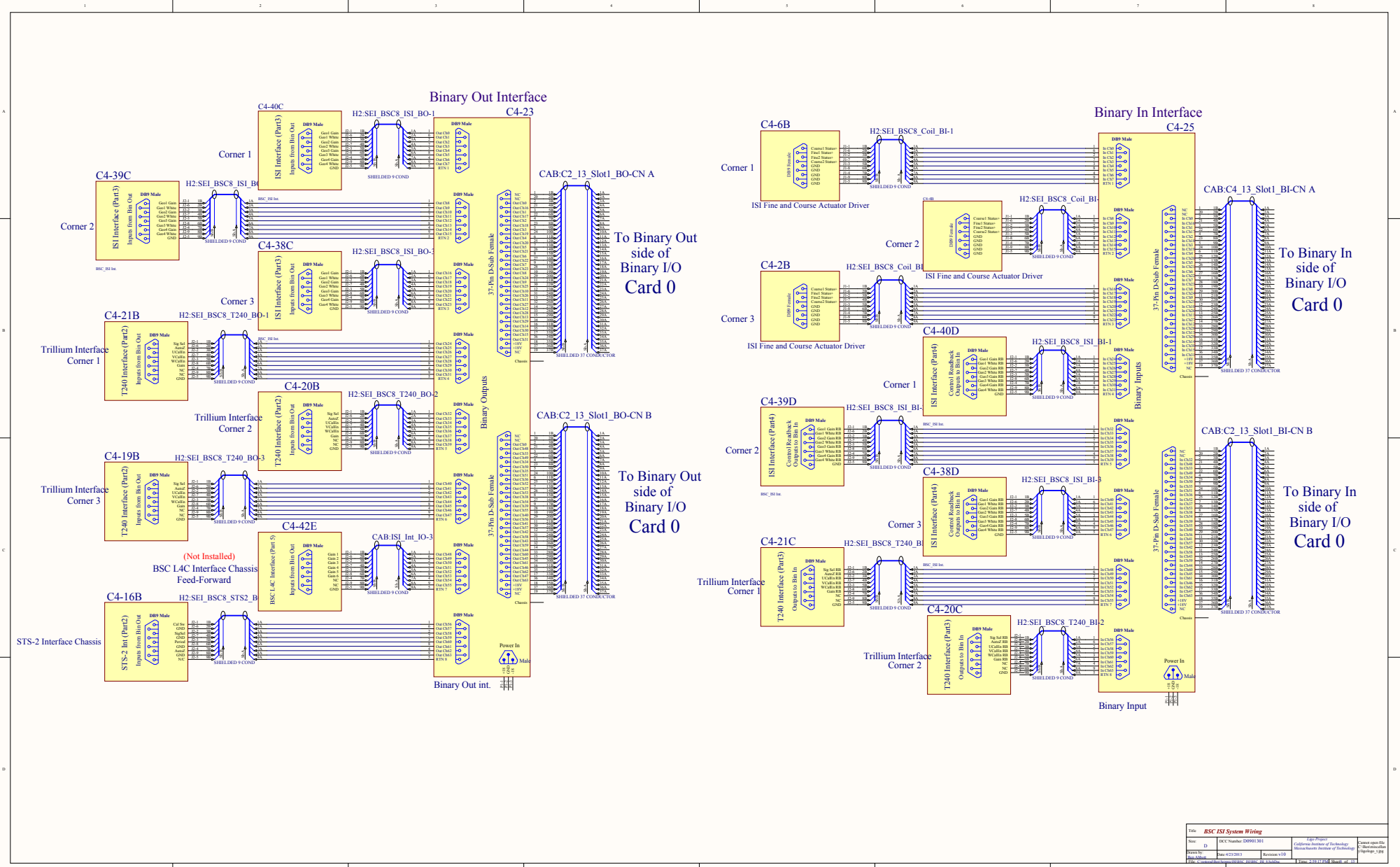
Corner 3 Coil Driver
Page 7



1X1-13
To ADC 0
ADC Interface Card

1X1-13
To ADC 1
ADC Interface Card

1X1-13
To ADC 2
ADC Interface Card



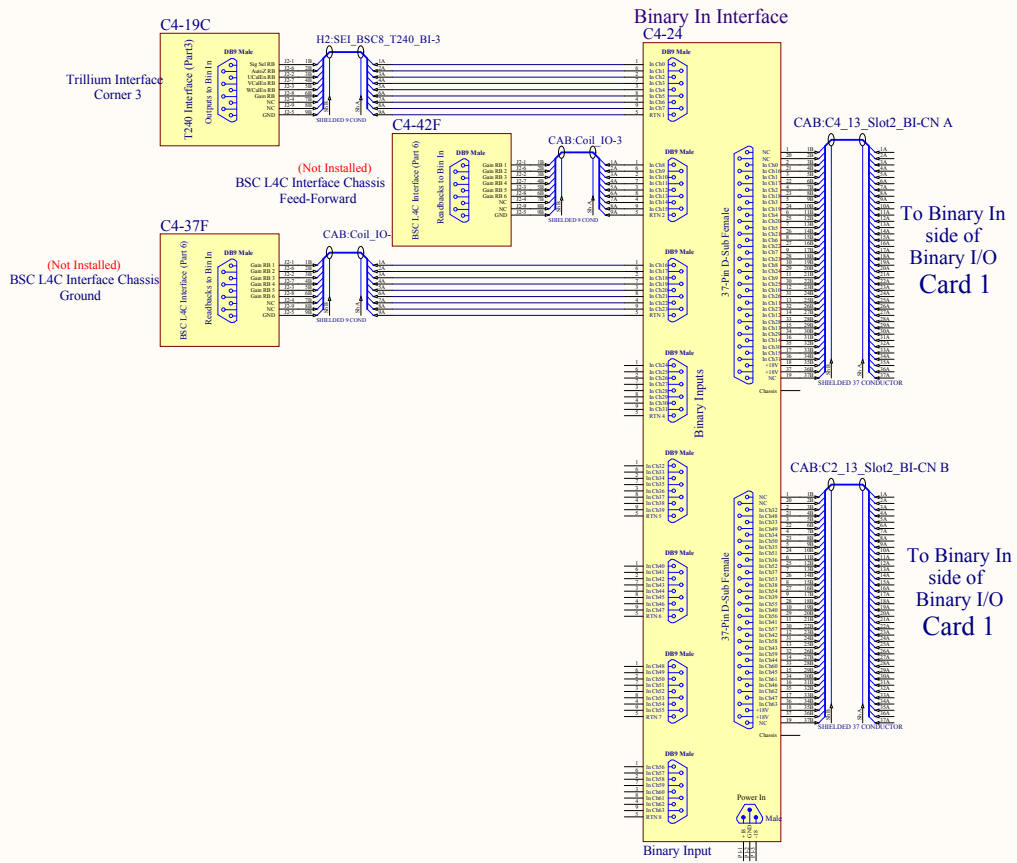
To Binary Out
side of
Binary I/O
Card 0

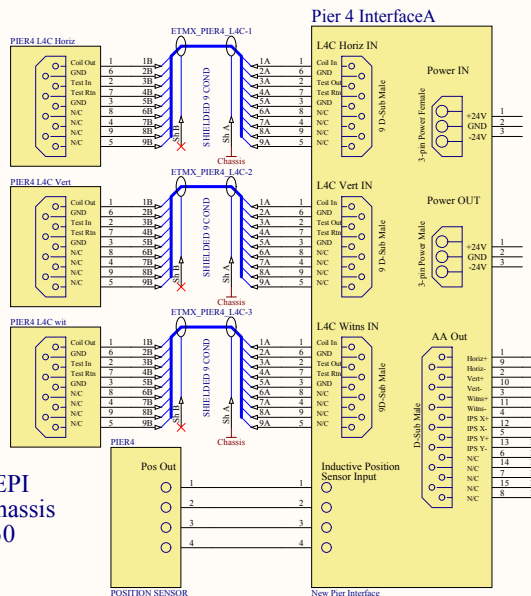
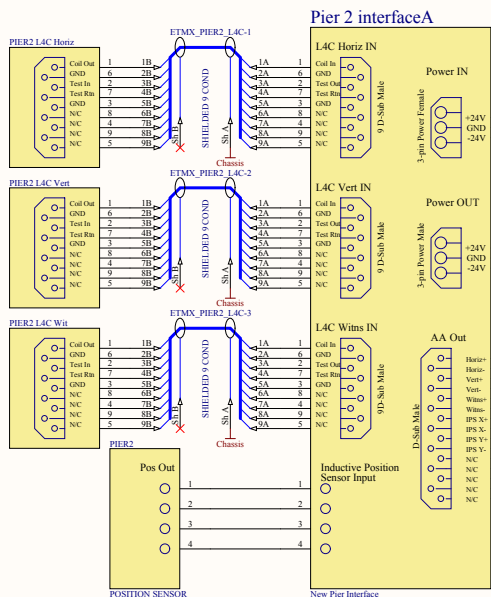
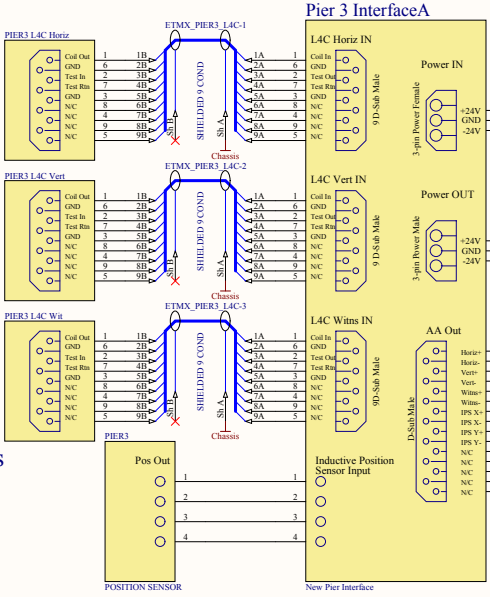
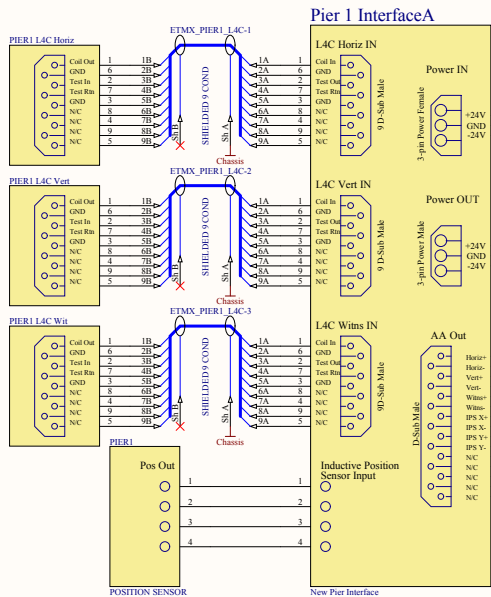
To Binary Out
side of
Binary I/O
Card 0

To Binary In
side of
Binary I/O
Card 0

To Binary In
side of
Binary I/O
Card 0

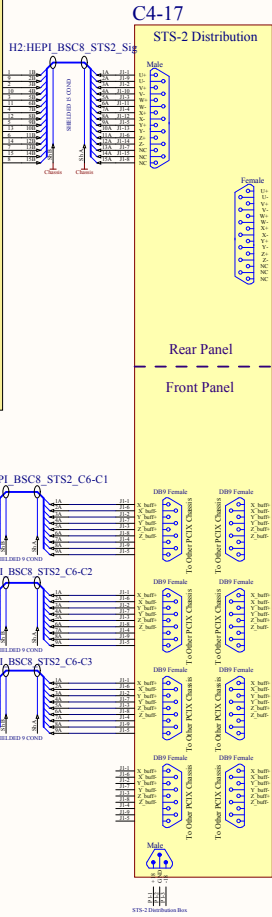
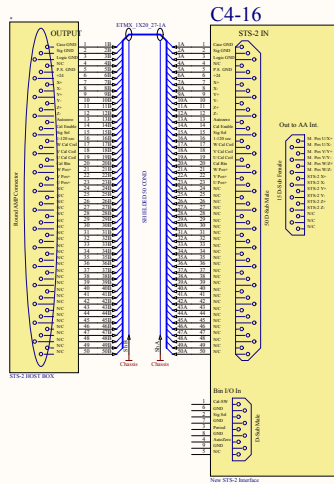
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Author: D	DOC Number: 020901.001	4400 Project:	Drawn and checked by: D
Issue: 1	Revision: 1.0	Customer: American Institute of Technology	Checked by: D
File Name: BSC-4252013	Revision: 1.0	Customer: American Institute of Technology	Checked by: D
Date: 11/11/2013 10:00:00 AM			





Title ALIGO BSC ISI HEPI System Schematics			
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Drawn by: Ben Abbott	Date:4/23/2013	Revision:v10	
File: C:\instrom\Bsc\Scheme\BSC_ISI\HEPI_SchDoc Title: 2:59:17 PM Sheet 10 of 13			

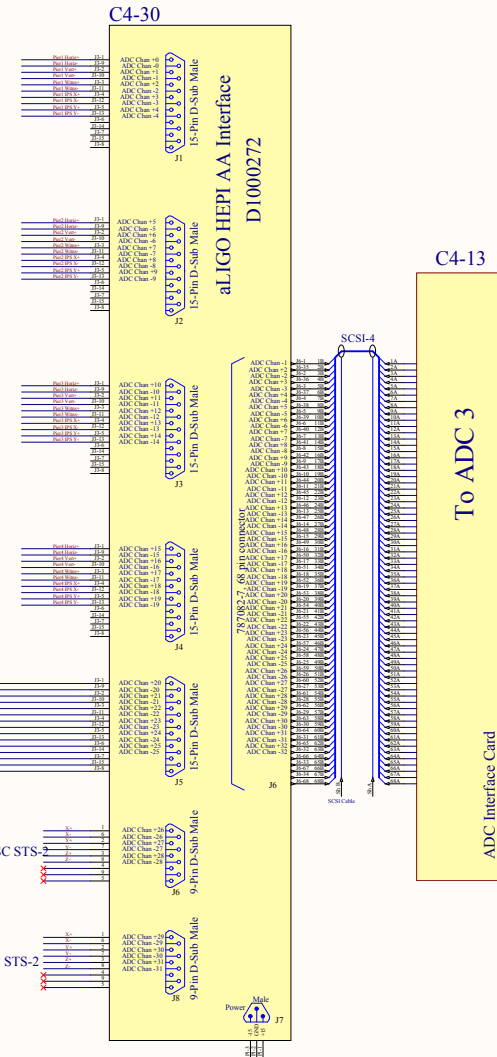
STS-2 Distribution Chassis not needed on end stations.
(Only One STS-2 at each end)



These signals go To the HAM ISI Spare ADC Inputs

These signals go To the other 2 BSC ADC Inputs Like these:

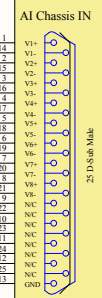
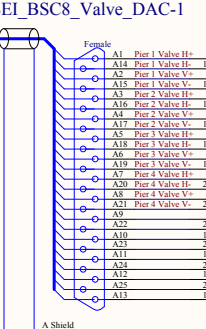
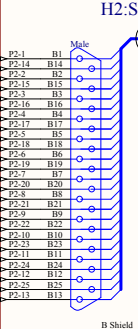
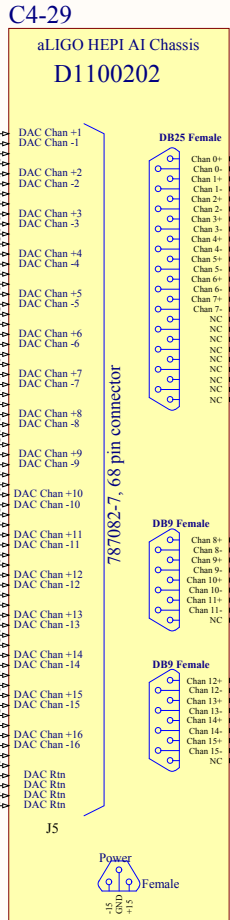
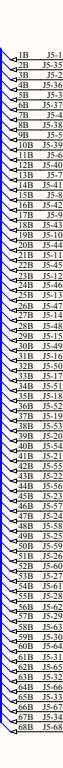
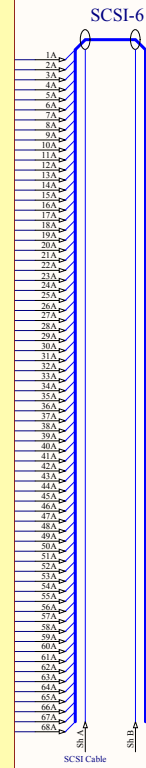
From Second BSC STS-2
From Third BSC STS-2



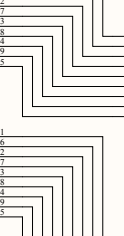
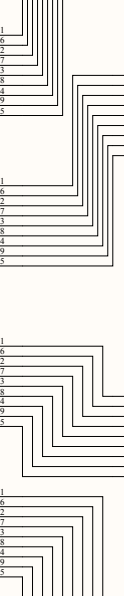
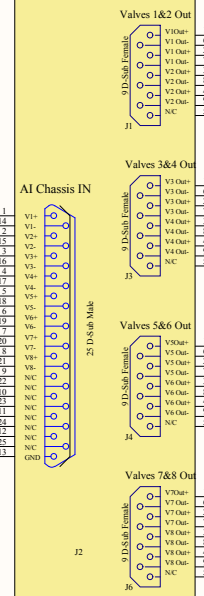
1X1-13

To DAC 0

DAC Interface Card



C4-27

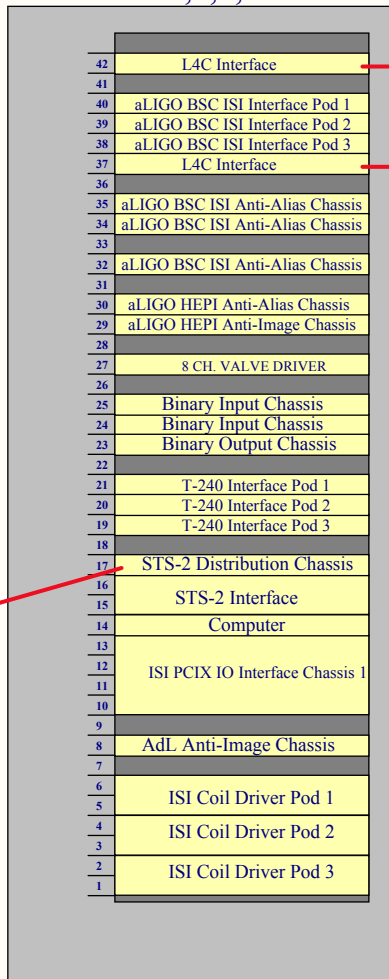


L1-SEI-C4, C5, C6, and Ends BSC 1,2,3,4&5

Per Chamber Per aLIGO (x16 BSCs)

BSC AA Chassis = 3	48
HEPI AA Chassis = 1	16
HEPI AI Chassis = 1	16
aLIGO AI Chassis = 1	16
Filter Boards = 20	320
Binary In Chassis = 2	32
Binary Out Chassis = 1	16
BSC ISI Int = 3	48
Trillium Chassis = 3	48
8Ch. Valve = 1	16
STS Dist Chassis = 1	16
STS Int. Chassis = 1	16
Coil dvr. Chassis = 3	48

STS-2 Distribution not
Installed at End Stations.



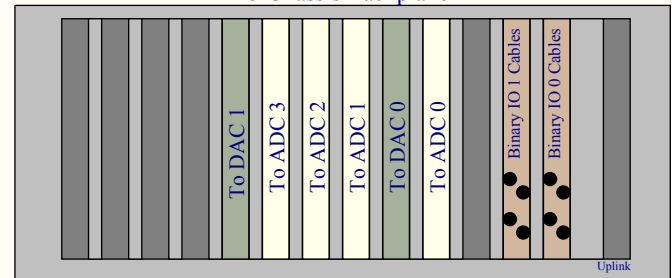
L4C Interface Not Installed

L4C Interface Not Installed

HEPI Components

STS-2 Input
/Distribution

IO Chassis Backplane



Title BSC ISI System Wiring			
Size: B	DCC Number: D0901301		Ligo Project California Institute of Technology Massachusetts Institute of Technology
Drawn by: Ben Abbott	Date: 4/23/2013	Revision: v10	Cannot open file C:\Ben\miscellan y\ligologo_1.jpg
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