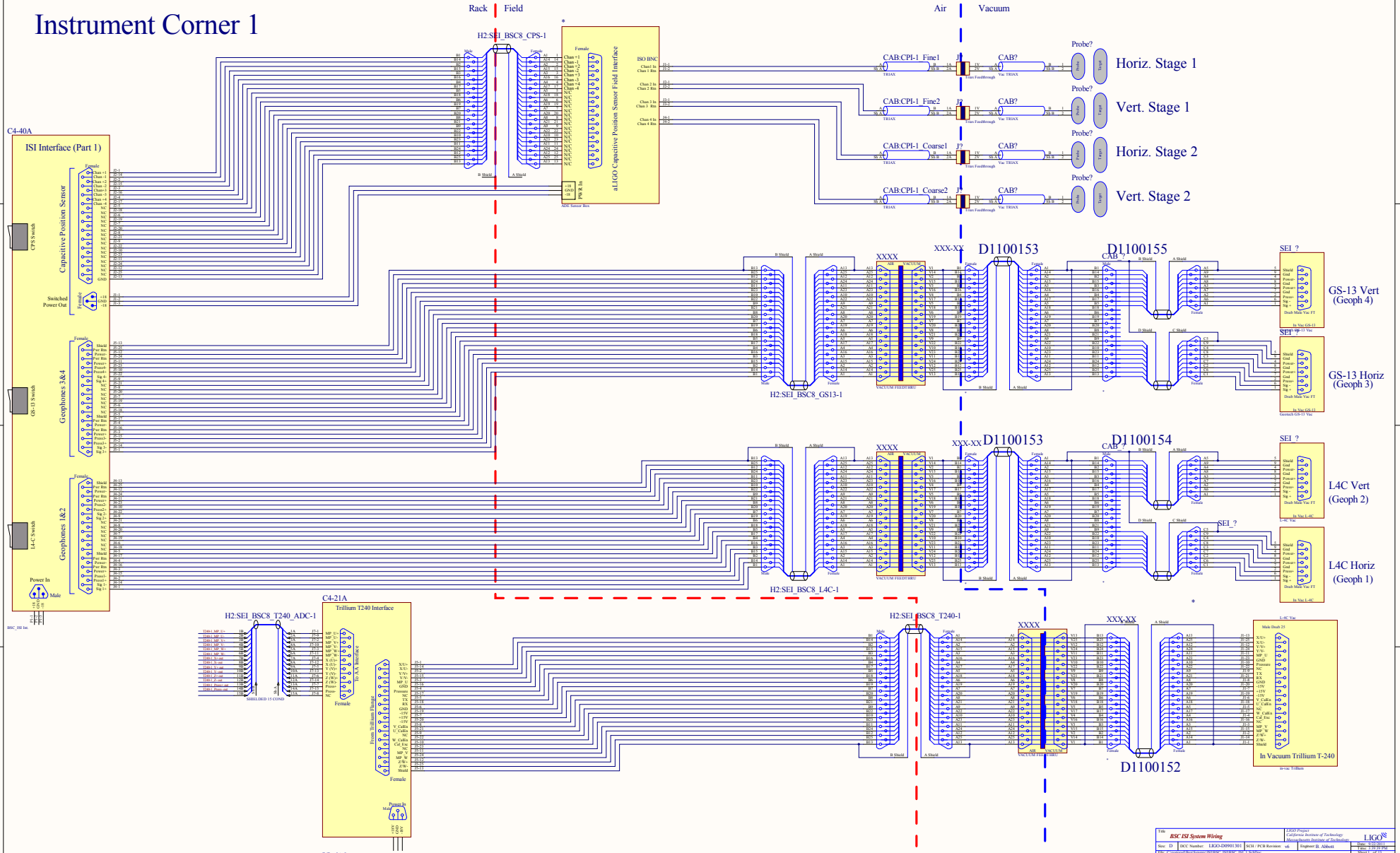
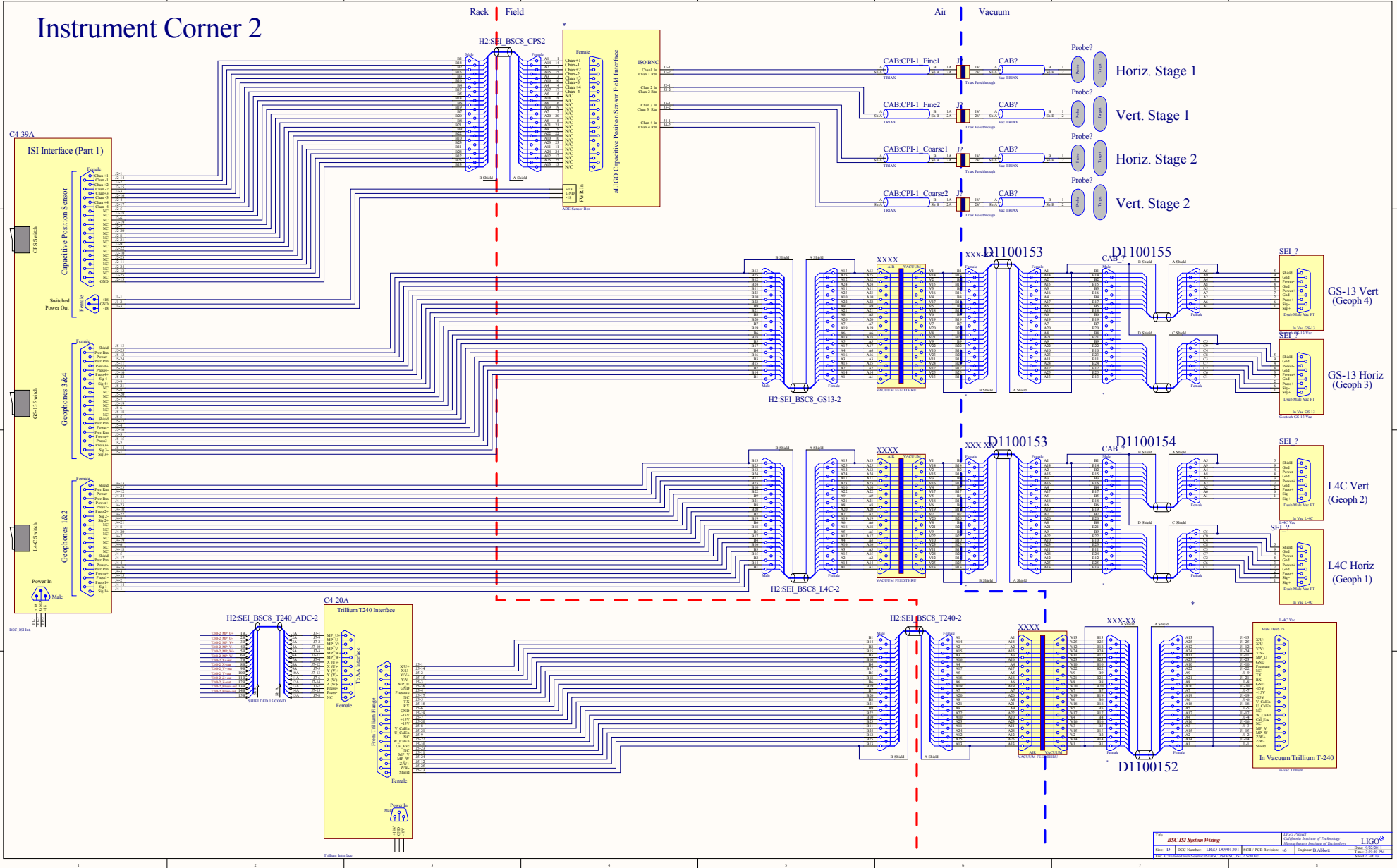


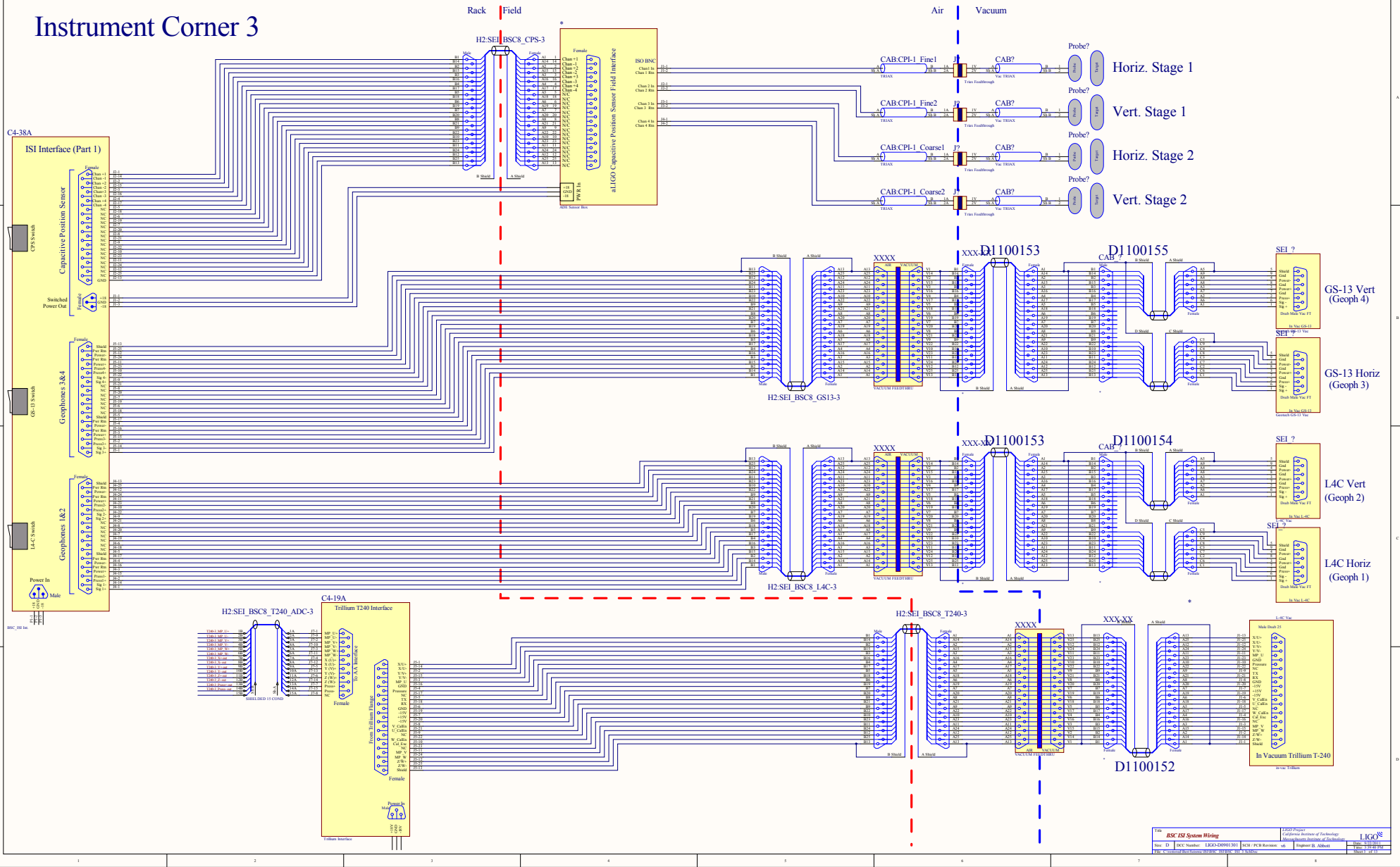
Instrument Corner 1



Instrument Corner 2

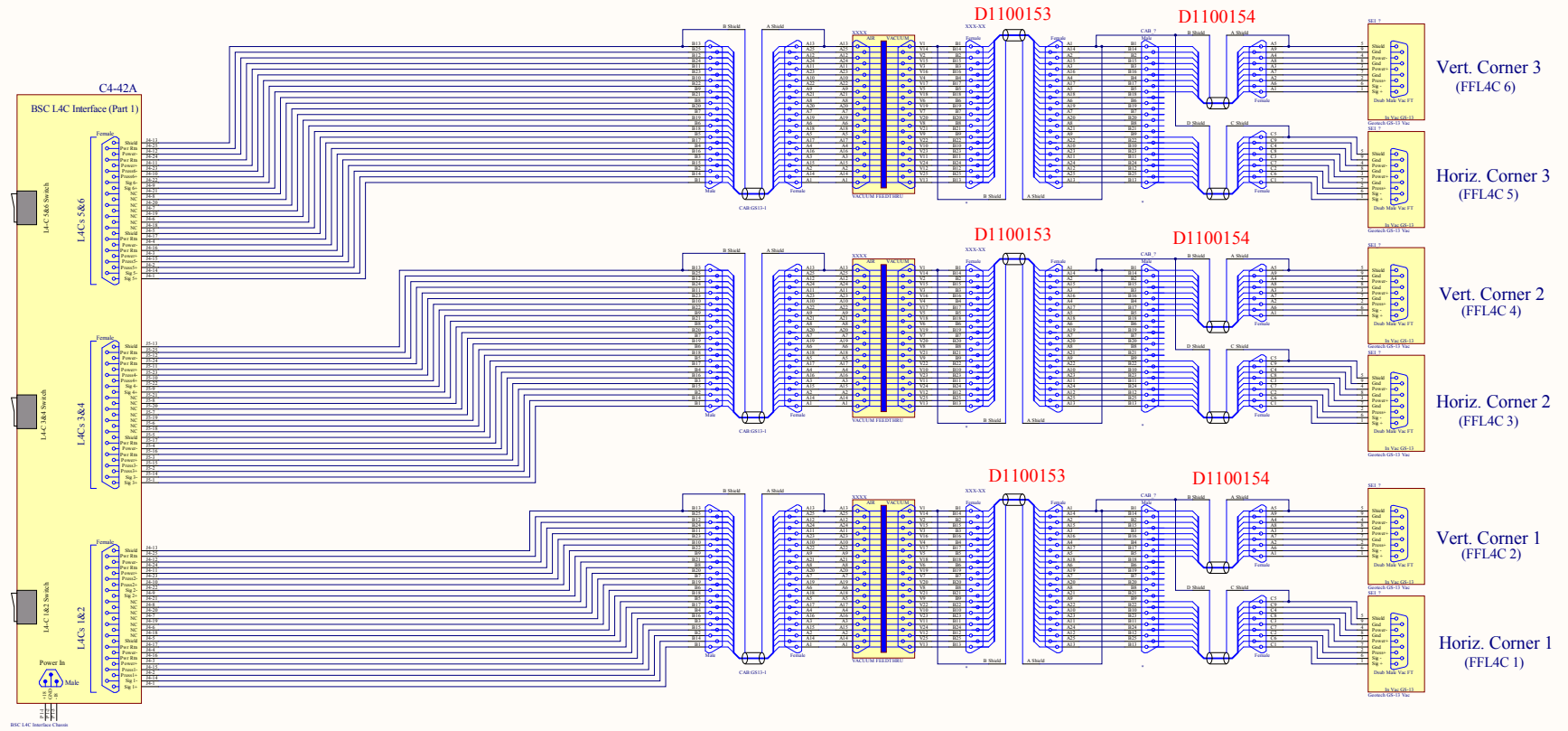


Instrument Corner 3

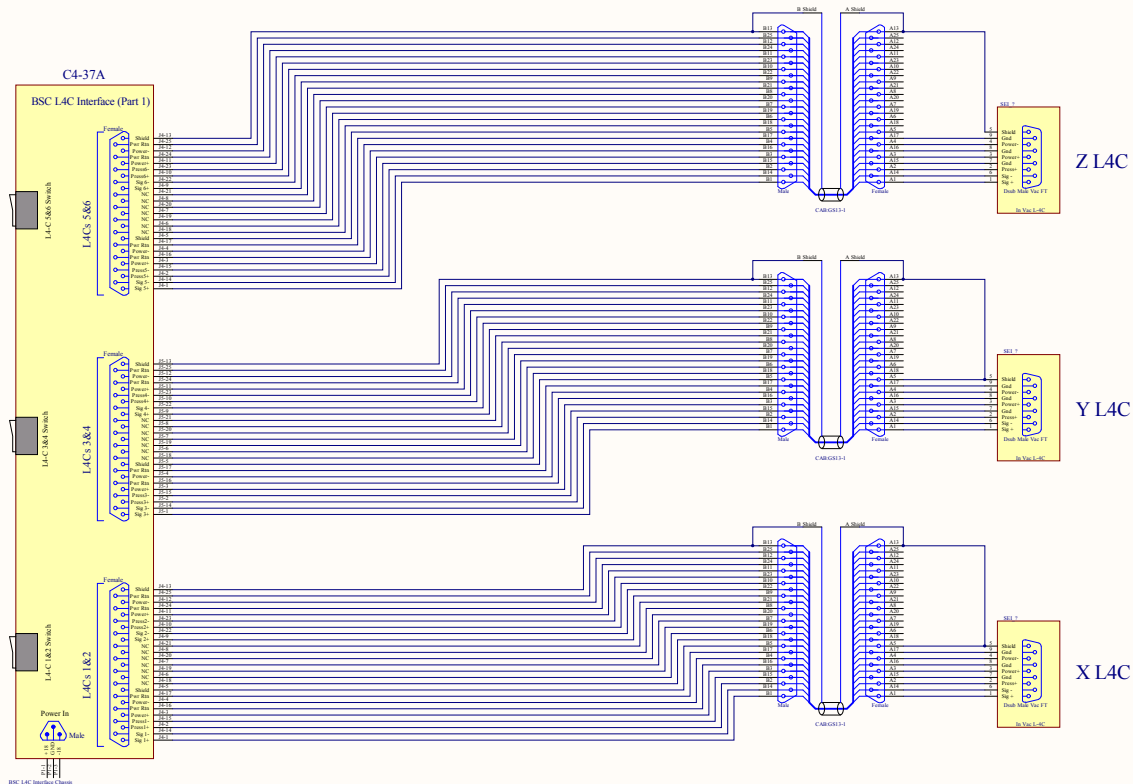


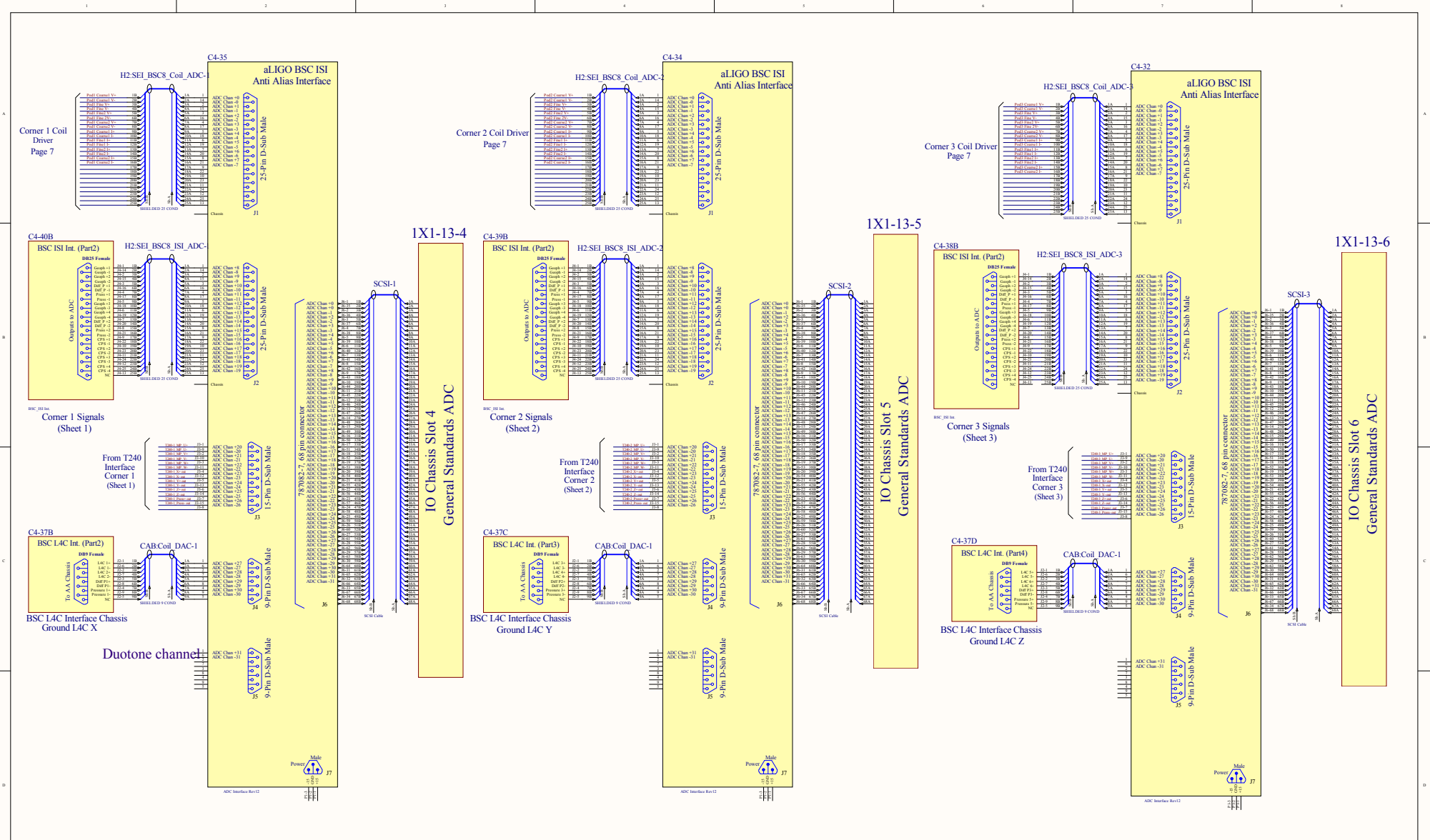
L4C Feed Forward Geophones

Red cables have not been ordered yet.



L4C Ground Sense geophones





Corner 1 Coil Driver
Page 7

Corner 2 Coil Driver
Page 7

Corner 3 Coil Driver
Page 7

C4-40B
BSC ISI Int. (Part2)

Corner 1 Signals
(Sheet 1)

From T240
Interface
Corner 1
(Sheet 1)

C4-37B
BSC L4C Int. (Part2)

BSC L4C Interface Chassis
Ground L4C X

Duotone channel

1X1-13-4

IO Chassis Slot 4
General Standards ADC

C4-39B
BSC ISI Int. (Part2)

Corner 2 Signals
(Sheet 2)

From T240
Interface
Corner 2
(Sheet 2)

C4-37C
BSC L4C Int. (Part3)

BSC L4C Interface Chassis
Ground L4C Y

1X1-13-5

IO Chassis Slot 5
General Standards ADC

C4-38B
BSC ISI Int. (Part2)

Corner 3 Signals
(Sheet 3)

From T240
Interface
Corner 3
(Sheet 3)

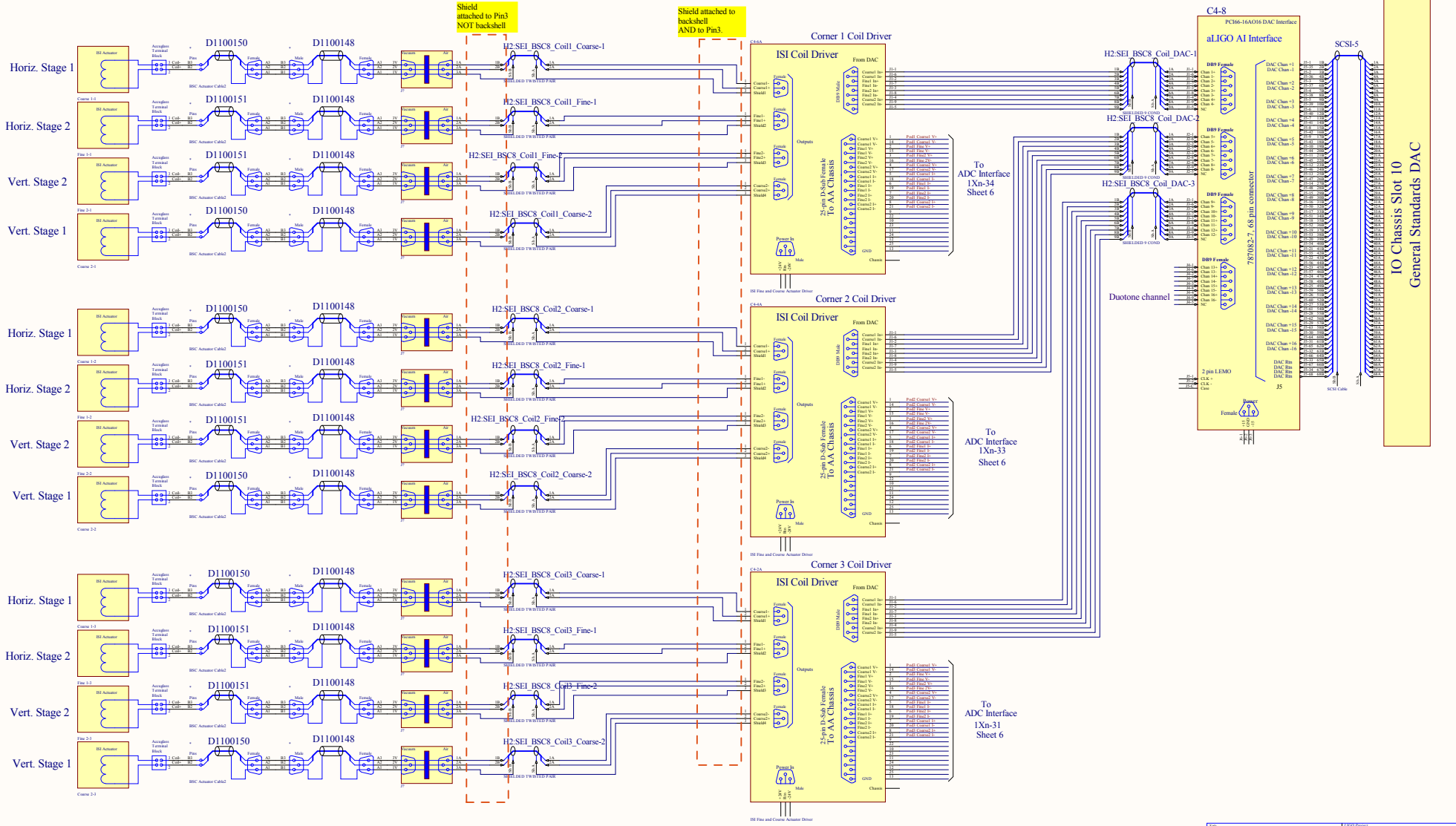
C4-37D
BSC L4C Int. (Part4)

BSC L4C Interface Chassis
Ground L4C Z

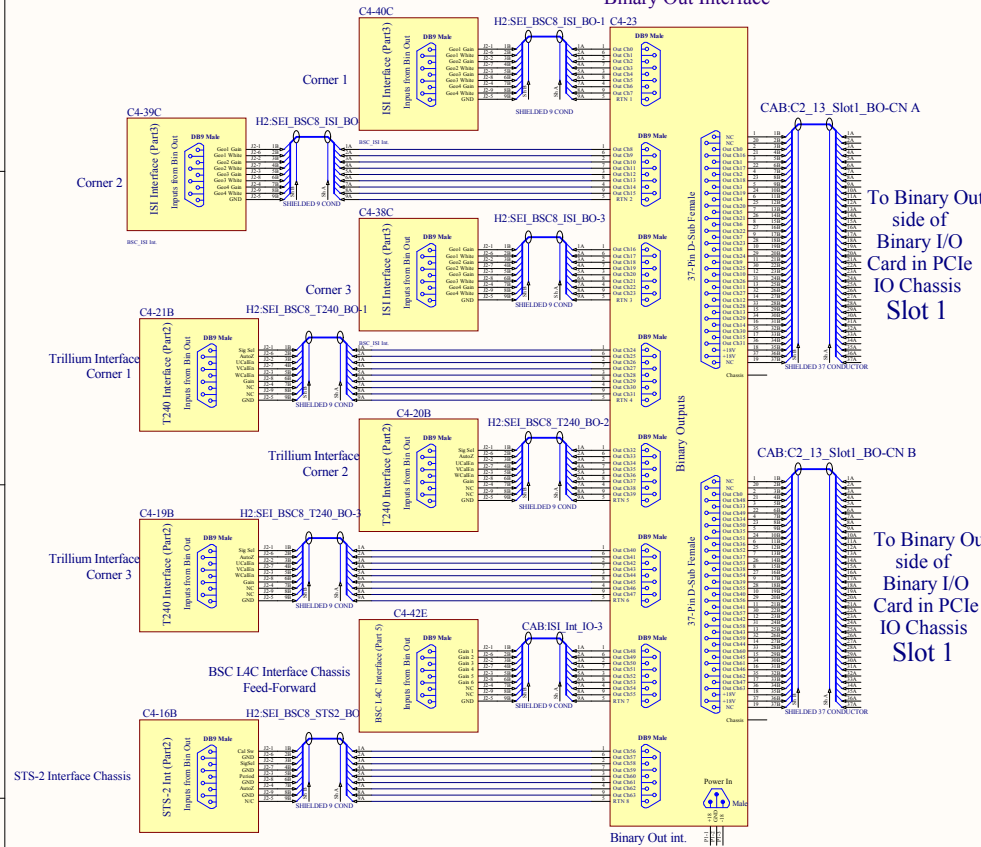
C4-32
aLIGO BSC ISI
Anti Alias Interface

1X1-13-6

IO Chassis Slot 6
General Standards ADC



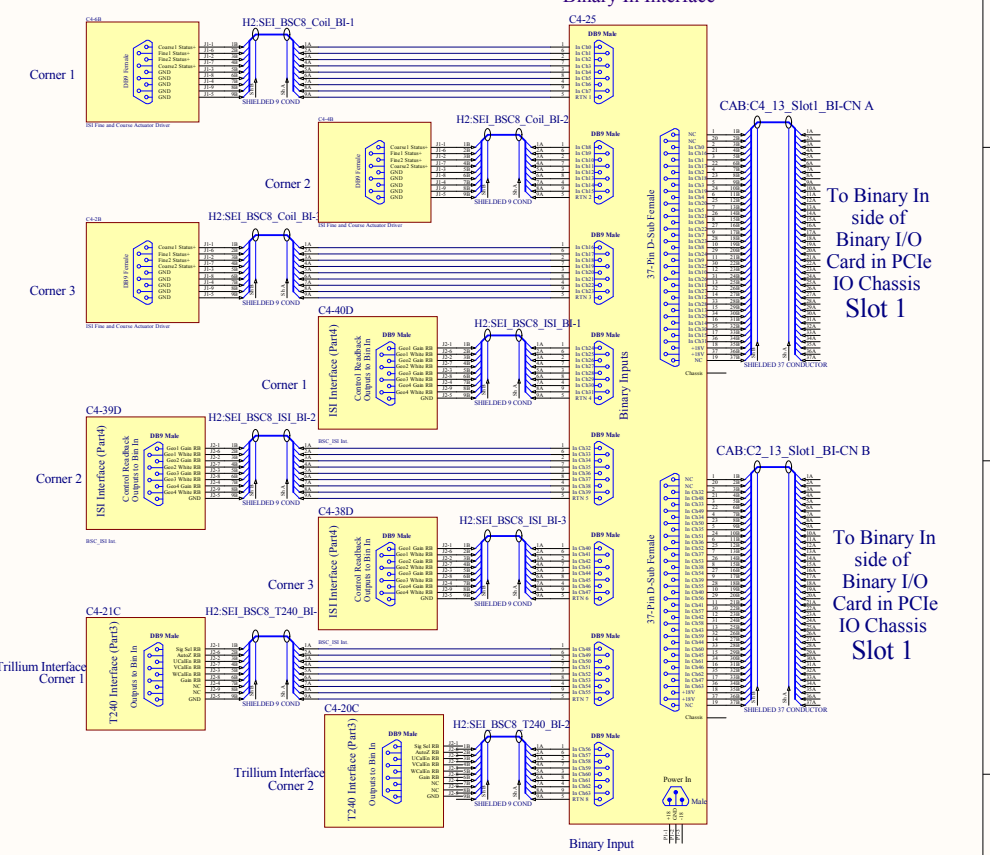
Binary Out Interface



To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 1

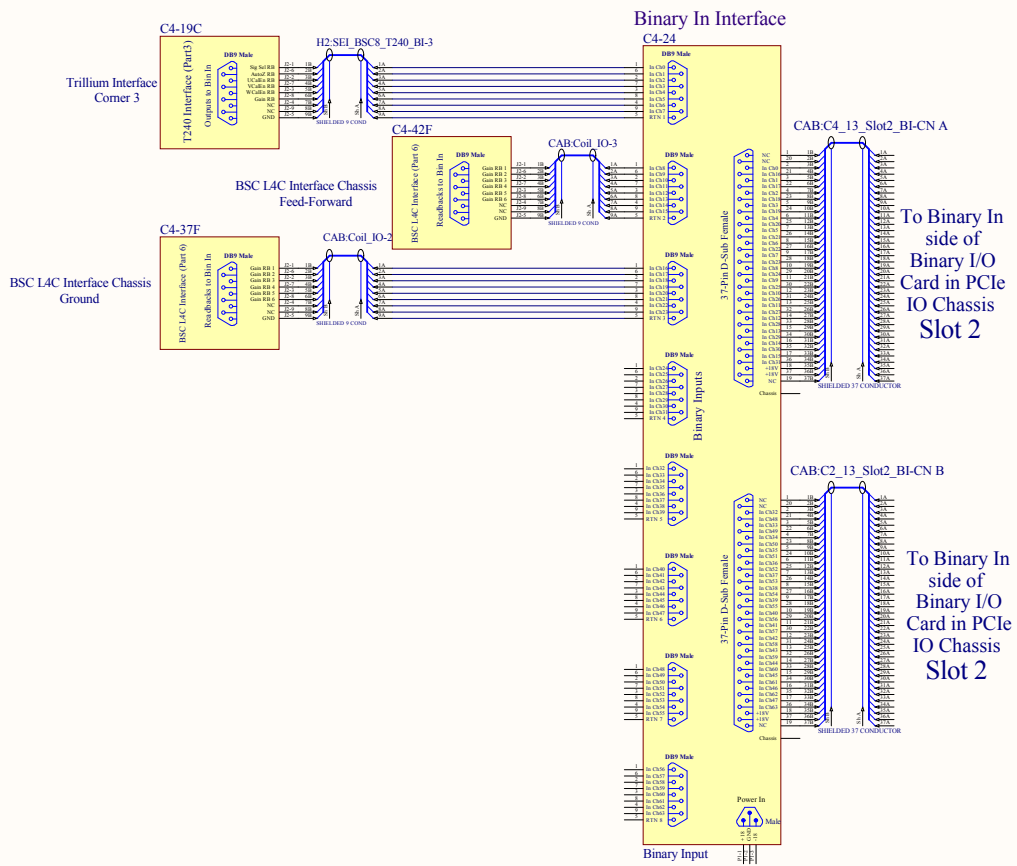
To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 1

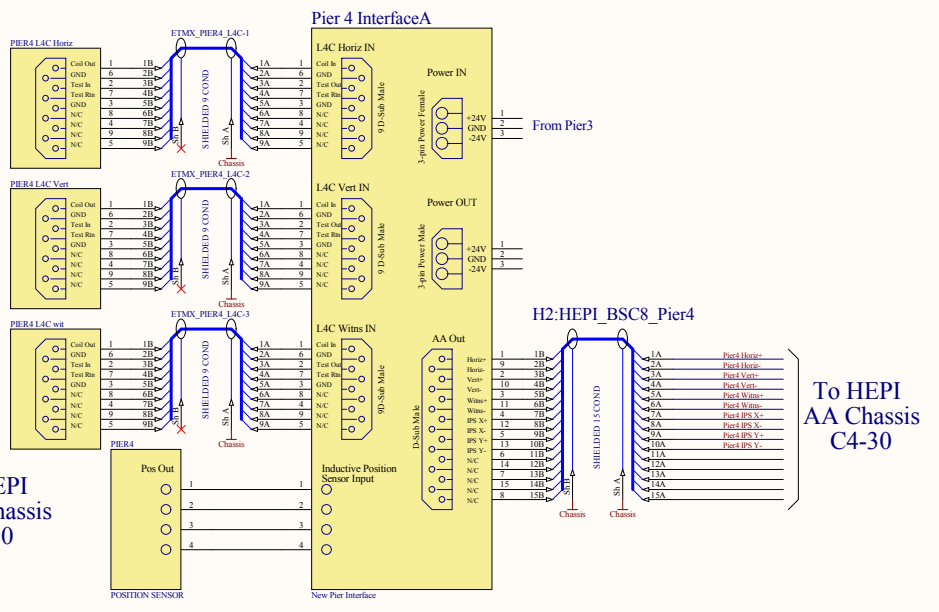
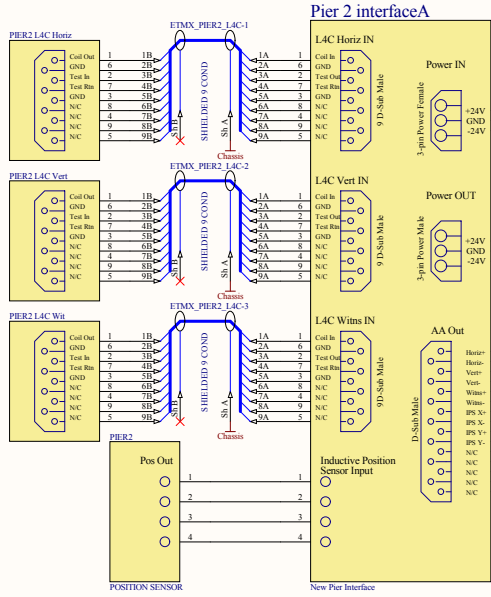
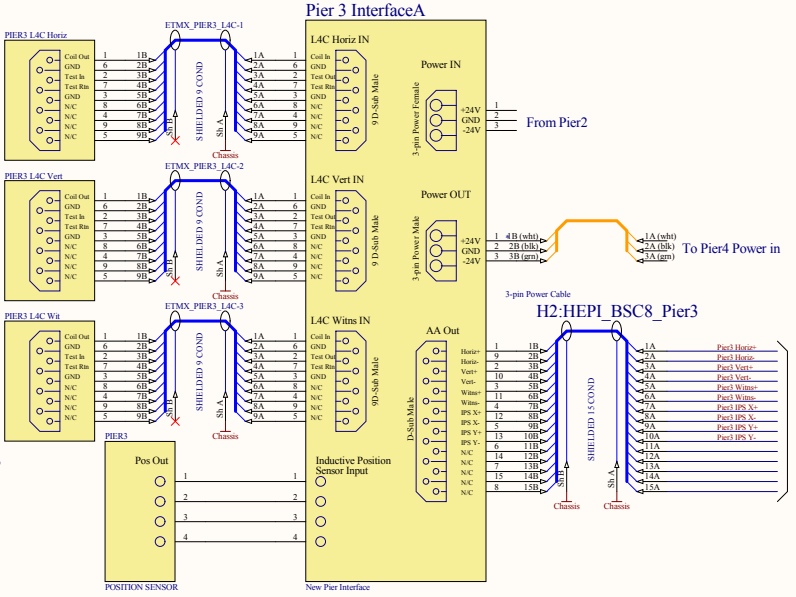
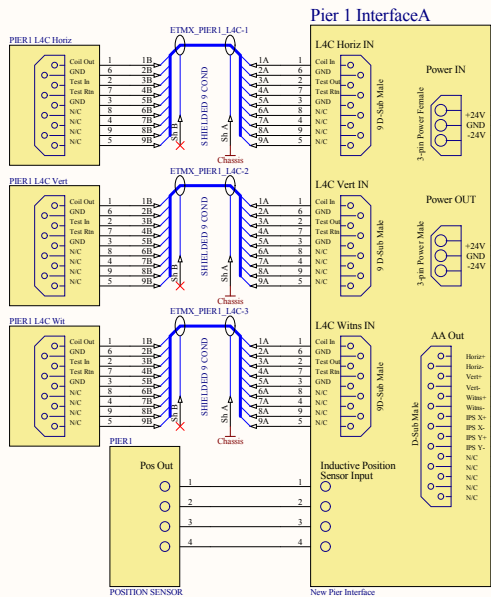
Binary In Interface



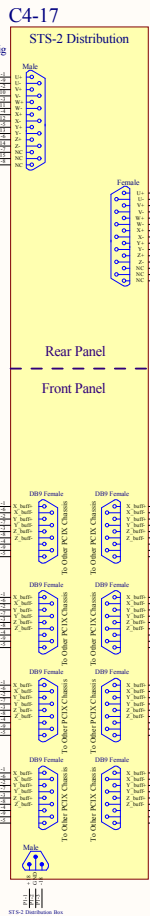
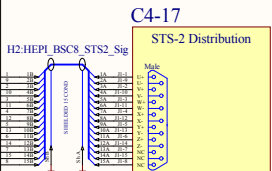
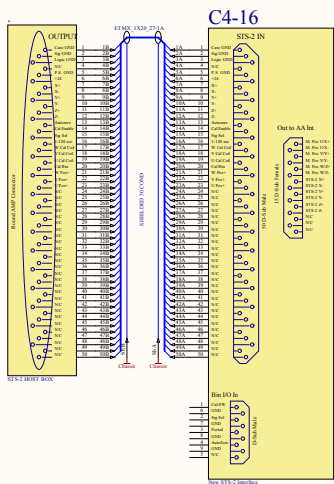
To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 1

To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 1



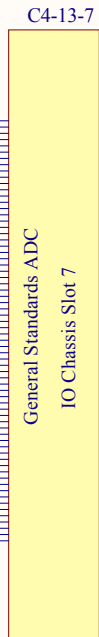
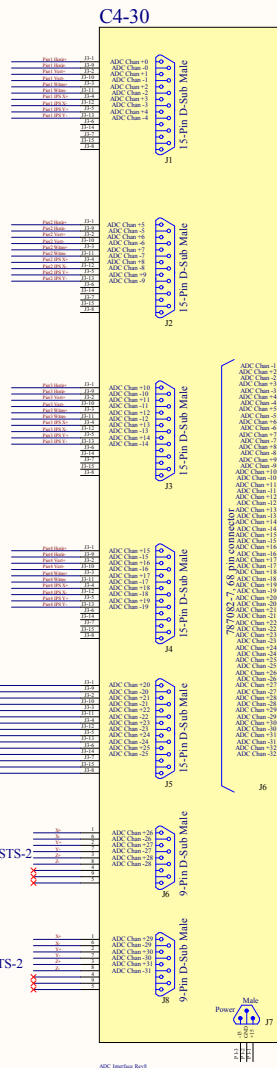
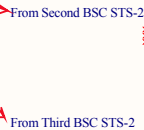


Title		ALIGO BSC ISI HEPI System Schematics	
Size:	C	DCC Number:	D0901301
Drawn by:	Ben Abbott	Date:	9/22/2011
File:	C:\Users\ben.abbott\Documents\ALIGO\BSC\HEPI_SchDoc	Revision:	v6
		Time:	3:39:42 PM
		Sheet:	10 of 13



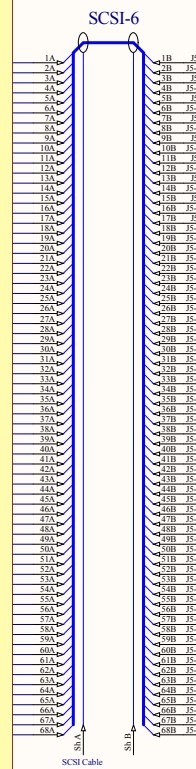
These signals go
To the HAM ISI
Spare ADC Inputs

These signals go
To the other 2
BSC ADC Inputs
Like these:

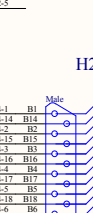
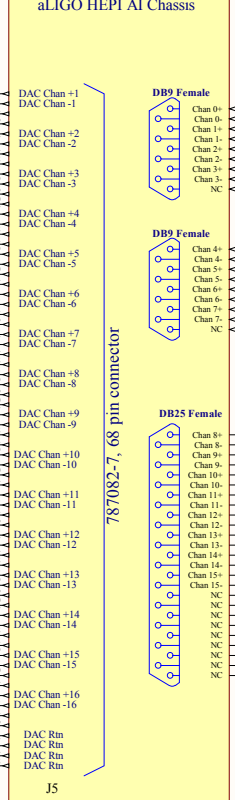


1X1-13-11

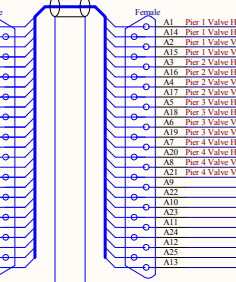
IO Chassis Slot 11
General Standards DAC



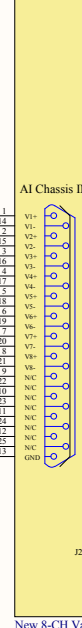
C4-29
aLIGO HEPI AI Chassis



H2:SEL_BSC8_Valve_DAC-1

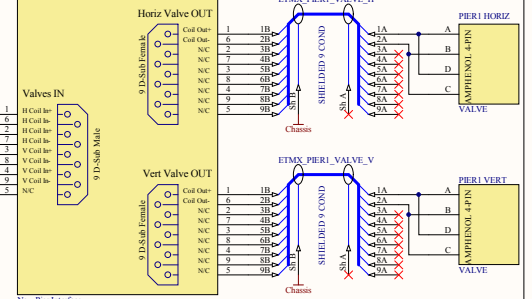


C4-27
AI Chassis IN

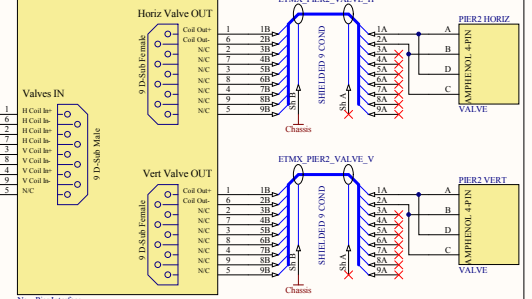


New 8-CH Valve Driver

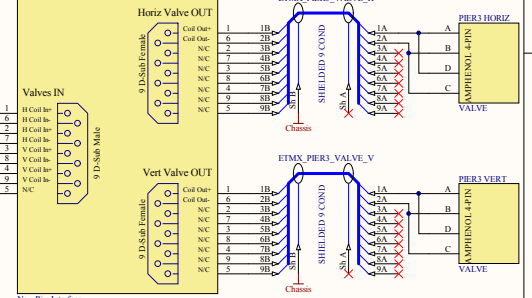
Pier 1 Interface



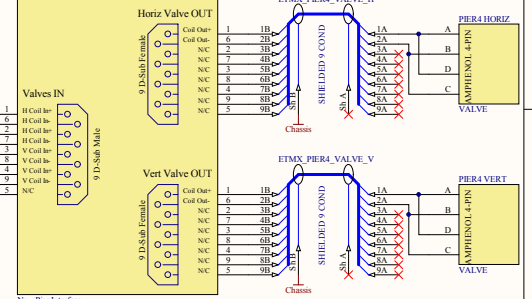
New Pier Interface
Pier 2 Interface



New Pier Interface
Pier 3 Interface



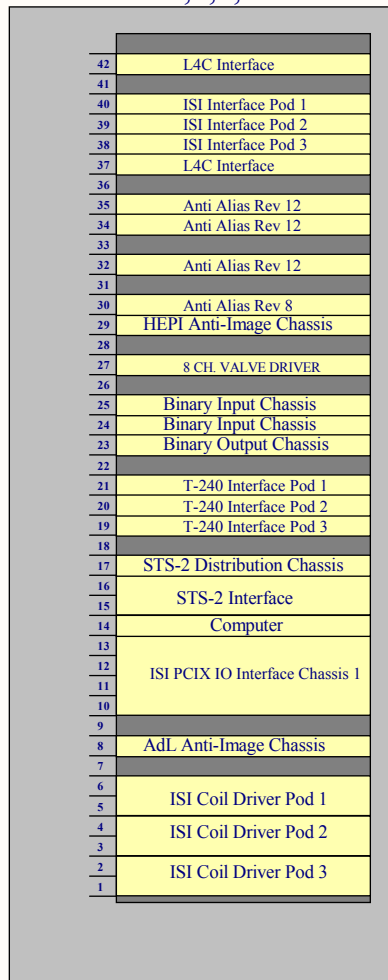
New Pier Interface
Pier 4 Interface



Title aLIGO BSC ISI HEPI System Schematics			
Size: C	DCC Number: D0901301	Ligo Project California Institute of Technology Massachusetts Institute of Technology	Cannot open file C:\Bermisclan y\ligo_1.jpg
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File: C:\rooted\Ben\isscm\ISI\BSC_ISIHEPI1.schDoc			

L1-SEI-C4, C5, C6, and Ends
BSC 1,2,3,4&5

BSC AA Chassis = 3
HEPI AA Chassis = 1
HEPI AI Chassis = 1
aLIGO AI Chassis = 1
Filter Boards = 20
Binary In Chassis = 2
Binary Out Chassis = 1



HEPI Components

STS-2 Input /Distribution

Title BSC ISI System Wiring			
Size: B	DCC Number: LIGO-D0901301	Ligo Project California Institute of Technology Massachusetts Institute of Technology	
Drawn by: Ben Abbott	Date: 9/22/2011	Revision: v6	Cannot open file C:\Ben\miscellan y\ligologo_1.jpg
File: C:\restored\Ben\Seismic\ISI\BSC_ISI_10_SchDoc			Time: 3:39:42 PM Sheet 13 of 13