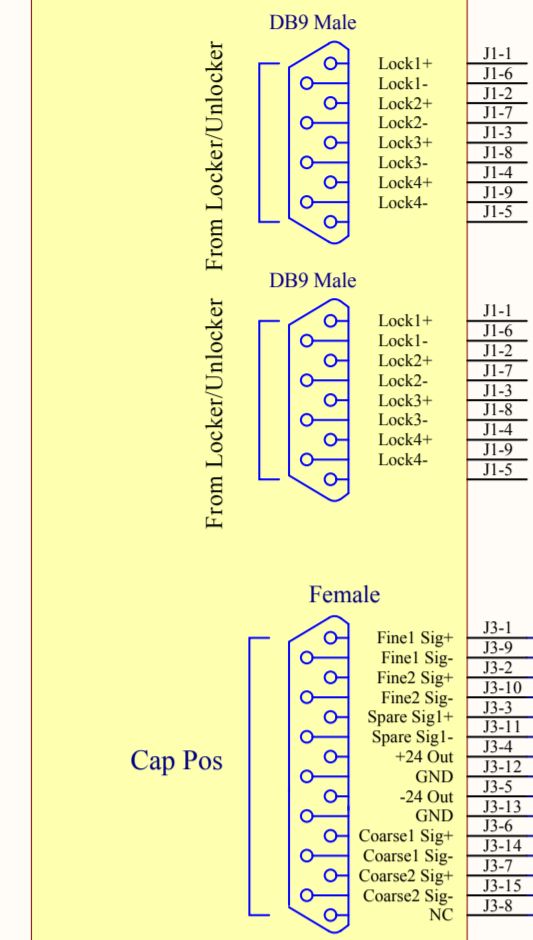
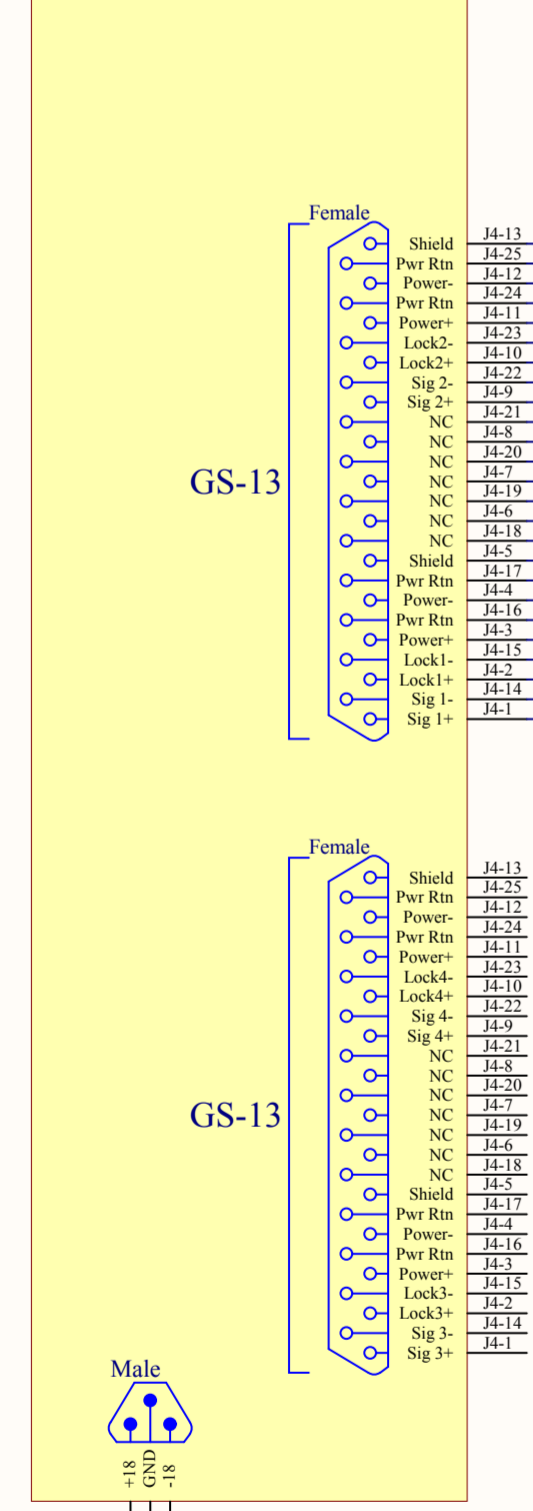


1Y30-9A

SS-ISI Int. (Part 1)

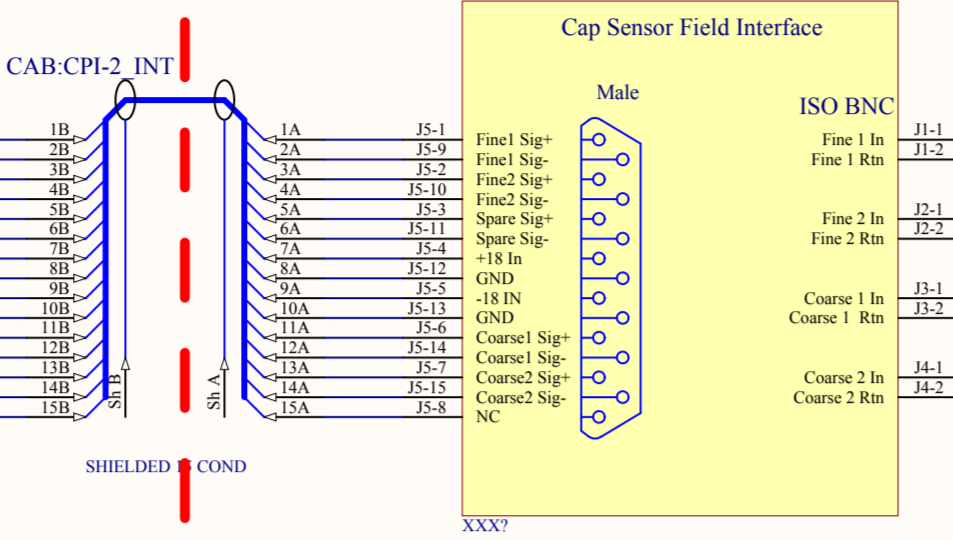


Pod 3

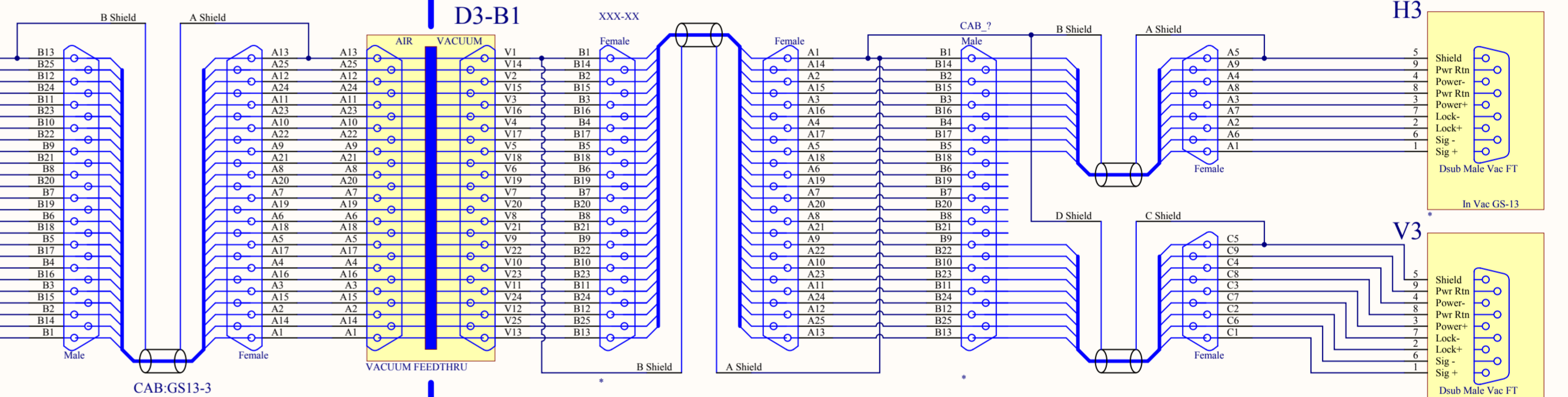
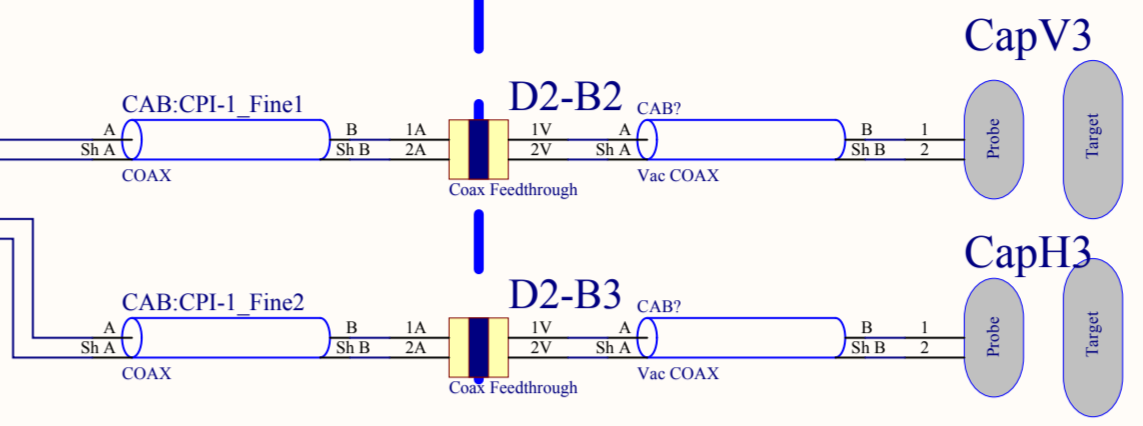


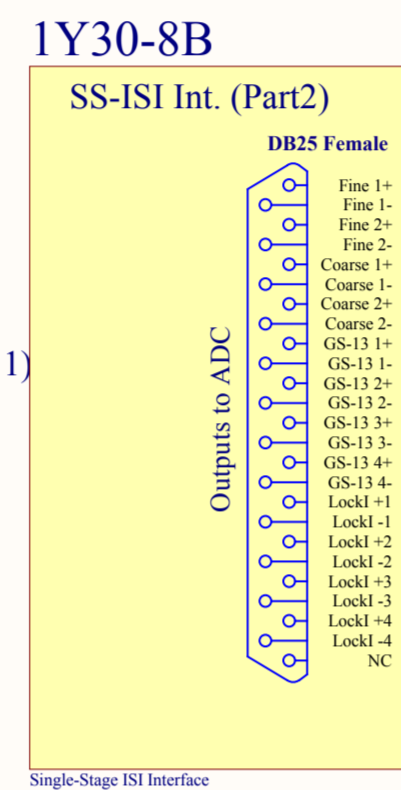
Single-Stage ISI Interface

Rack | Field

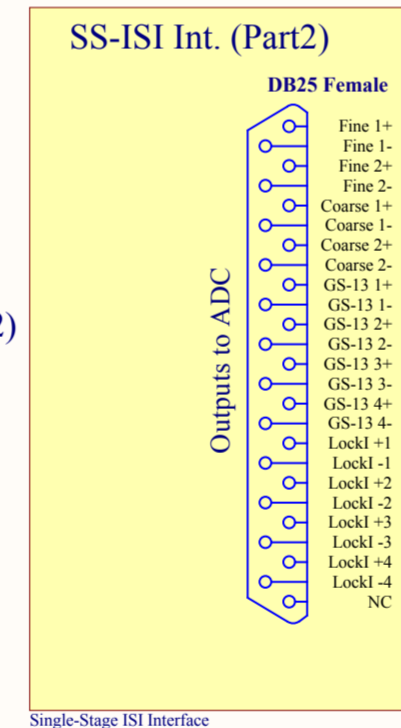


Air | Vacuum





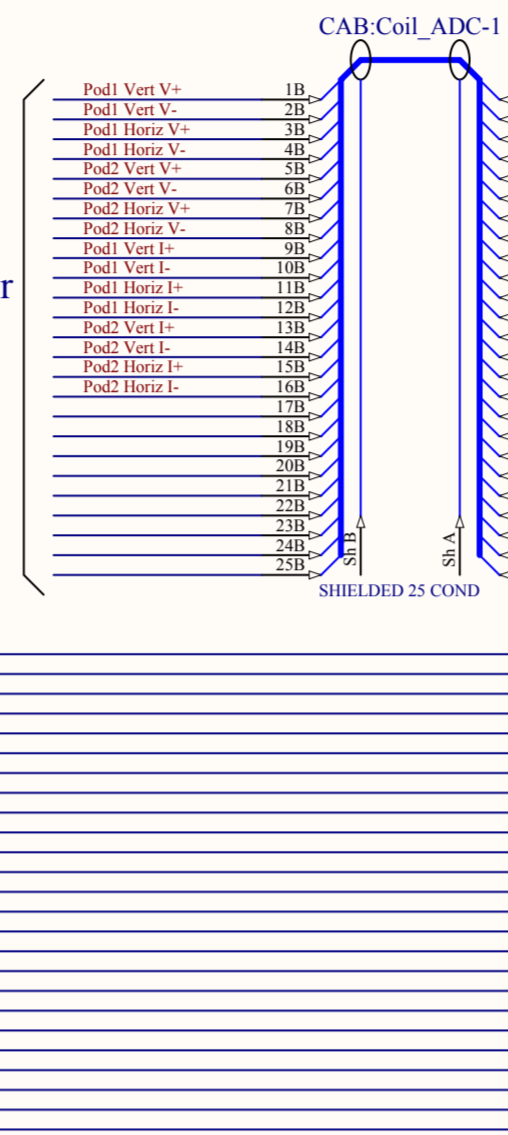
Pod 1&2 Signals (Sheet 1)



Pod 3 Signals (Sheet 2)

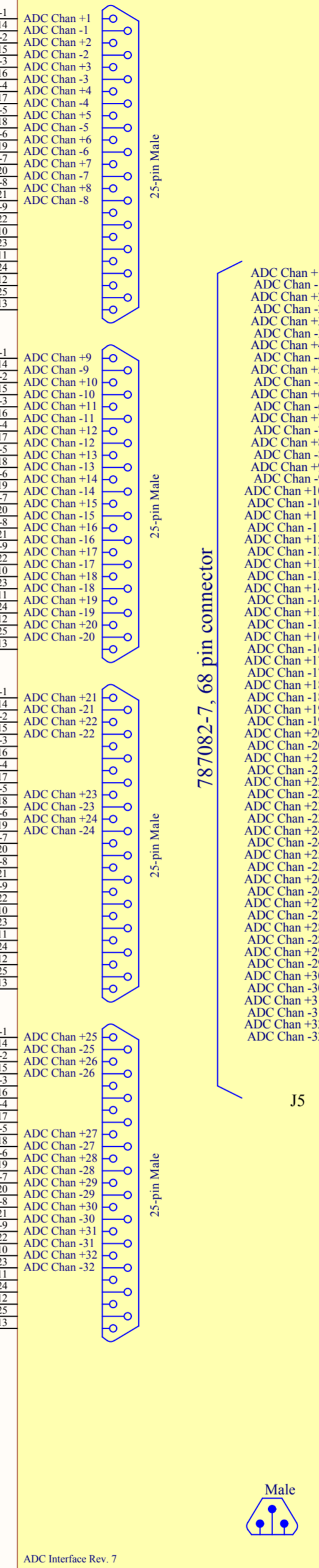
Pod 1&2 Coil Driver Page 4

Pod 3 Coil Driver Page 4



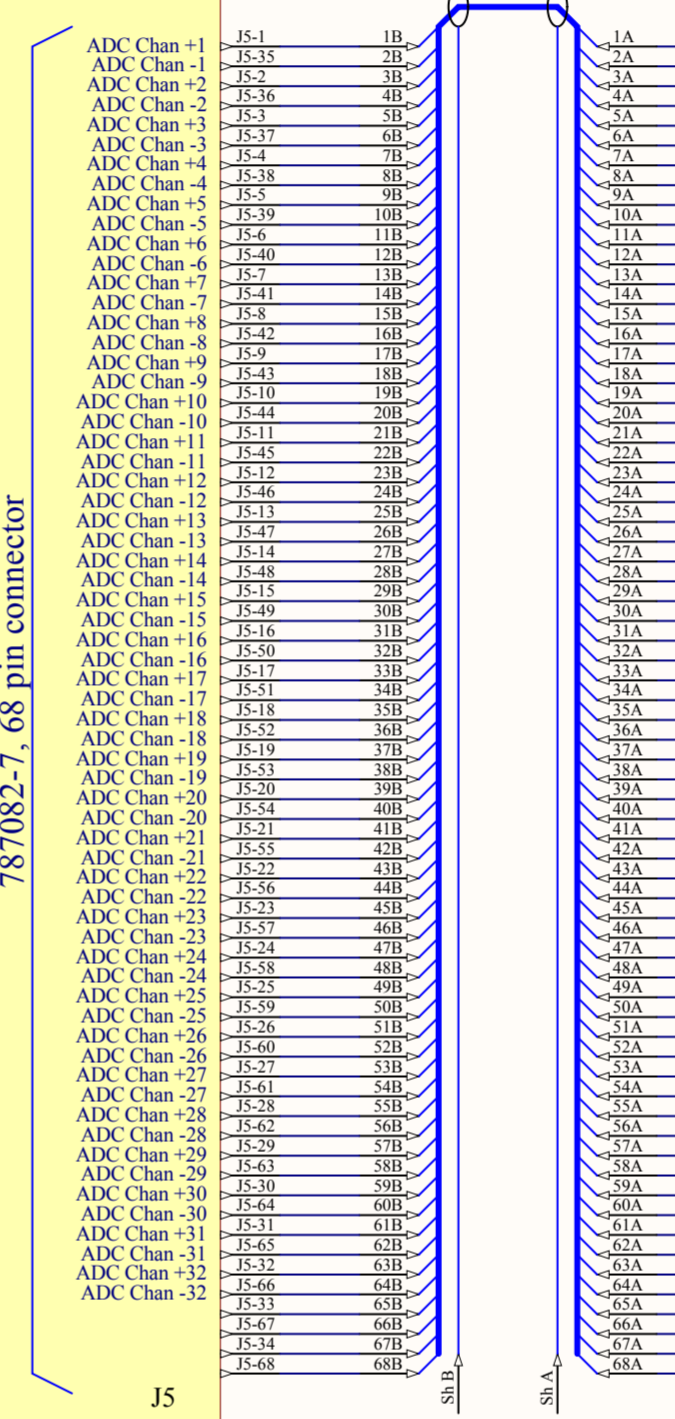
1Y30-11

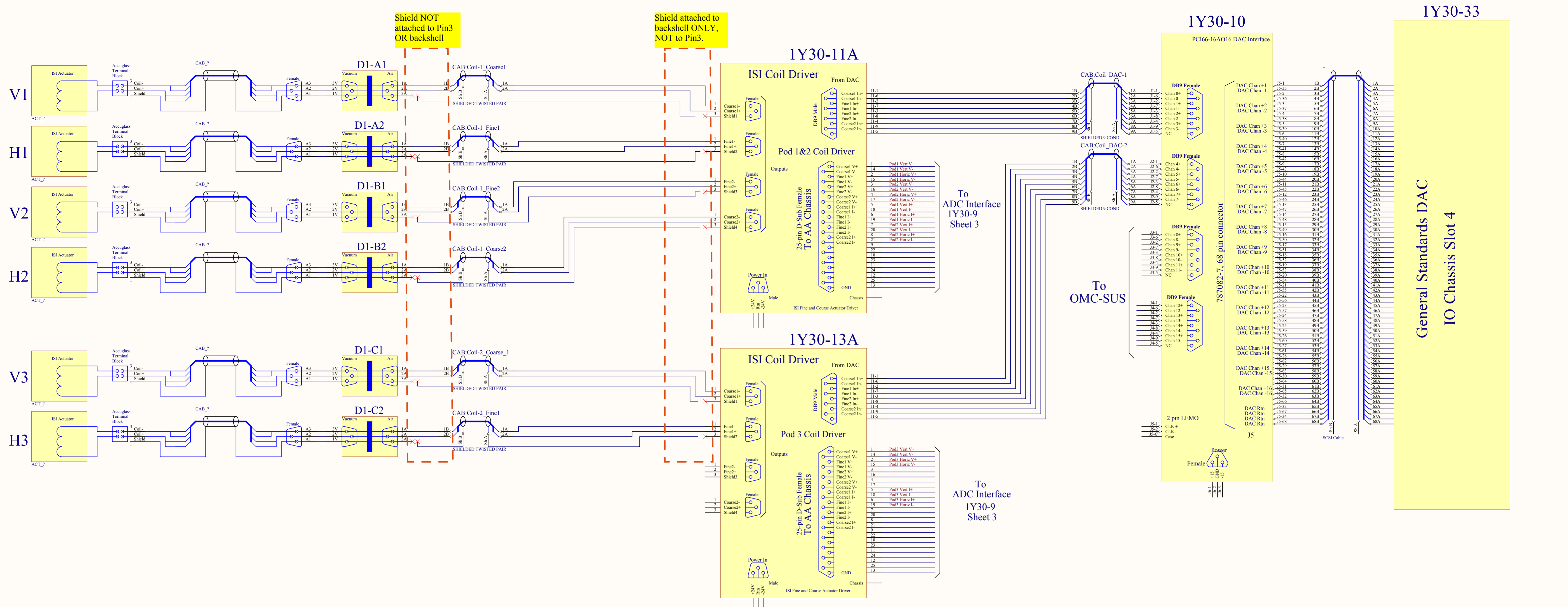
ADC Interface Rev.7B



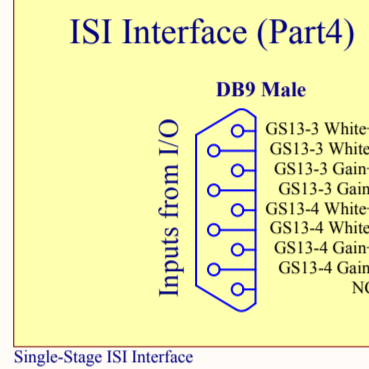
1Y30-34

General Standards ADC IO Chassis Slot 3

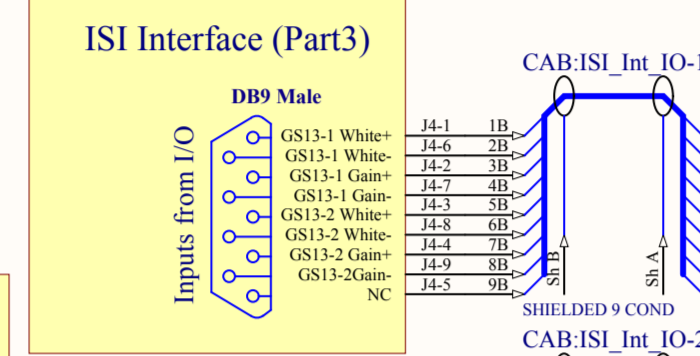




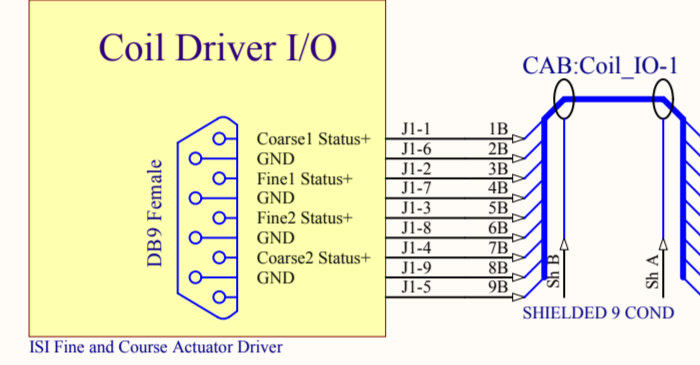
1Y30-7D



1Y30-7C

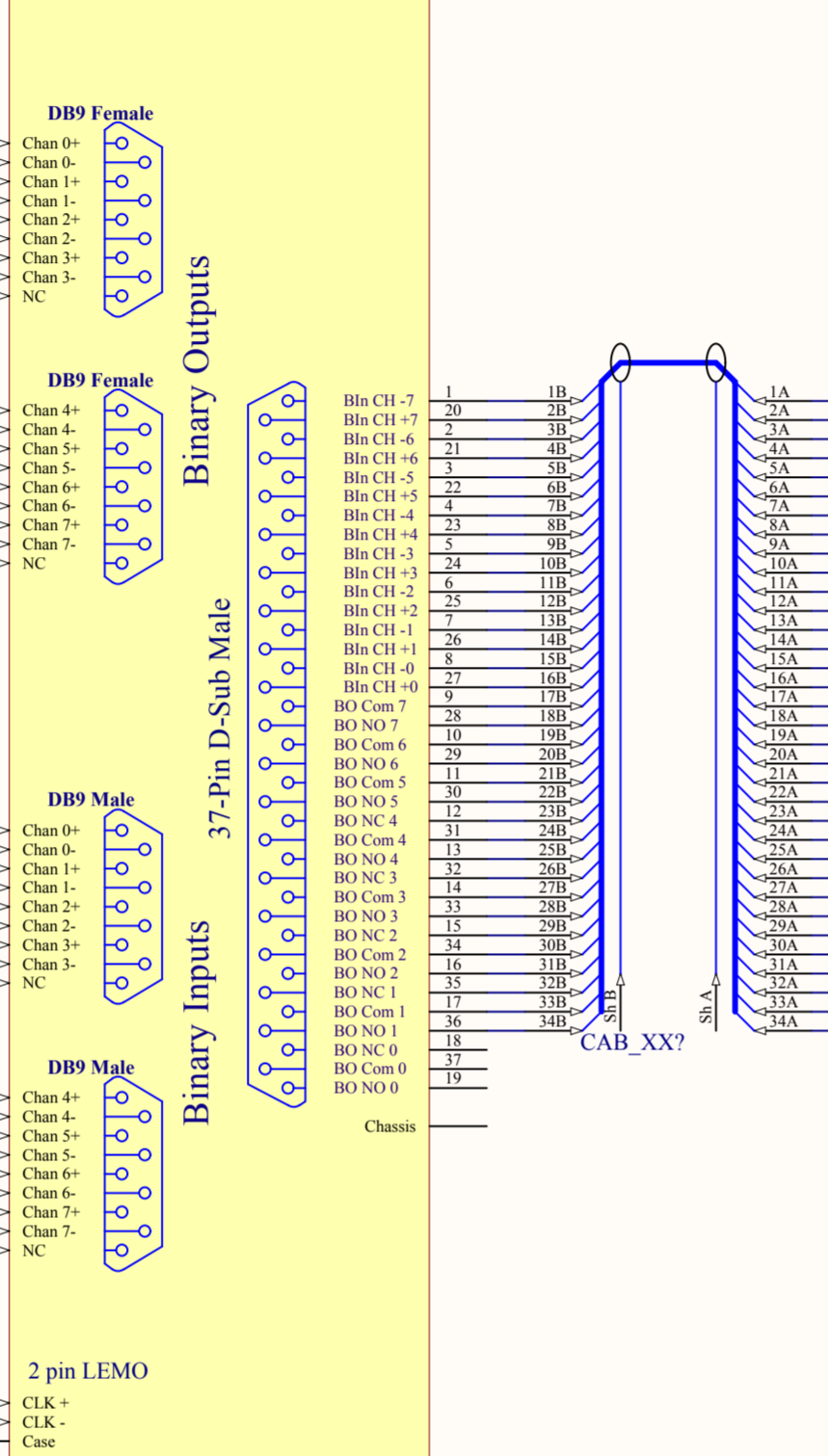


1Y30-11B



1Y30-5

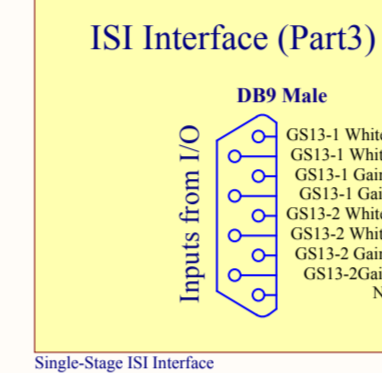
Binary I/O Interface



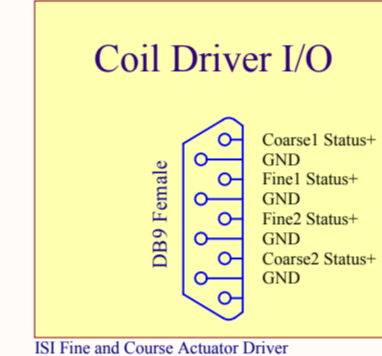
1Y30-33

General Standards Binary I/O
IO Chassis Slot 5A

1Y30-8C

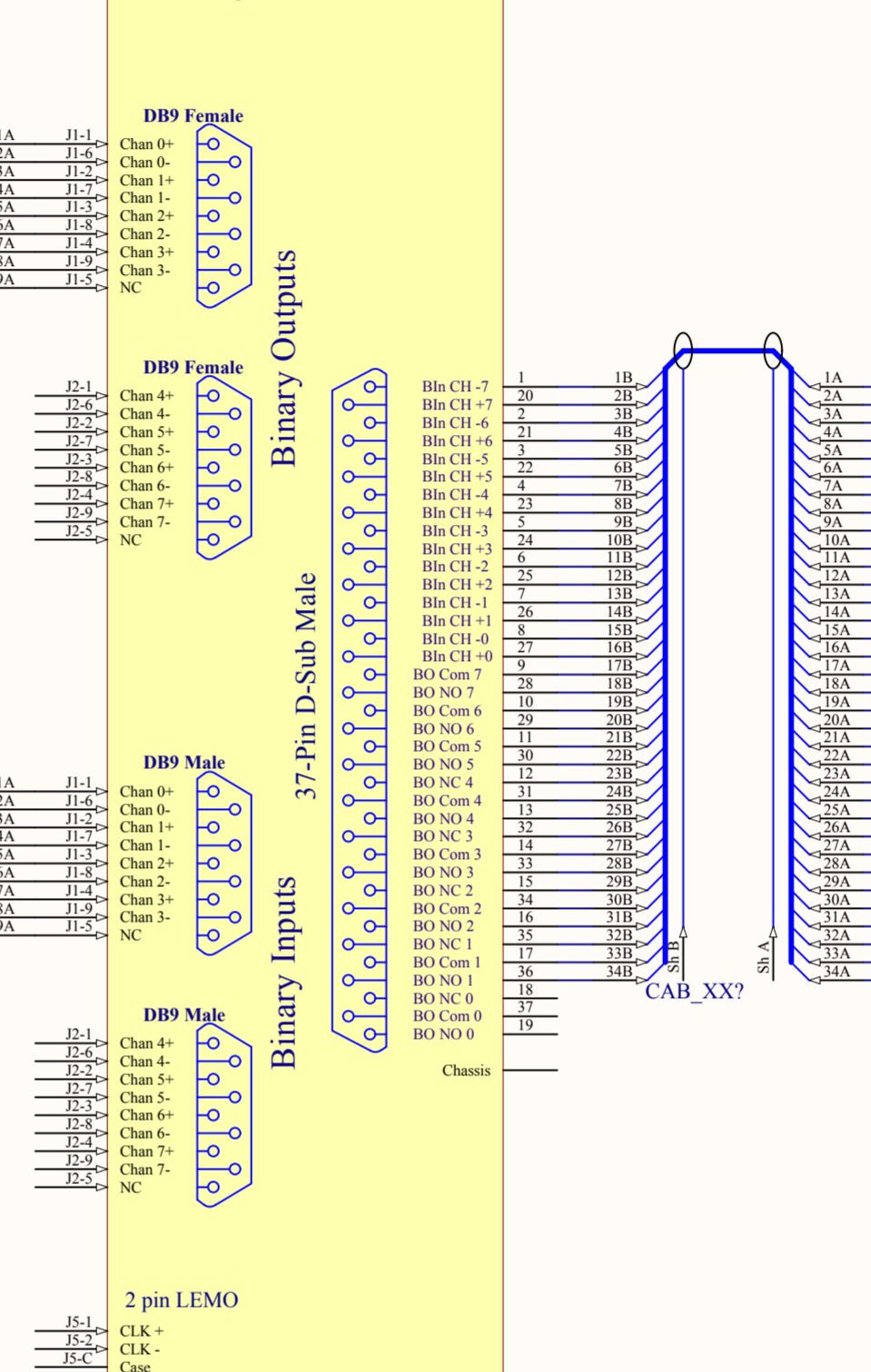


1Y30-13B



1Y30-6

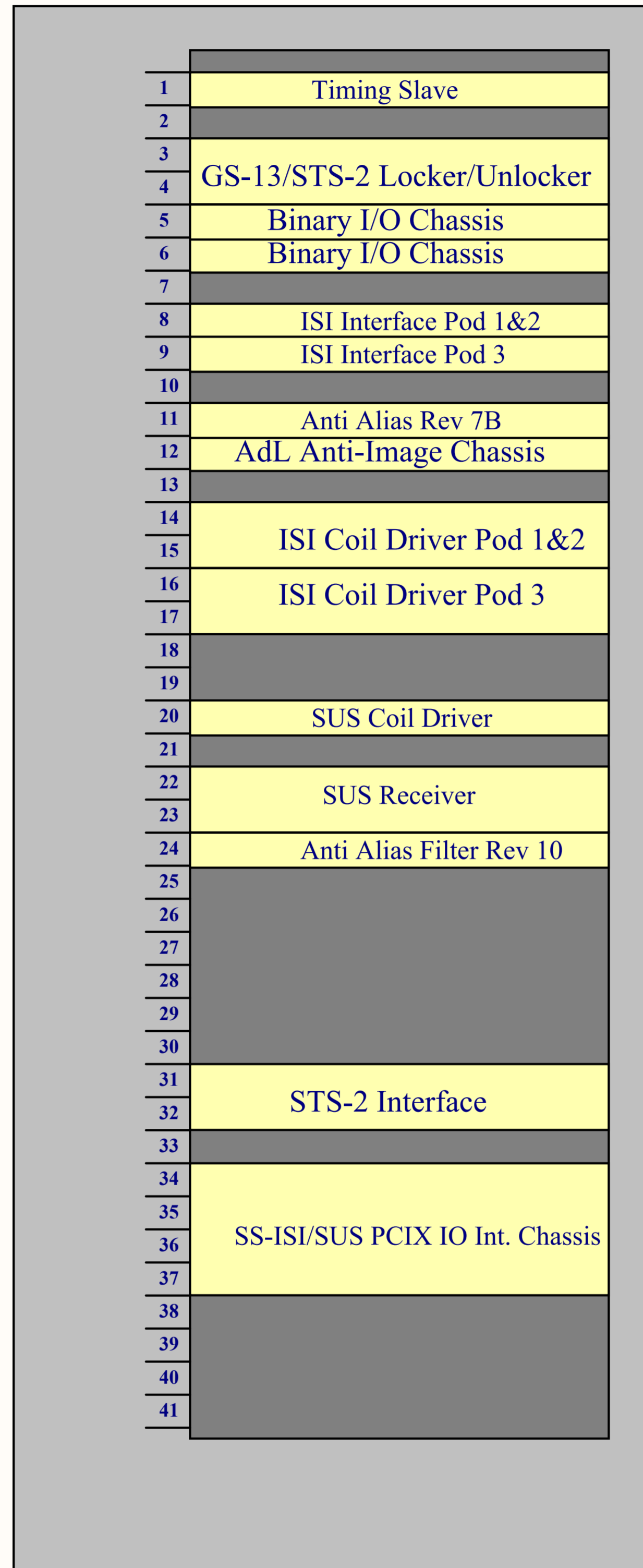
Binary I/O Interface



1Y30-33

General Standards Binary I/O
IO Chassis Slot 5B

1Y30

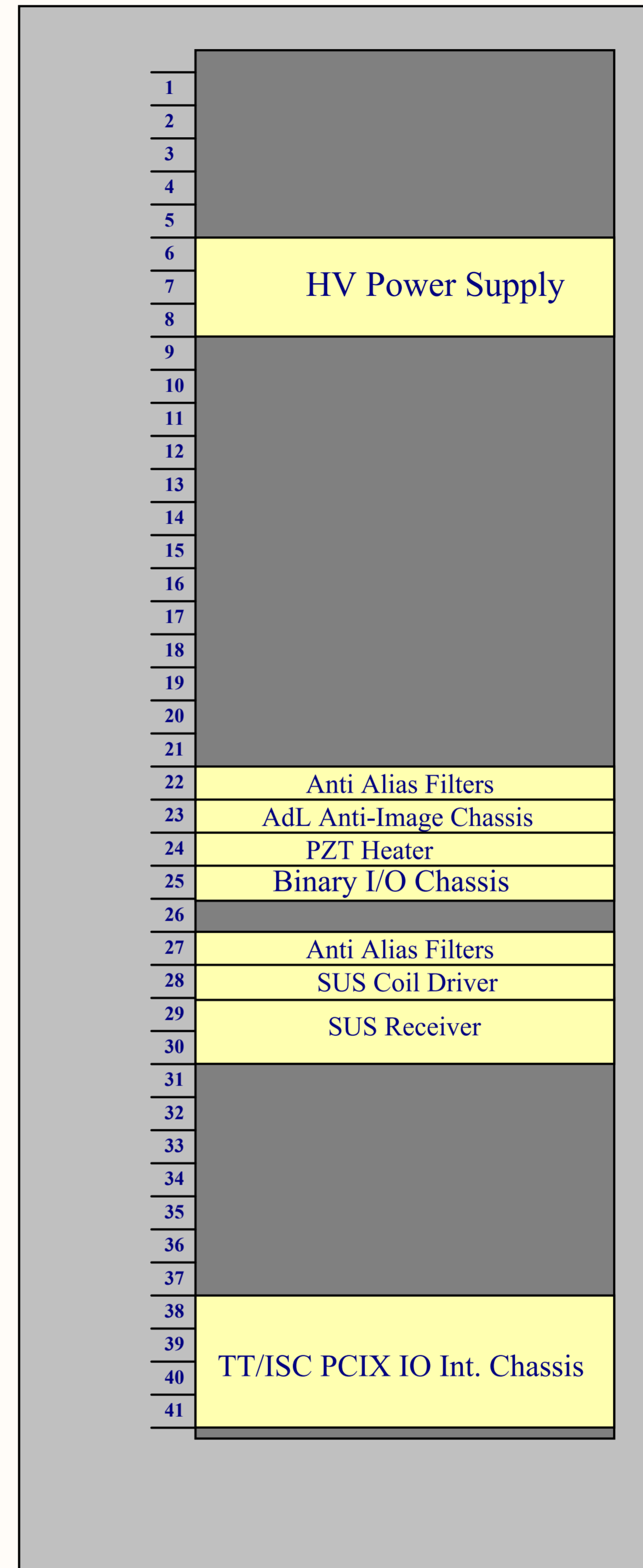


ISI

OMC SUS

ISI STS-2 Input

1Y31



ISC

TIP/TILT

Title HAM ISI System Wiring			
Size: B	DCC Number: D080114-00-C	Ligo Project California Institute of Technology Massachusetts Institute of Technology	
Drawn by: Ben Abbott	Date: 3/31/2009	Revision: *	
File: C:\Ben\Seismic\ISI\Single Stage ISI\OldHAM_ISI\AdL\SEI_Single Stage\1Y30.dwg 11:45:50 PM Sheet 6 of 6			