

LASER INTERFEROMETER GRAVITATIONAL WAVE OBSERVATORY
-LIGO-
CALIFORNIA INSTITUTE OF TECHNOLOGY
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Technical Note	LIGO-E0900016	01/04/09
Integration Plan Slave Module Advanced LIGO		
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This is an internal working note
of the LIGO Project.

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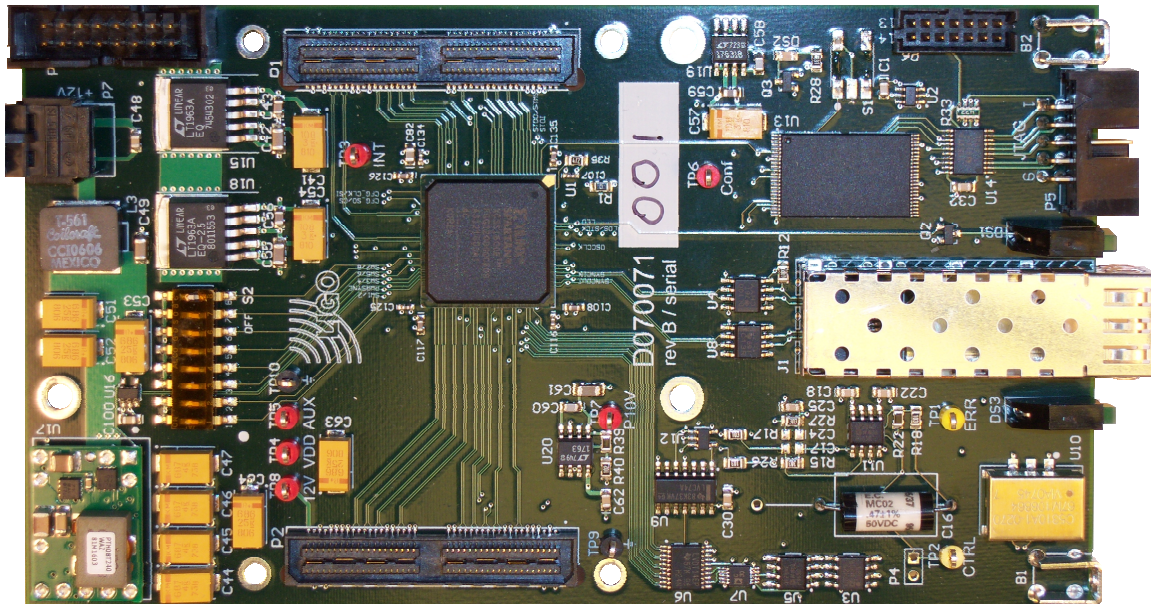


Integration Plan: Slave Module for Advanced LIGO

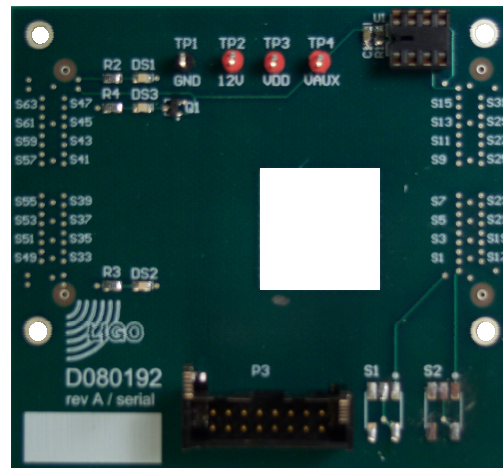
This document provides an overview of the documentation covering the production, programming, and testing of the Slave Module. Its intended purpose is to enable the manufacturing of deployment ready modules.

Visual identification of circuit boards

Slave board:



Hardware and FPGA test daughter circuit board



Board Designs

1. Slave board ([LIGO-D070071](#)) design:

1. Schematics is described in:
 1. Overview of the schematics: [D070071-B.pdf](#)
 2. Detailed Altium files: [D070071-B.zip](#)
2. PCB Design is described in:
 1. Detailed Altium files: [D070071-B.zip](#)
3. Order/Manufacturing package is provided in:
 1. Detailed files: [D070071-B.zip](#)

2. Hardware and FPGA test daughter board ([LIGO-D080192](#)) design:

1. Schematics is described in:
 1. Overview of the schematics: [D080192-B.pdf](#)
 2. Detailed Altium files: [D080192-B.zip](#)
2. PCB Design is described in:
 1. Detailed Altium files: [D080192-B.zip](#)
3. Order/Manufacturing package is provided in:
 1. Detailed files: [D080192-B.zip](#)



Board Test

3. Test Documents:
 1. Slave board test:
 1. Procedure: [LIGO-E050262.pdf](#)
 2. FPGA: [Test Code](#)

Chassis Assembly

- Front Panel Design:
 - [D080703-A.fpd](#)
- Quick Start Guide / Specifications:
 - [LIGO-E080541.pdf](#)
- Product FPGA Code:
 - [Code](#)

