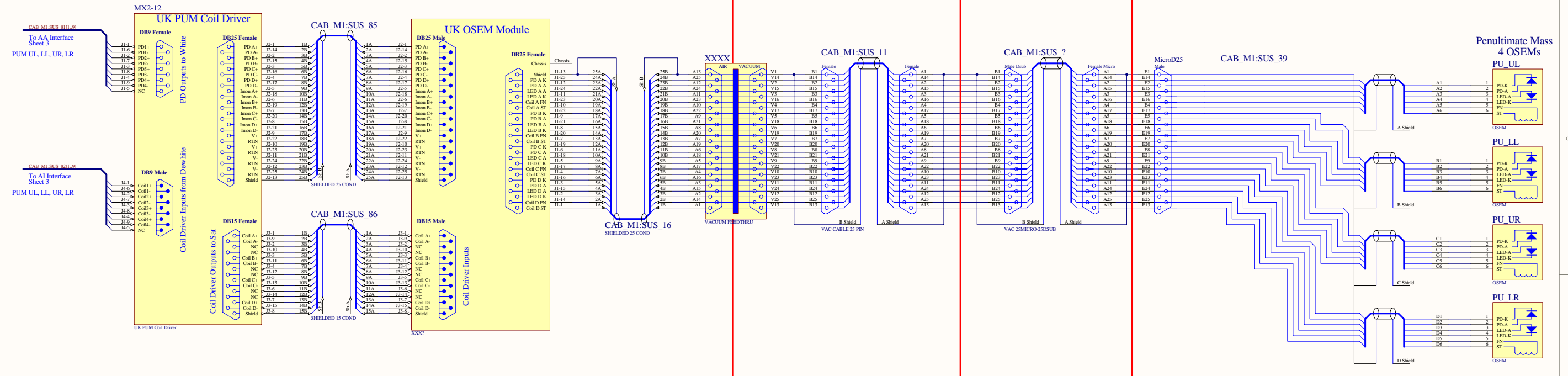
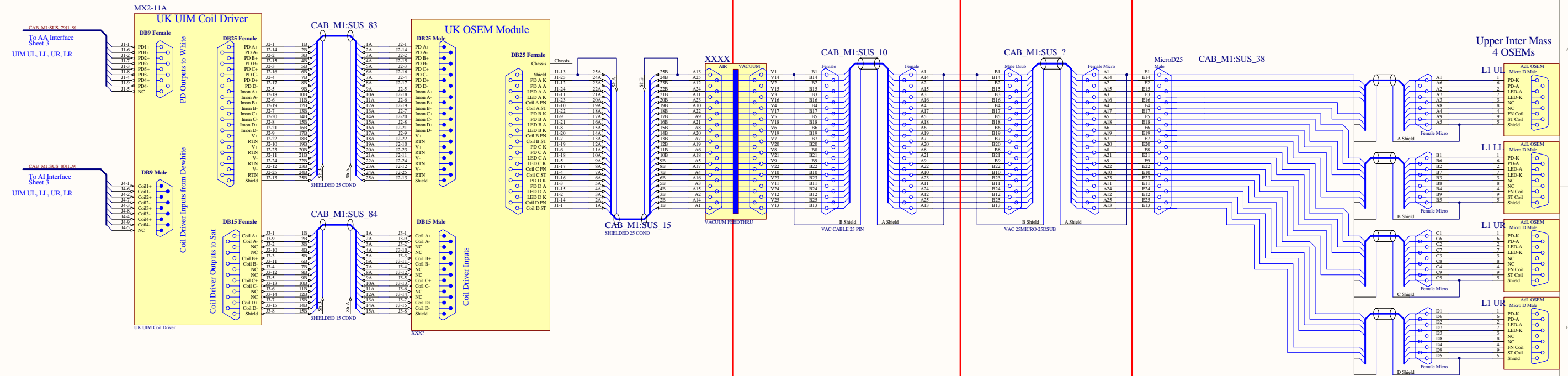
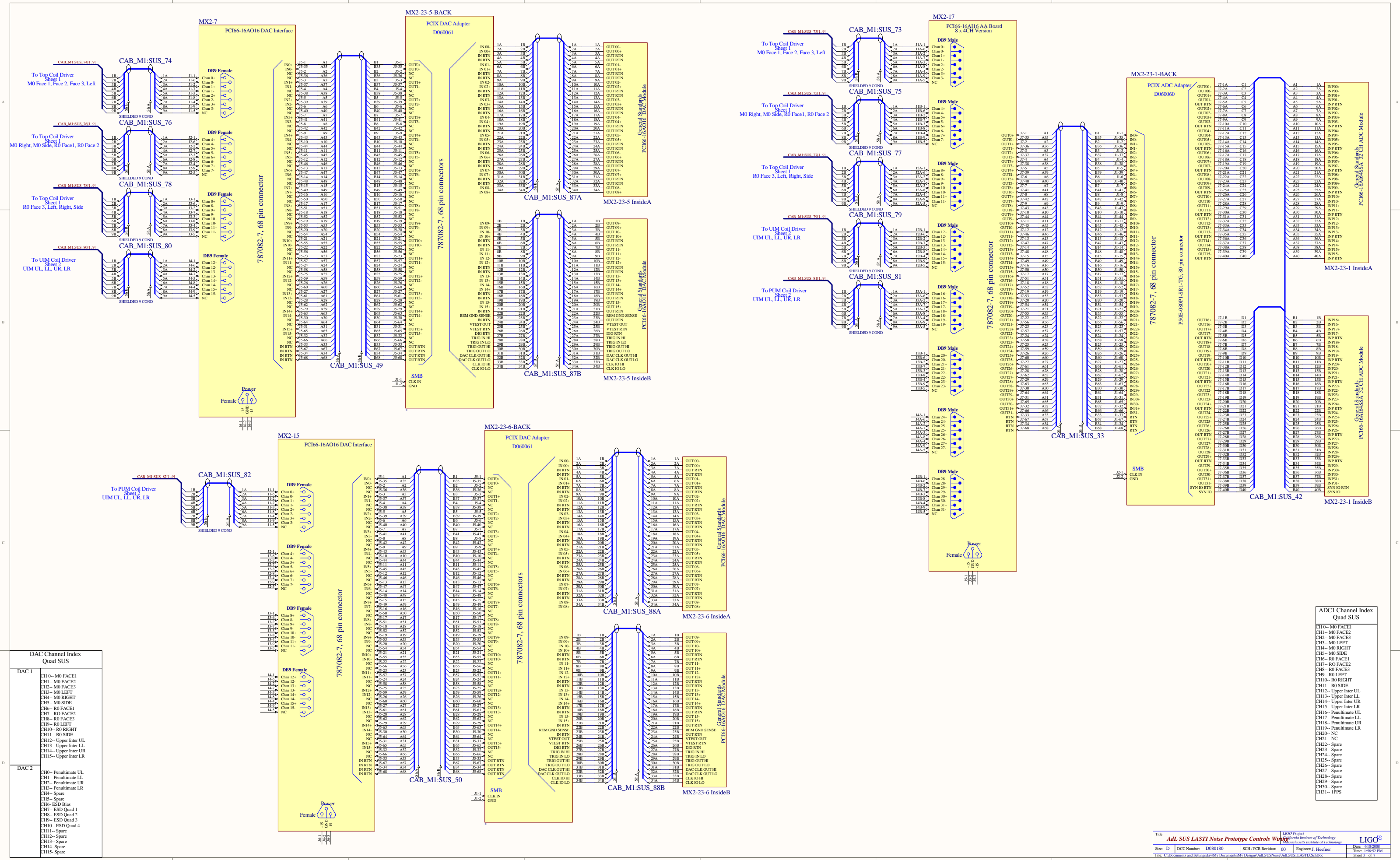


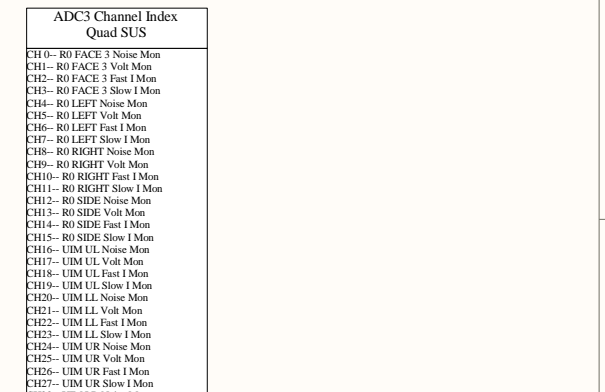
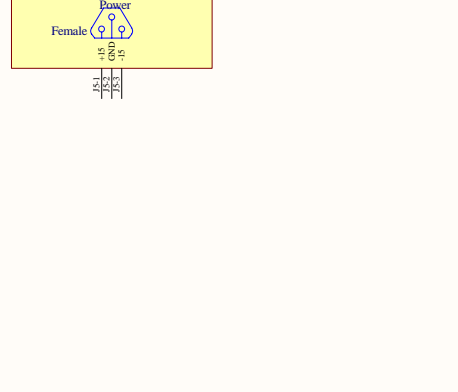
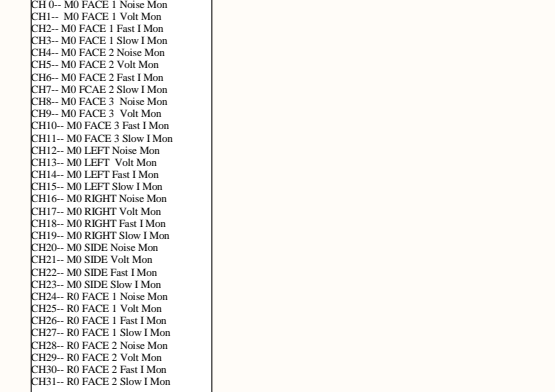
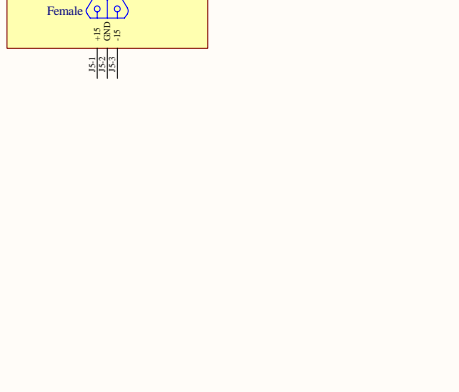
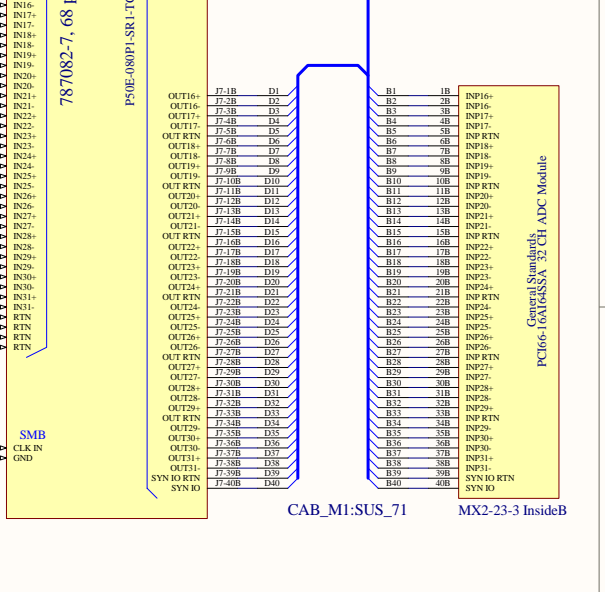
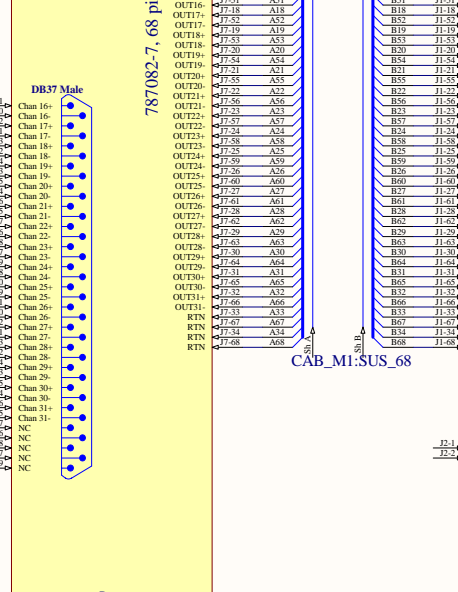
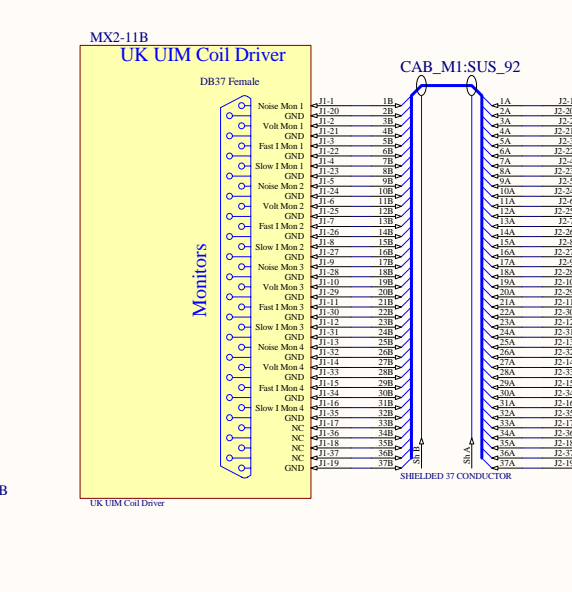
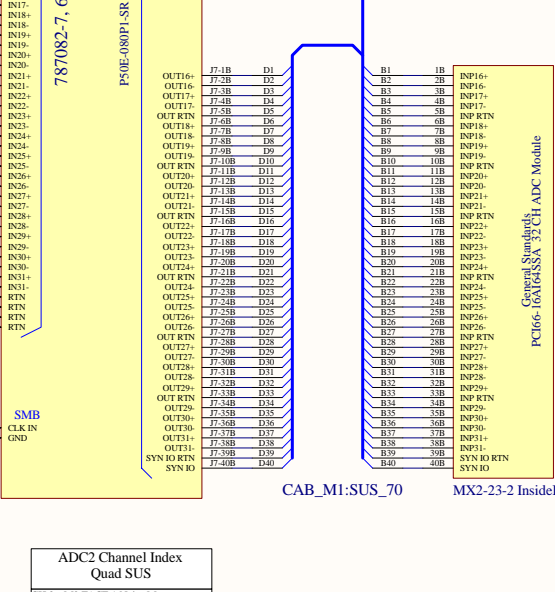
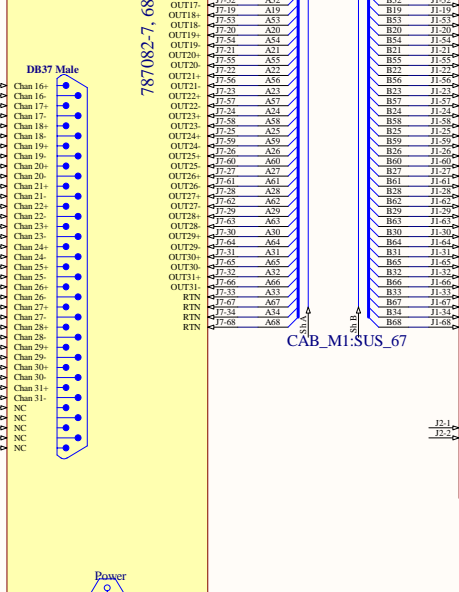
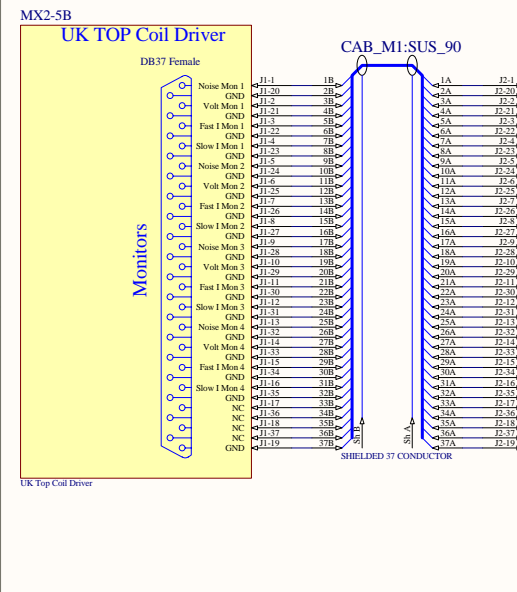
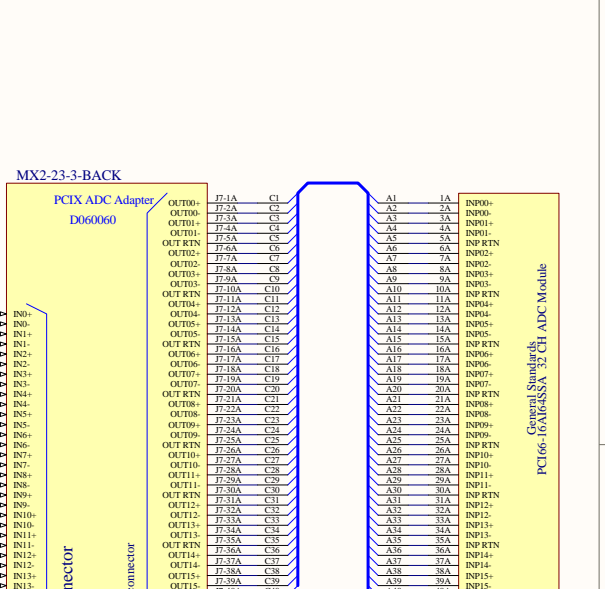
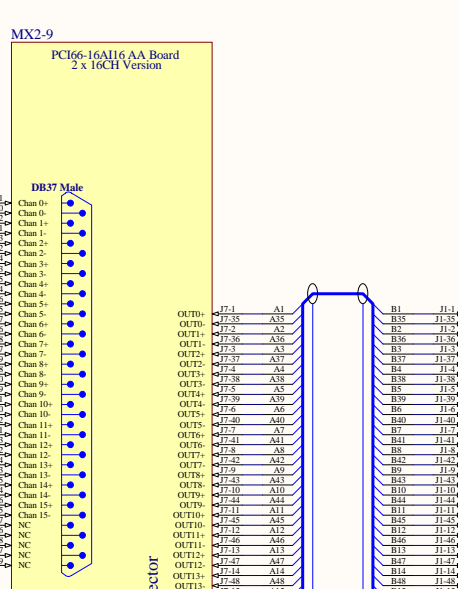
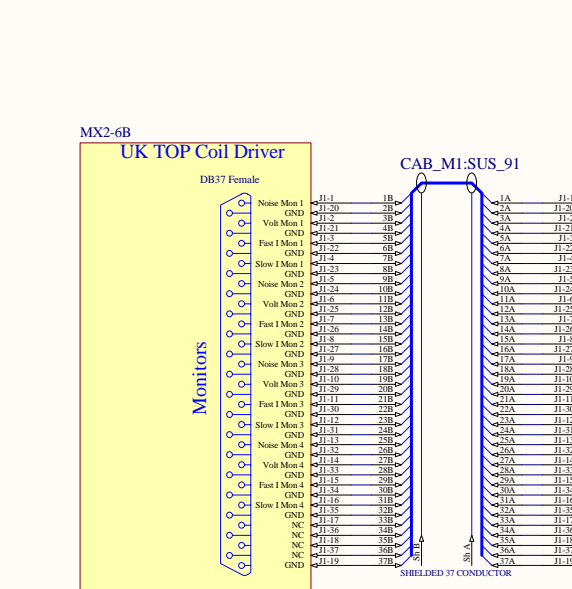
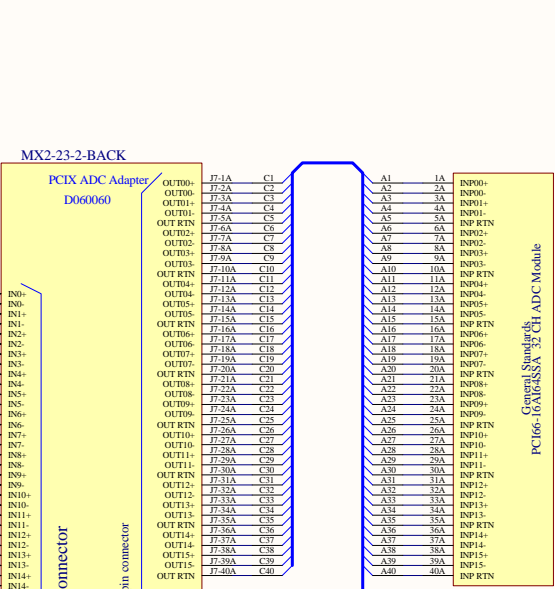
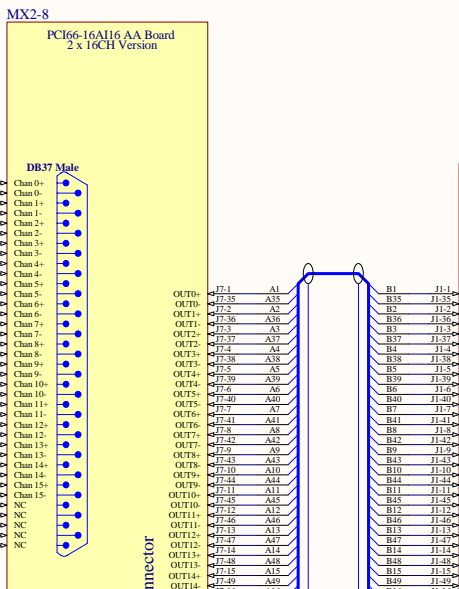
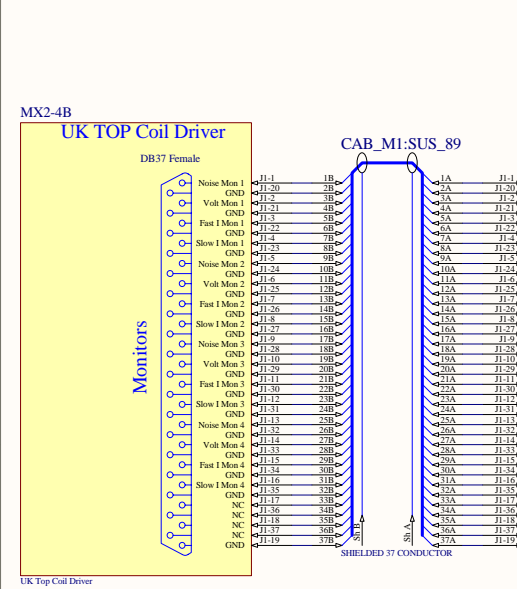
Top Mass
12 OSEMs





DAC Channel Index	
Quad SUS	
DAC 1	CH0 - M0 FACE1
DAC 1	CH1 - M0 FACE2
DAC 1	CH2 - M0 FACE3
DAC 1	CH3 - M0 LEFT
DAC 1	CH4 - M0 RIGHT
DAC 1	CH5 - M0 SIDE
DAC 1	CH6 - R0 FACE1
DAC 1	CH7 - R0 FACE2
DAC 1	CH8 - R0 FACE3
DAC 1	CH9 - R0 LEFT
DAC 1	CH10 - R0 RIGHT
DAC 1	CH11 - R0 SIDE
DAC 1	CH12 - Upper Inter UL
DAC 1	CH13 - Upper Inter LL
DAC 1	CH14 - Upper Inter UR
DAC 1	CH15 - Upper Inter LR
DAC 2	CH0 - Penultimate UL
DAC 2	CH1 - Penultimate LL
DAC 2	CH2 - Penultimate UR
DAC 2	CH3 - Penultimate LR
DAC 2	CH4 - Spare
DAC 2	CH5 - Spare
DAC 2	CH6 - ESD Bias
DAC 2	CH7 - ESD Quad 1
DAC 2	CH8 - ESD Quad 2
DAC 2	CH9 - ESD Quad 3
DAC 2	CH10 - ESD Quad 4
DAC 2	CH11 - Spare
DAC 2	CH12 - Spare
DAC 2	CH13 - Spare
DAC 2	CH14 - Spare
DAC 2	CH15 - Spare

ADC1 Channel Index	
Quad SUS	
CH0 - M0 FACE1	
CH1 - M0 FACE2	
CH2 - M0 FACE3	
CH3 - M0 LEFT	
CH4 - M0 RIGHT	
CH5 - M0 SIDE	
CH6 - R0 FACE1	
CH7 - R0 FACE2	
CH8 - R0 FACE3	
CH9 - R0 LEFT	
CH10 - R0 RIGHT	
CH11 - R0 SIDE	
CH12 - Upper Inter UL	
CH13 - Upper Inter LL	
CH14 - Upper Inter UR	
CH15 - Upper Inter LR	
CH16 - Penultimate UL	
CH17 - Penultimate LL	
CH18 - Penultimate UR	
CH19 - Penultimate LR	
CH20 - NC	
CH21 - NC	
CH22 - Spare	
CH23 - Spare	
CH24 - Spare	
CH25 - Spare	
CH26 - Spare	
CH27 - Spare	
CH28 - Spare	
CH29 - Spare	
CH30 - Spare	
CH31 - IPPS	



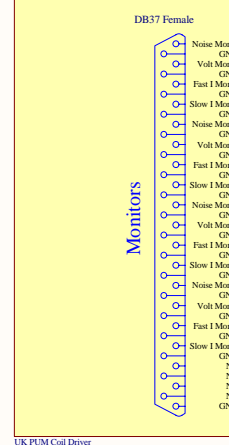
ADC2 Channel Index Quad SUS

- CH0 - M0 FACE 1 Noise Mon
- CH1 - M0 FACE 1 Volt Mon
- CH2 - M0 FACE 1 Fast 1 Mon
- CH3 - M0 FACE 1 Slow 1 Mon
- CH4 - M0 FACE 2 Noise Mon
- CH5 - M0 FACE 2 Volt Mon
- CH6 - M0 FACE 2 Fast 1 Mon
- CH7 - M0 FACE 2 Slow 1 Mon
- CH8 - M0 FACE 3 Noise Mon
- CH9 - M0 FACE 3 Volt Mon
- CH10 - M0 FACE 3 Fast 1 Mon
- CH11 - M0 FACE 3 Slow 1 Mon
- CH12 - M0 LEFT Noise Mon
- CH13 - M0 LEFT Volt Mon
- CH14 - M0 LEFT Fast 1 Mon
- CH15 - M0 LEFT Slow 1 Mon
- CH16 - M0 RIGHT Noise Mon
- CH17 - M0 RIGHT Volt Mon
- CH18 - M0 RIGHT Fast 1 Mon
- CH19 - M0 RIGHT Slow 1 Mon
- CH20 - M0 SIDE Noise Mon
- CH21 - M0 SIDE Volt Mon
- CH22 - M0 SIDE Fast 1 Mon
- CH23 - M0 SIDE Slow 1 Mon
- CH24 - R0 FACE 1 Noise Mon
- CH25 - R0 FACE 1 Volt Mon
- CH26 - R0 FACE 1 Fast 1 Mon
- CH27 - R0 FACE 1 Slow 1 Mon
- CH28 - R0 FACE 2 Noise Mon
- CH29 - R0 FACE 2 Volt Mon
- CH30 - R0 FACE 2 Fast 1 Mon
- CH31 - R0 FACE 2 Slow 1 Mon

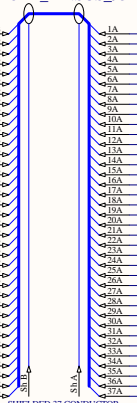
ADC3 Channel Index Quad SUS

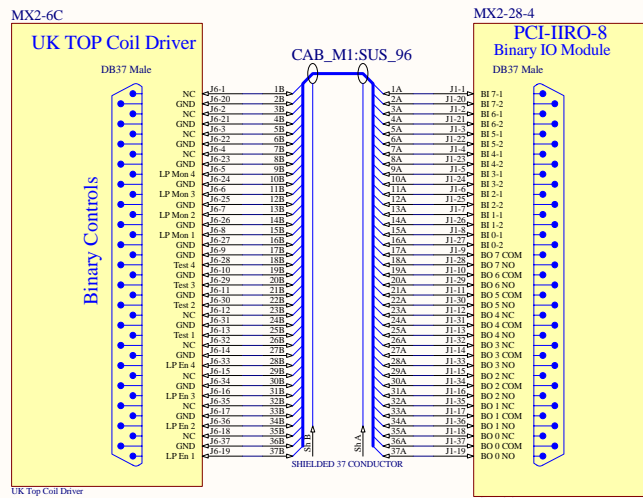
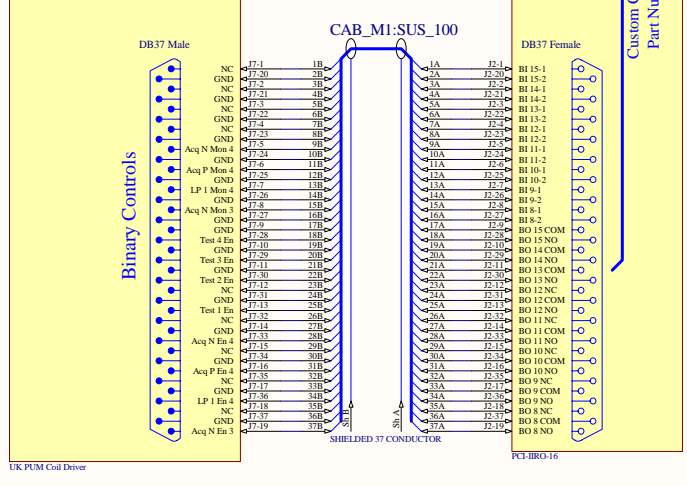
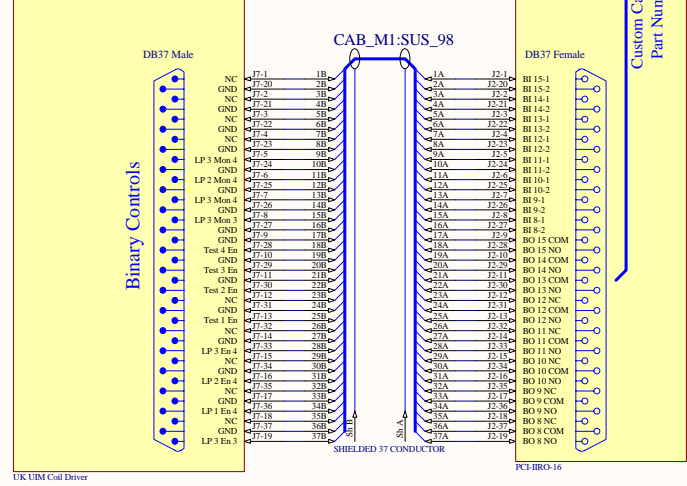
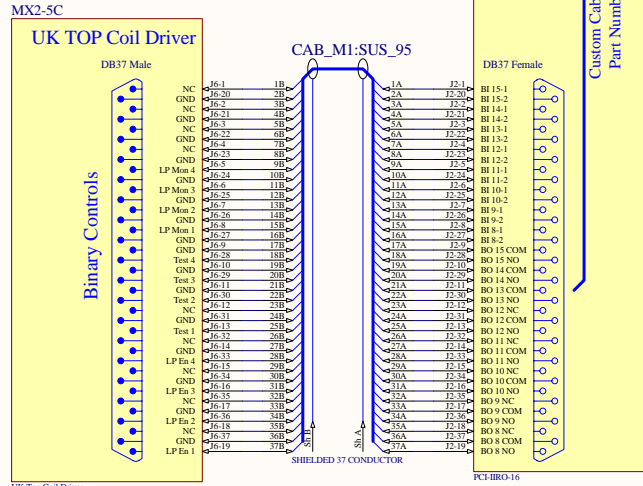
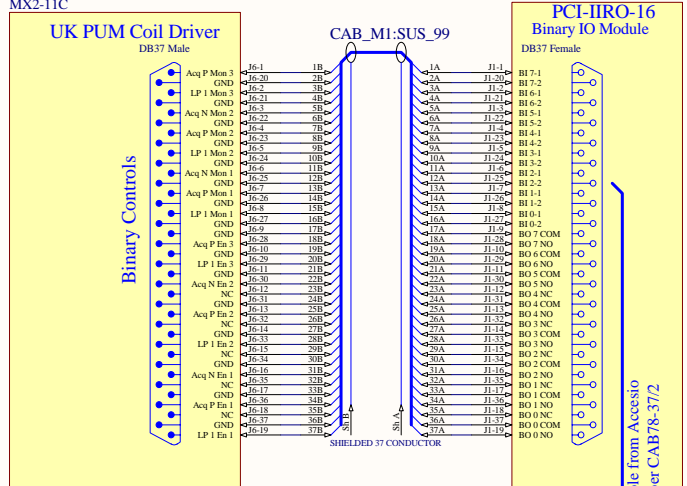
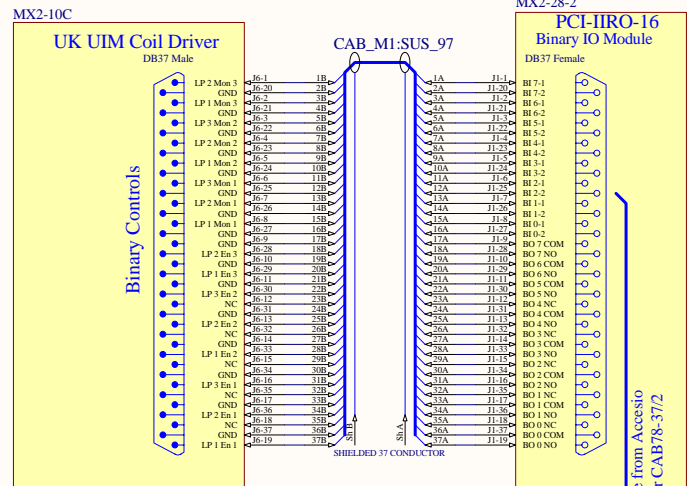
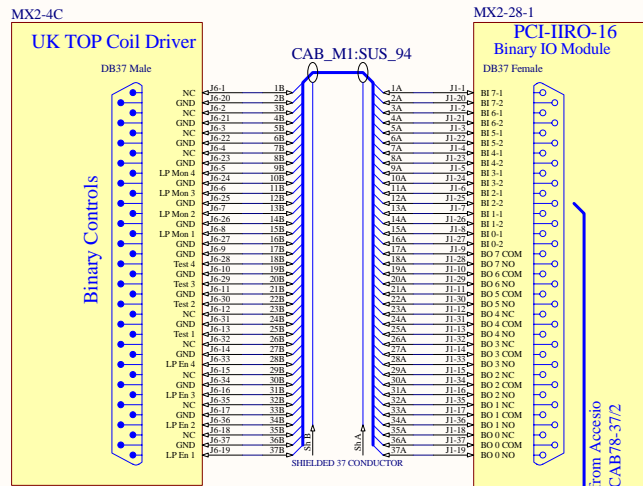
- CH0 - R0 FACE 3 Noise Mon
- CH1 - R0 FACE 3 Volt Mon
- CH2 - R0 FACE 3 Fast 1 Mon
- CH3 - R0 FACE 3 Slow 1 Mon
- CH4 - R0 LEFT Noise Mon
- CH5 - R0 LEFT Volt Mon
- CH6 - R0 LEFT Fast 1 Mon
- CH7 - R0 LEFT Slow 1 Mon
- CH8 - R0 RIGHT Noise Mon
- CH9 - R0 RIGHT Volt Mon
- CH10 - R0 RIGHT Fast 1 Mon
- CH11 - R0 RIGHT Slow 1 Mon
- CH12 - R0 SIDE Noise Mon
- CH13 - R0 SIDE Volt Mon
- CH14 - R0 SIDE Fast 1 Mon
- CH15 - R0 SIDE Slow 1 Mon
- CH16 - UIM UL Noise Mon
- CH17 - UIM UL Volt Mon
- CH18 - UIM UL Fast 1 Mon
- CH19 - UIM UL Slow 1 Mon
- CH20 - UIM LL Noise Mon
- CH21 - UIM LL Volt Mon
- CH22 - UIM LL Fast 1 Mon
- CH23 - UIM LL Slow 1 Mon
- CH24 - UIM UR Noise Mon
- CH25 - UIM UR Volt Mon
- CH26 - UIM UR Fast 1 Mon
- CH27 - UIM UR Slow 1 Mon
- CH28 - UIM LR Noise Mon
- CH29 - UIM LR Volt Mon
- CH30 - UIM LR Fast 1 Mon
- CH31 - UIM LR Slow 1 Mon

MX2-12B
UK PUM Coil Driver



CAB_M1:SUS_93





A

B

C

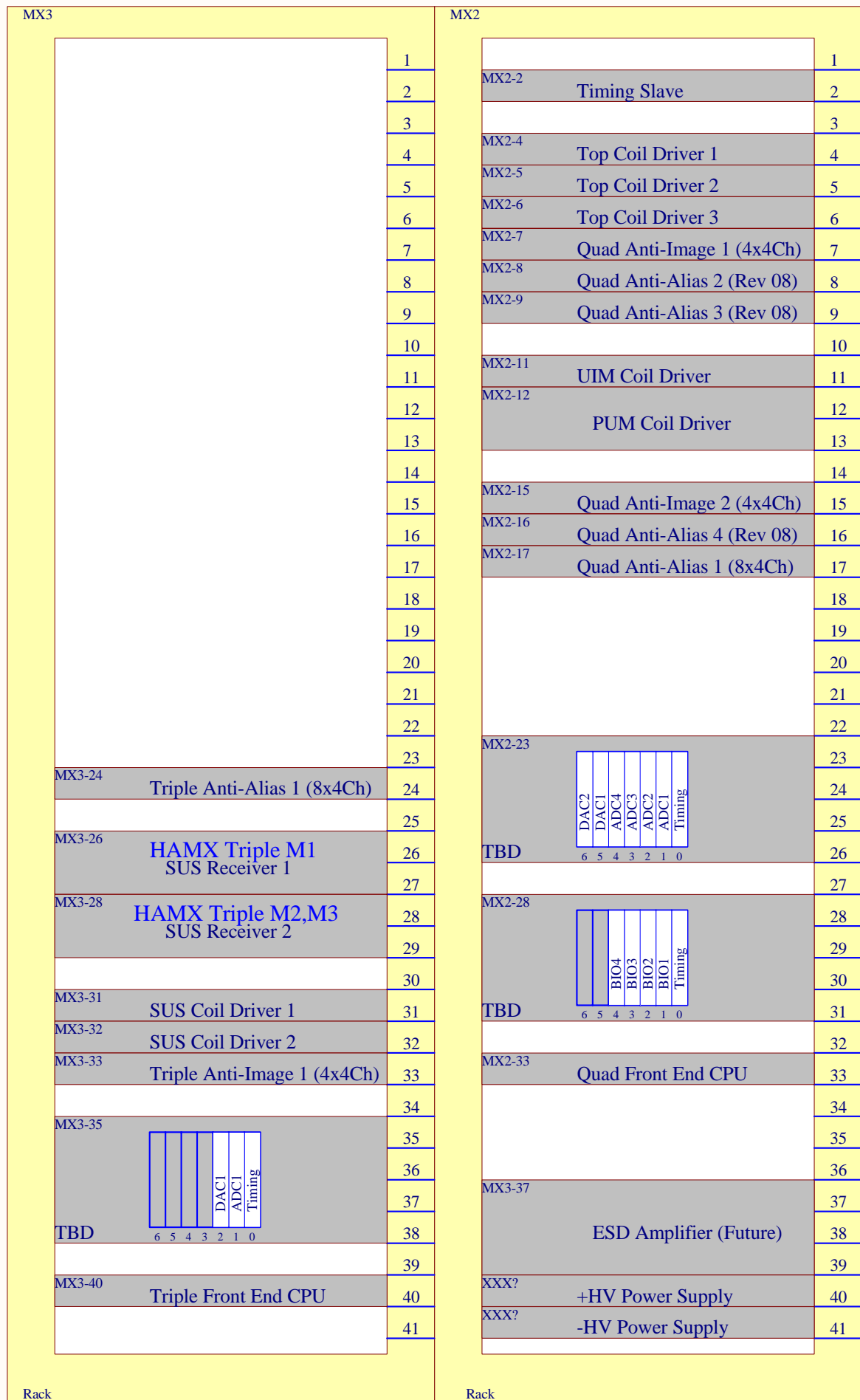
D

A

B

C

D



Title		LIGO Project California Institute of Technology Massachusetts Institute of Technology		LIGO	
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