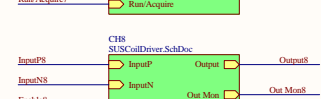
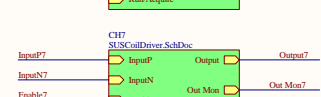
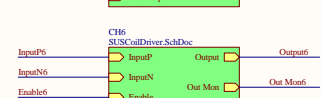
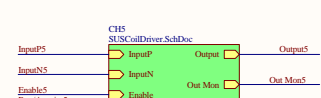
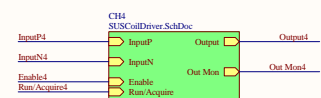
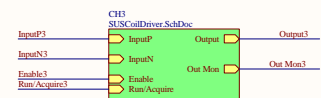
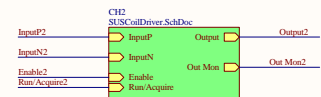
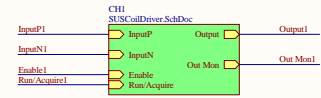
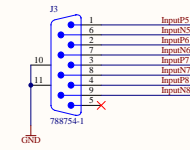
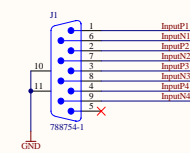
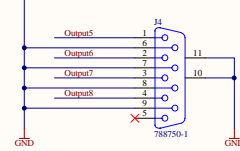
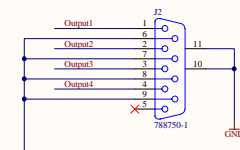


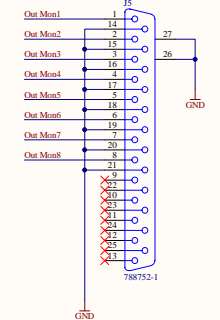
Coil Driver Inputs Front Panel



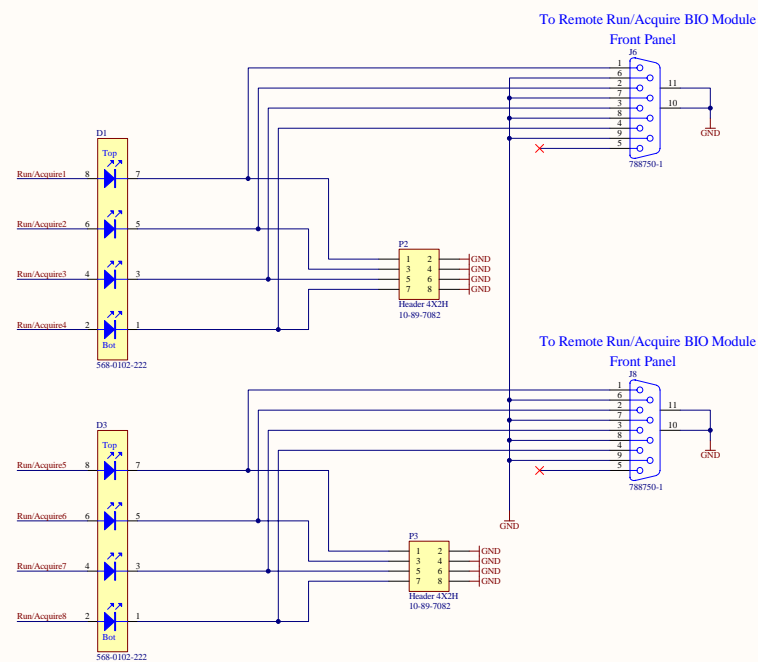
Coil Driver Outputs Rear Panel



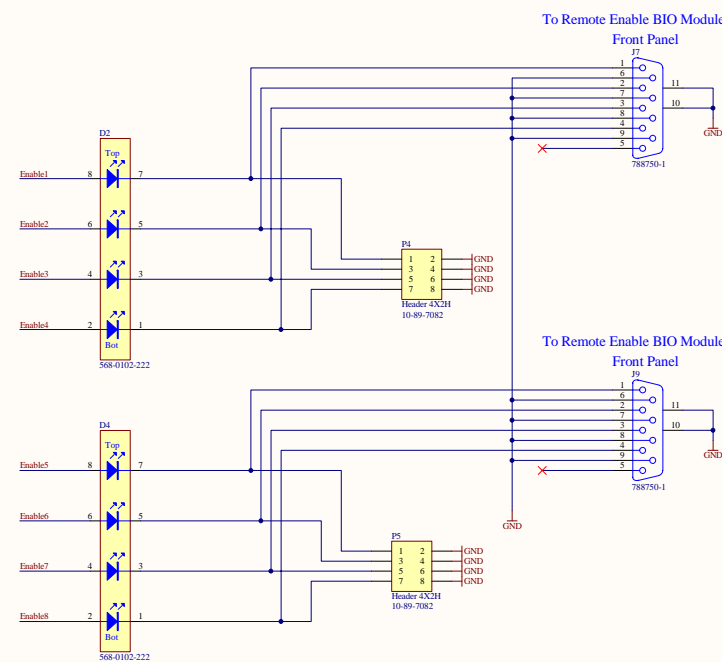
Output Monitors Rear Panel



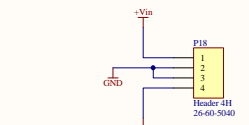
Run/Acquire Controls and Indicators



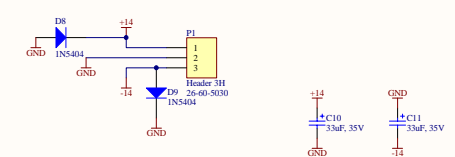
Coil Driver Enable Controls and Indicators



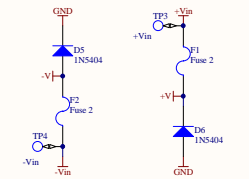
Raw DC Power Input



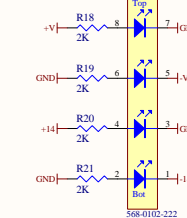
Regulated DC Power Input

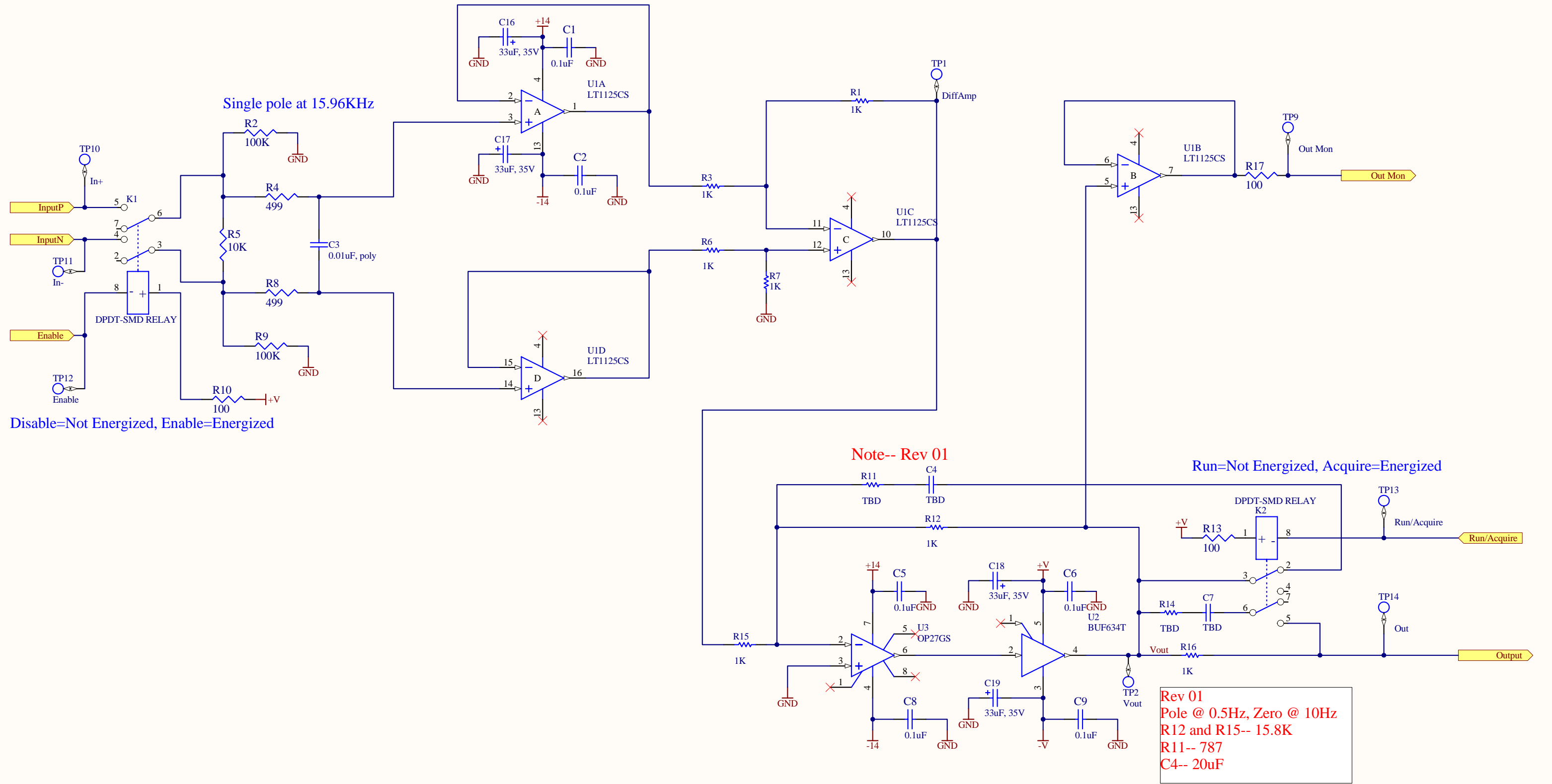


Fused BUF634 Power



Power Regulation





Title 8 Channel SUS Coil Driver			LIGO Project California Institute of Technology Massachusetts Institute of Technology		LIGO
Size: B	DCC Number: D060278	SCH / PCB Revision: 01	Engineer: J. Heefner	Date: 1/17/2008	
File: C:\Documents and Settings\Jay\My Documents\My Designs\SUSCoilDriver\SUSCoilDriver.SchDoc					Time: 10:09:08 AM
					Sheet 2 of 2